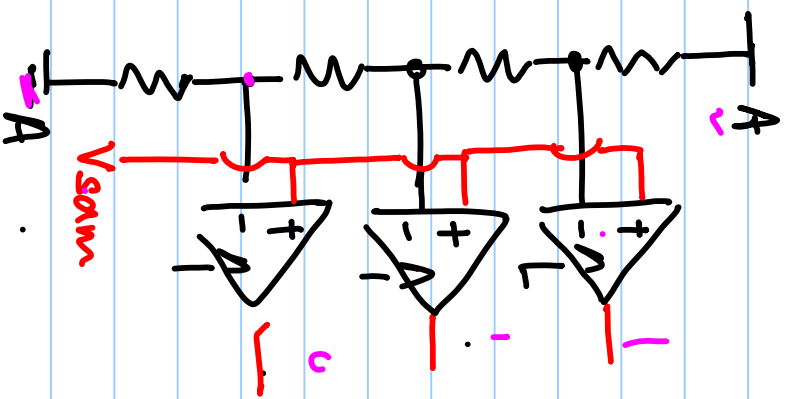
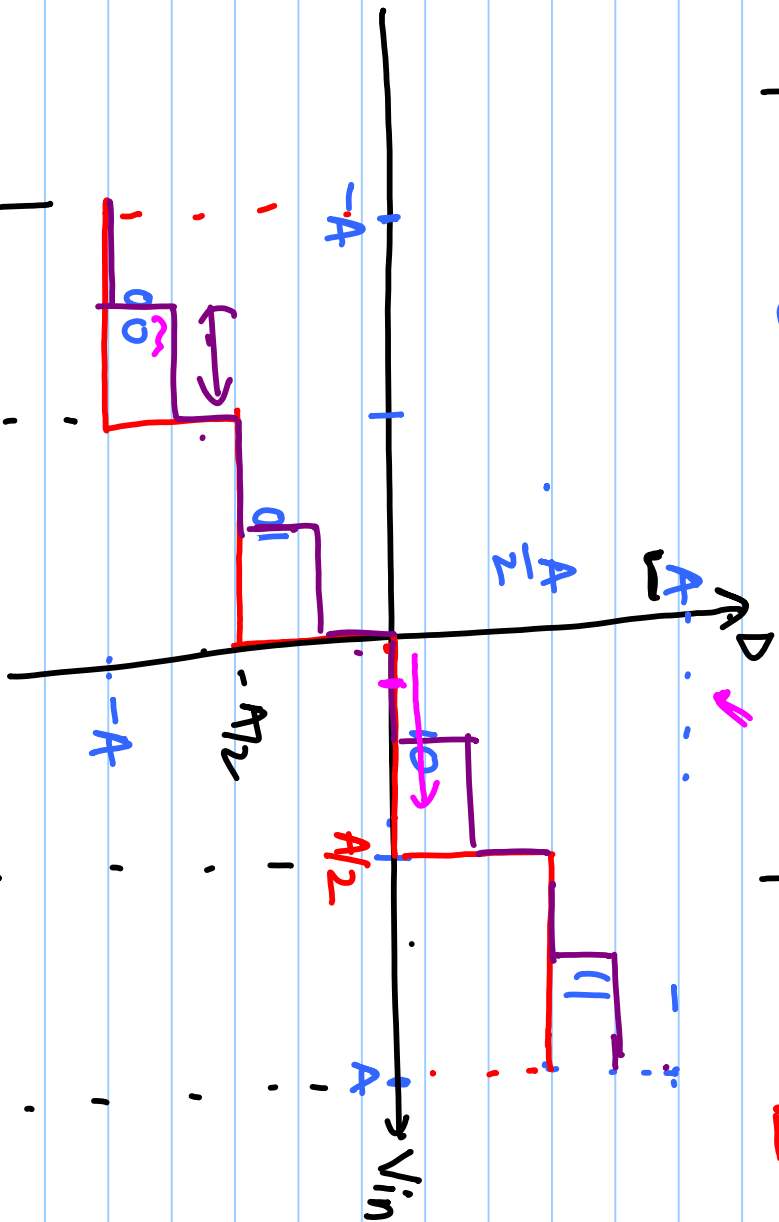
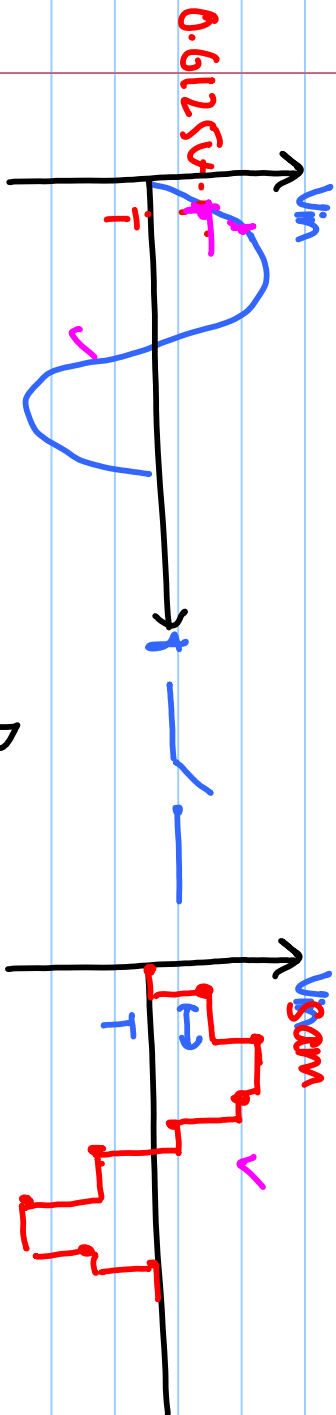
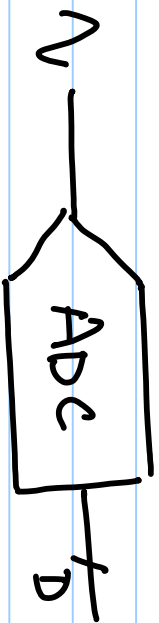
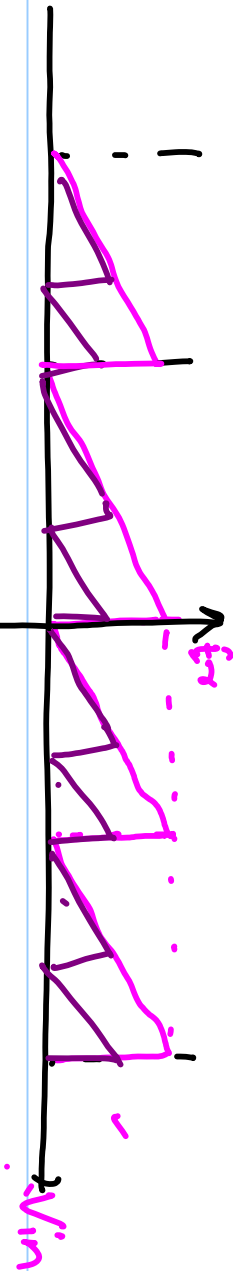


Lecture # 37

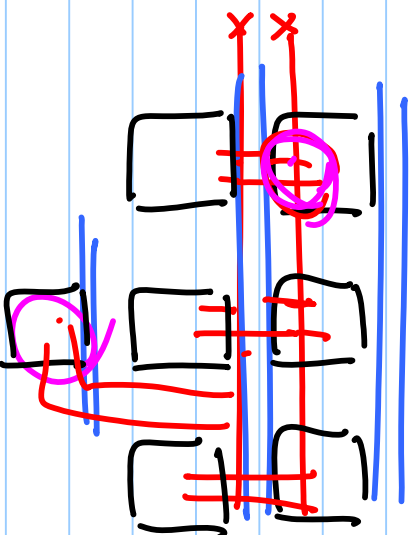


FLASH ADC

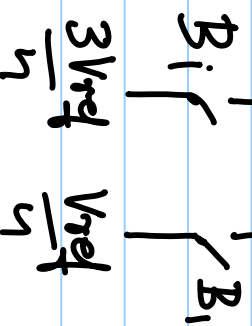
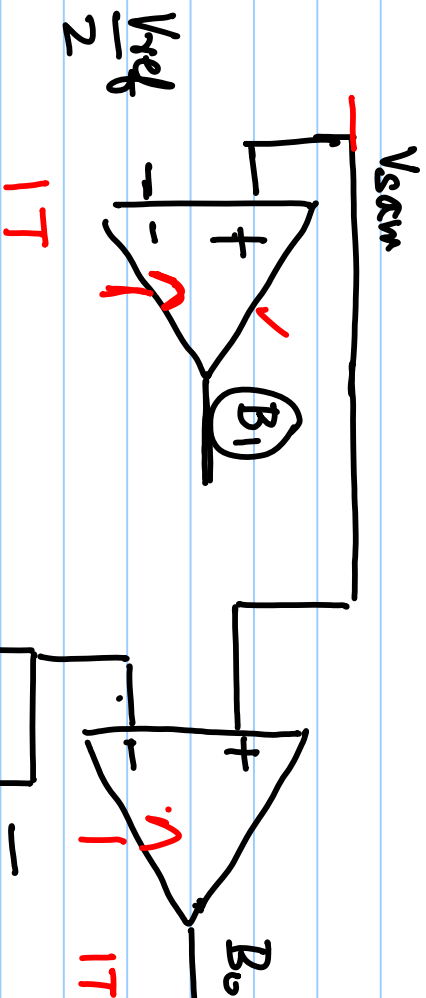
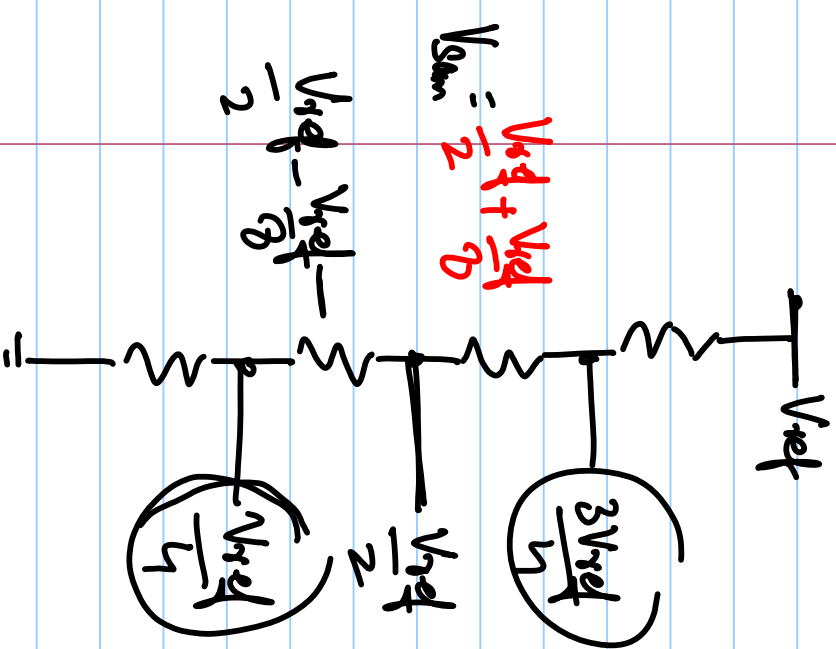
Error = Input - Output

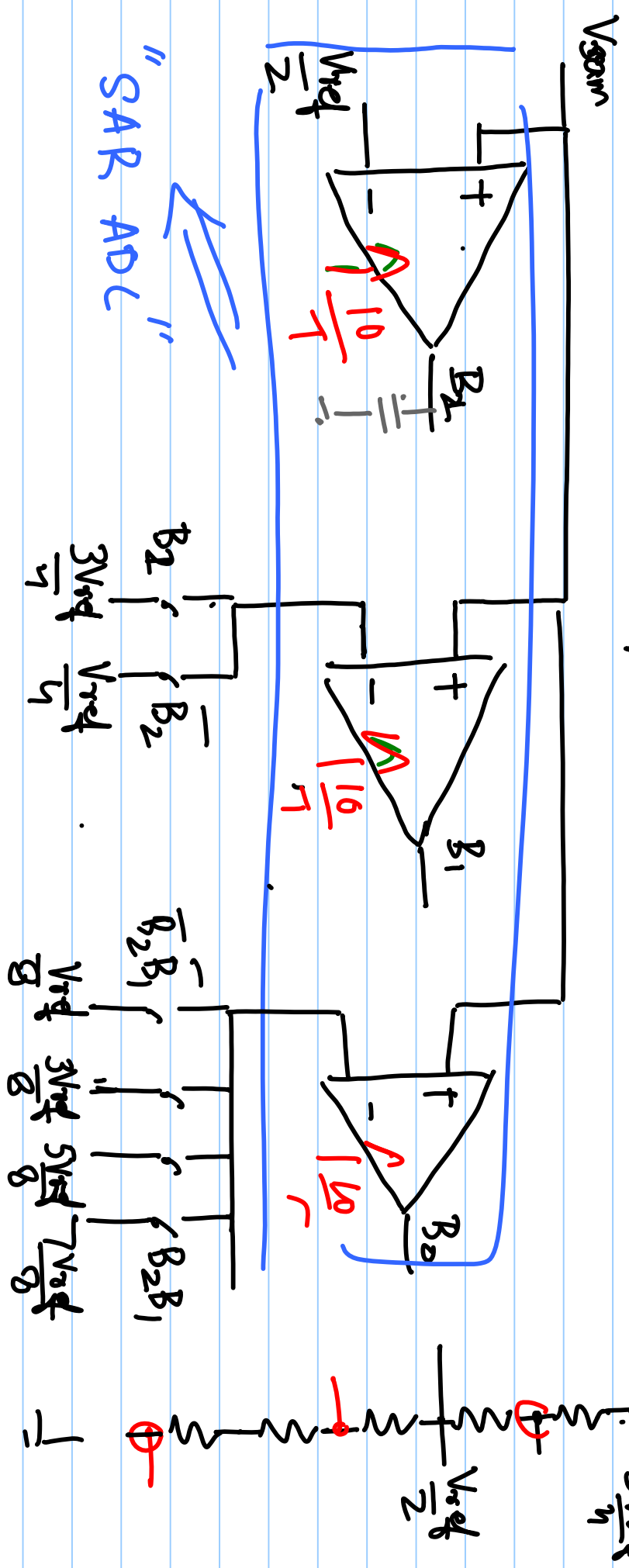
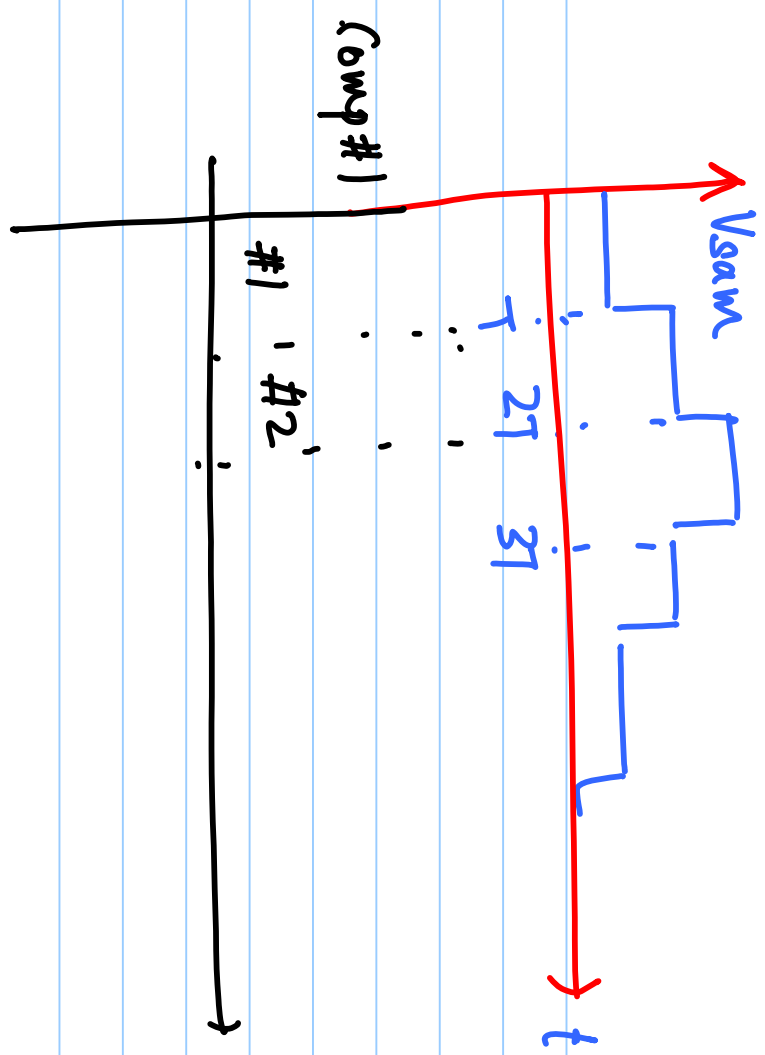


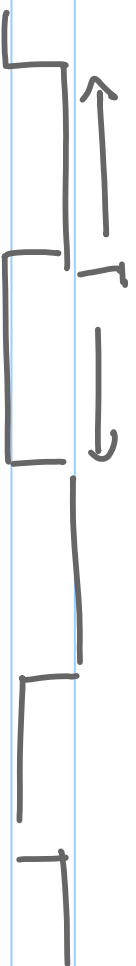
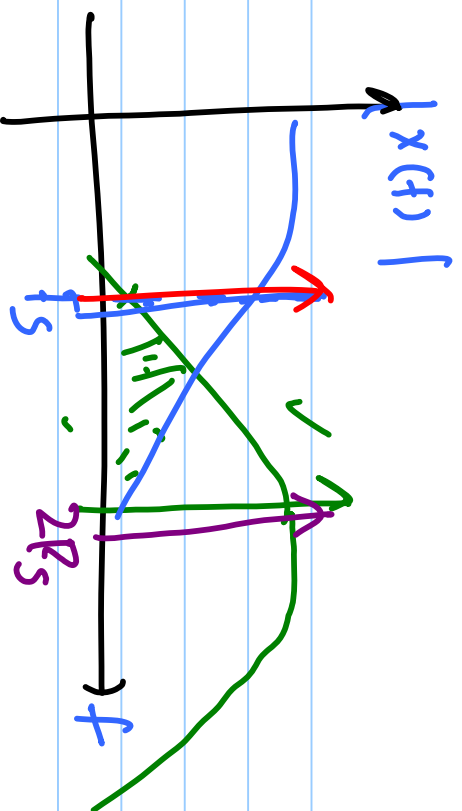
N-bit ADC requires $2^N - 1$ comparators



$$V_{sam} \geq \frac{V_{ref}}{2} \Rightarrow 1$$







Charge Discharge

