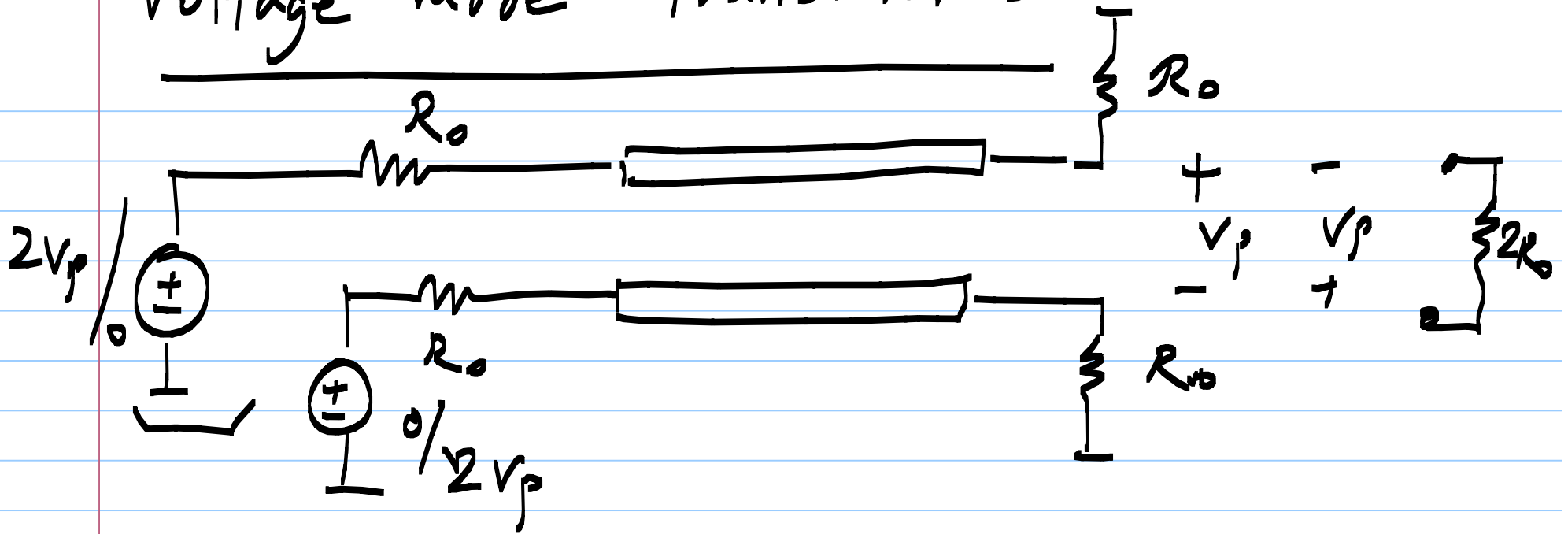


Current mode transmitters

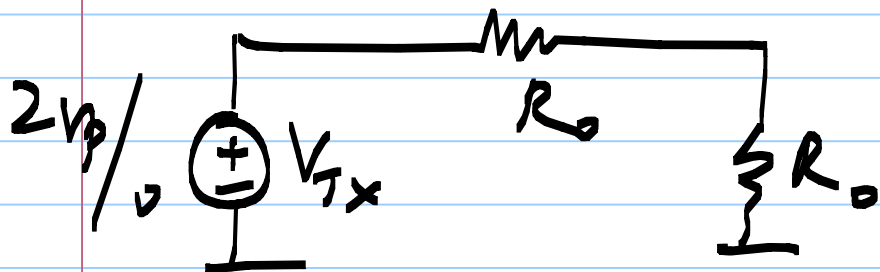
- Diff pairs in parallel - ^{tap values by} ^{More flexible} size/currents
- Separate diff pairs for each tap,
or form a single DAC (eg: digital)
- pMOS + nMOS \Rightarrow higher efficiency, but
higher parasitics of CMOS
- Many taps : ^{large} \wedge Common mode current \Rightarrow
pMOS sources to raise the CM level

Fixed equalizer

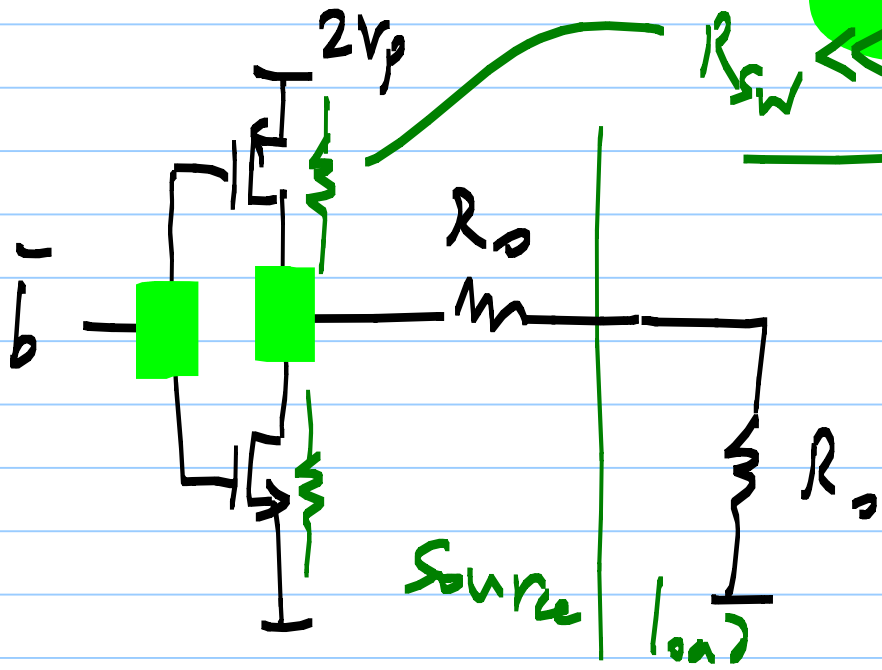
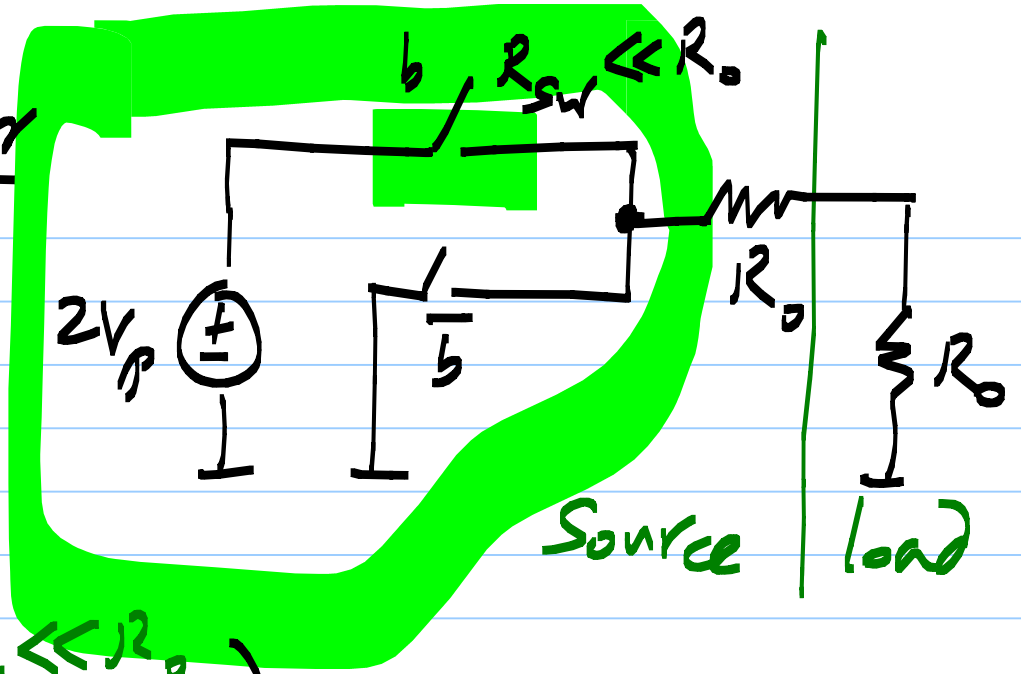
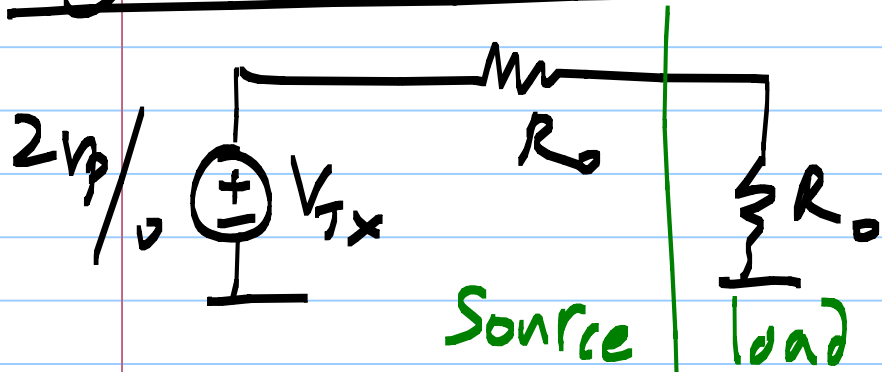
Voltage mode transmitters



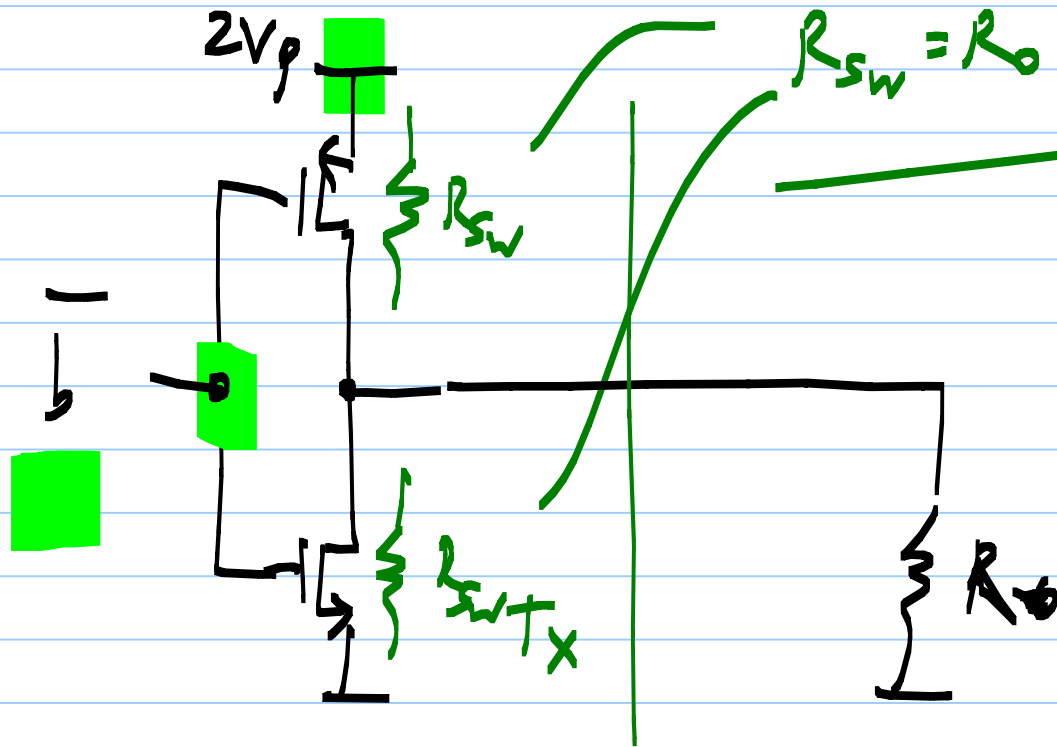
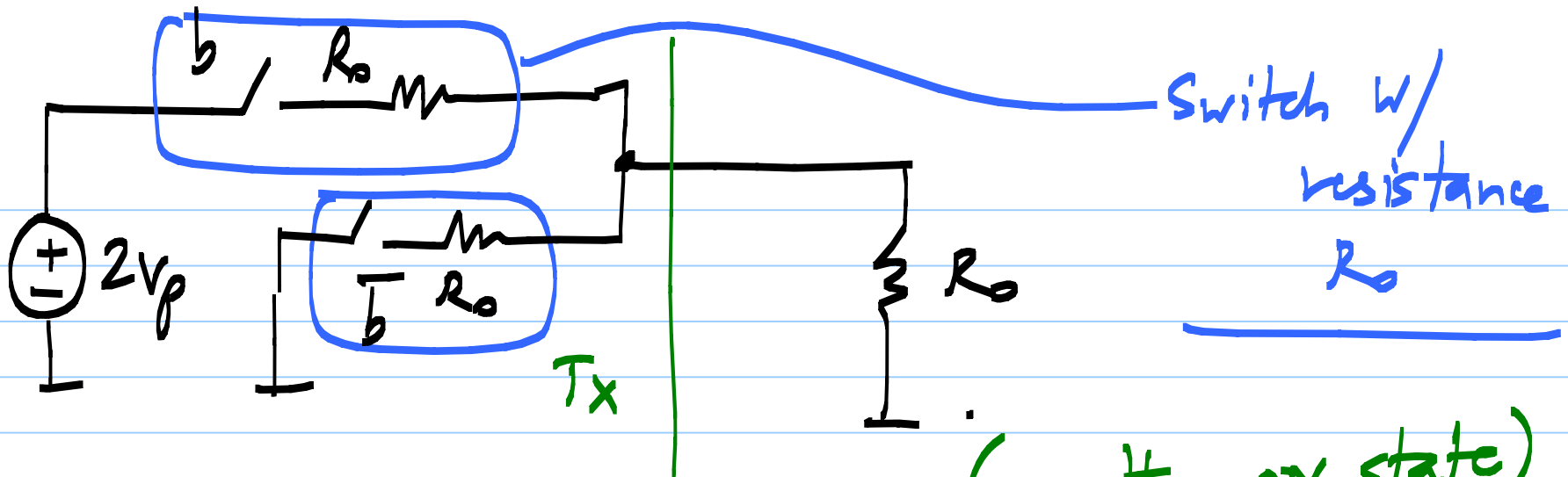
Single-ended transmitter



Single-ended transmitter



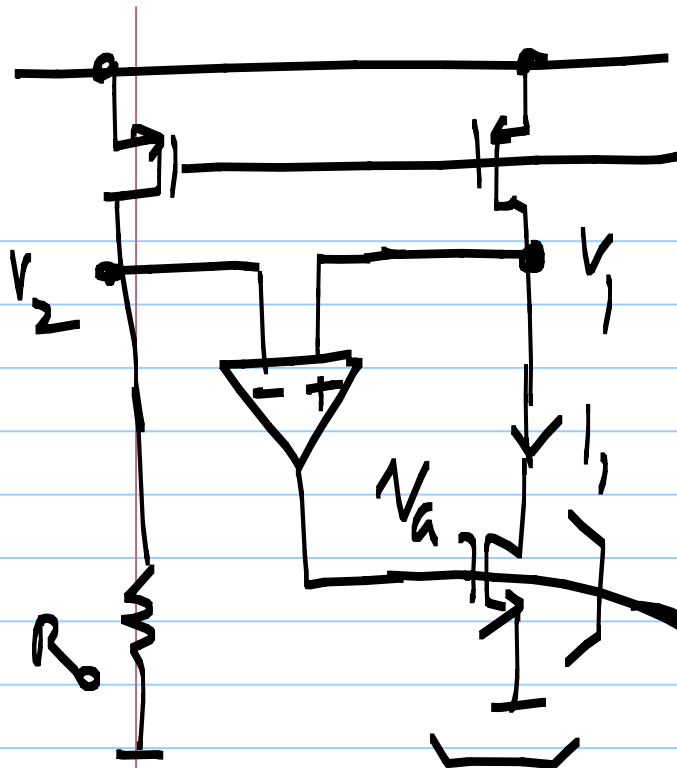
- very wide transistors
- large parasitics
- increased switching power



$R_{sw} = R_0$ (in the ON state)

PVT variations
 MOS: T, P, ($2V_p, V_h$ in ON state)

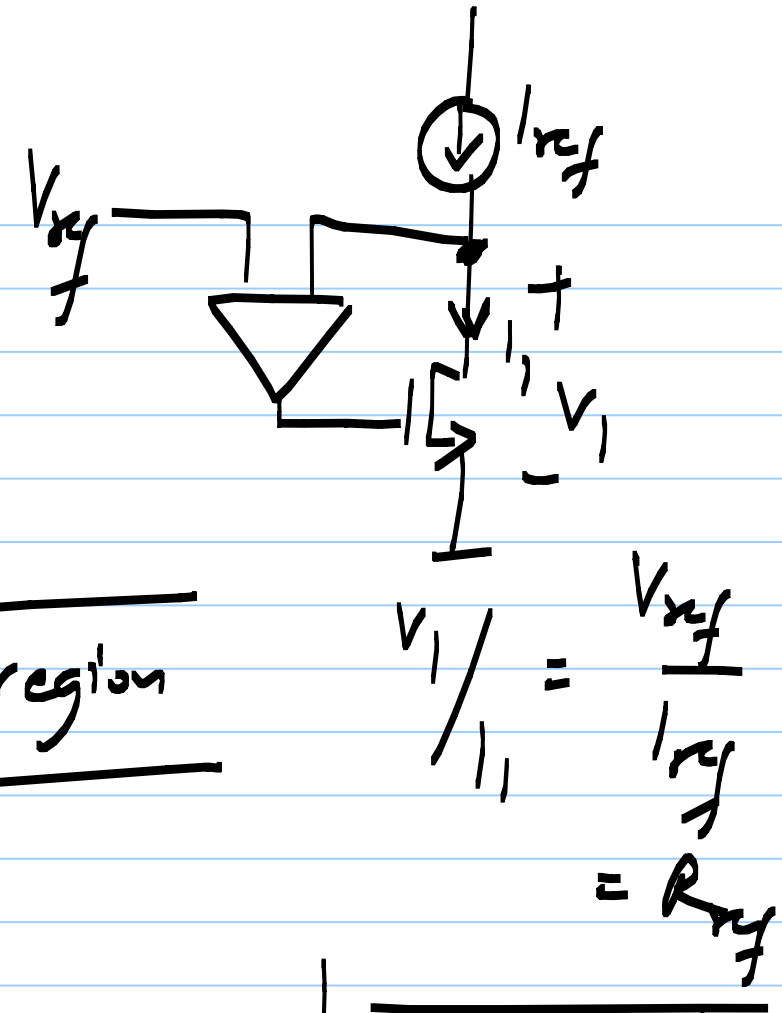
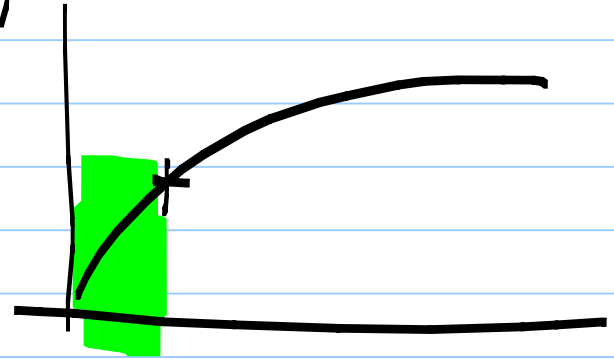
Control R_{sw} using a negative fb loop.



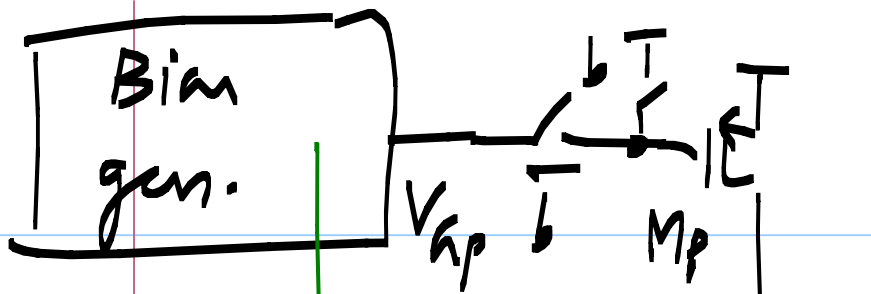
$$\frac{V_1}{I_1} = R_0$$

Triode region

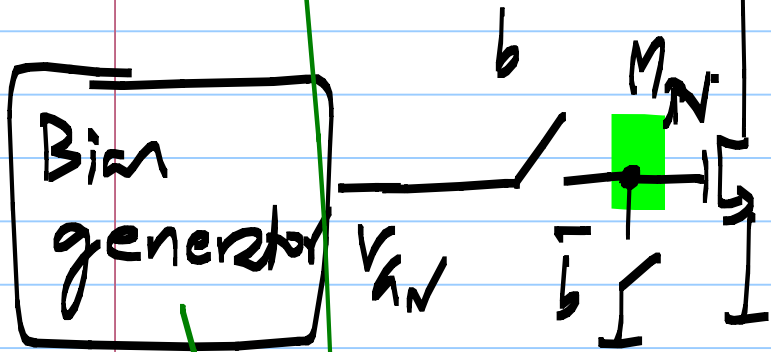
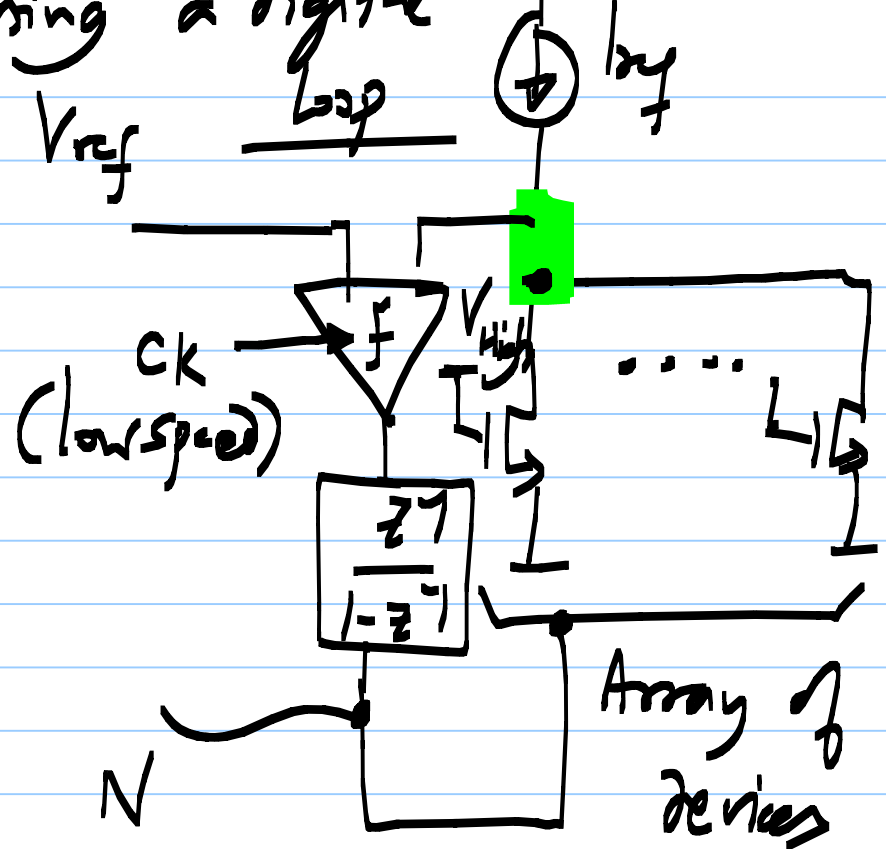
Need $R_{sw} = R_0$



$$\frac{V_1}{I_1} = \frac{V_{ref}}{I_{ref}} = R_{ref}$$



devices turned on controlled using a digital loop



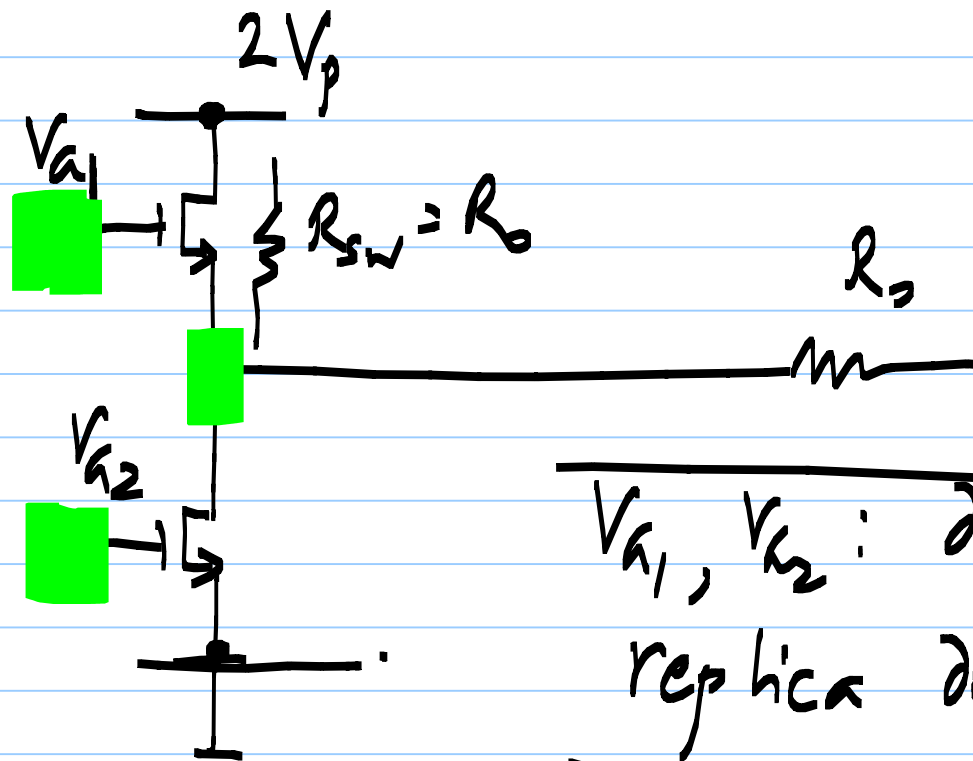
$M_p, M_n: R_{sw} = R_0$

Feedback to ensure $R_{sw} = R_0$

devices that are turned on

$$V_{a1} > 2V_p + V_T$$

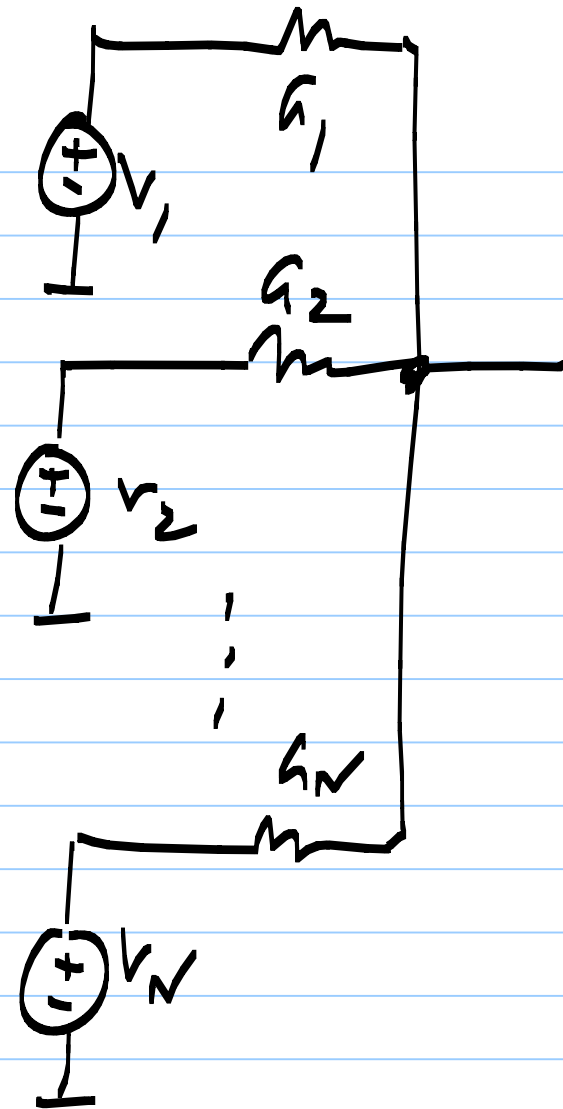
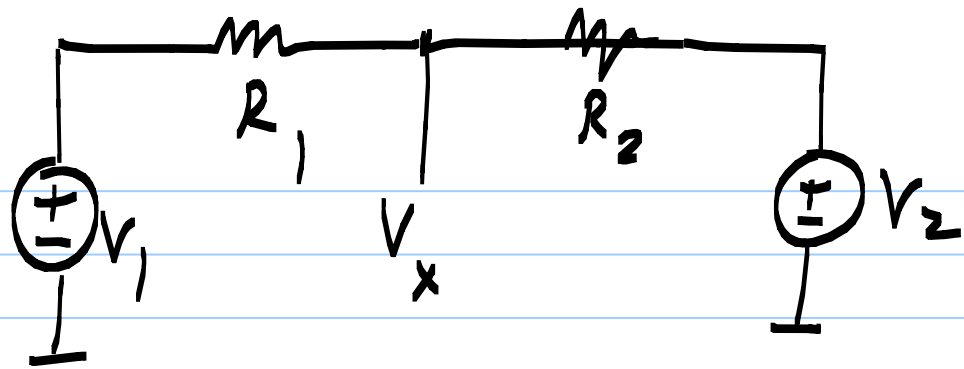
$$2V_p < V_{DD}$$



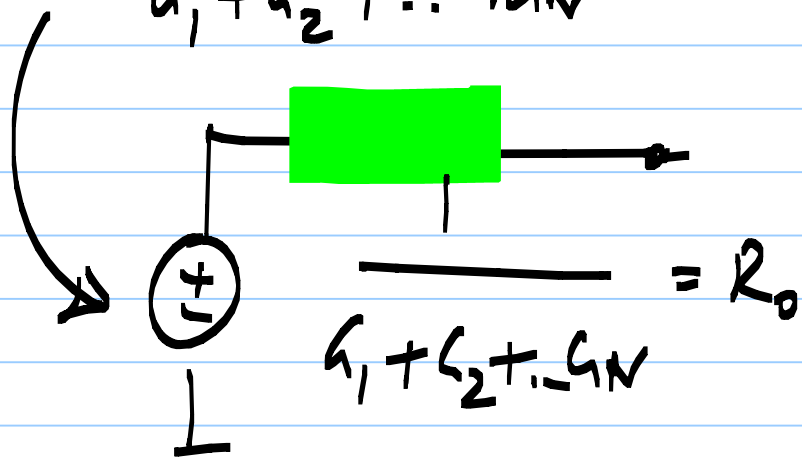
V_{a1}, V_{k2} : derive using
replica devices in feedback

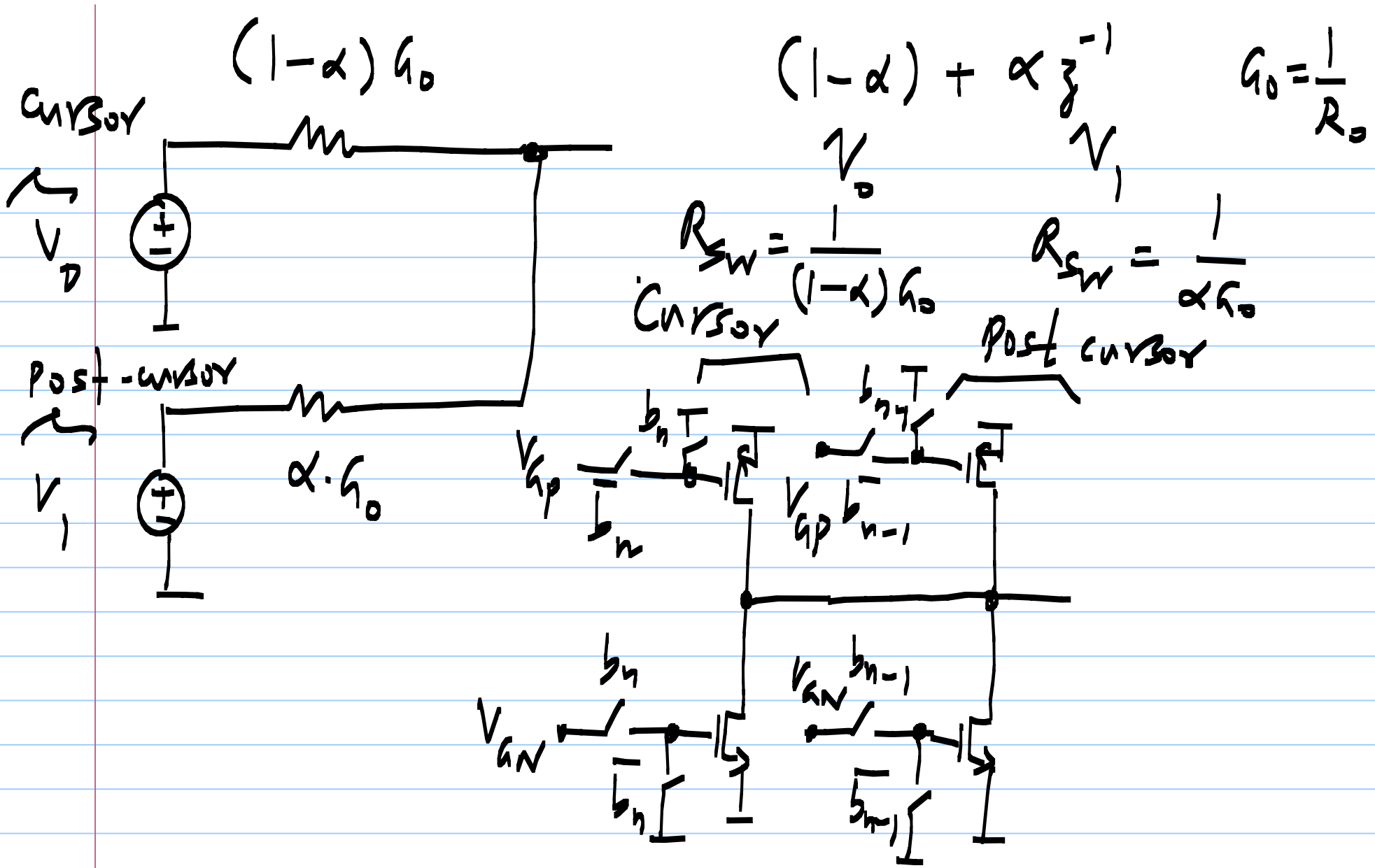
N-over-N driver; works when $2V_p < V_{DD}$

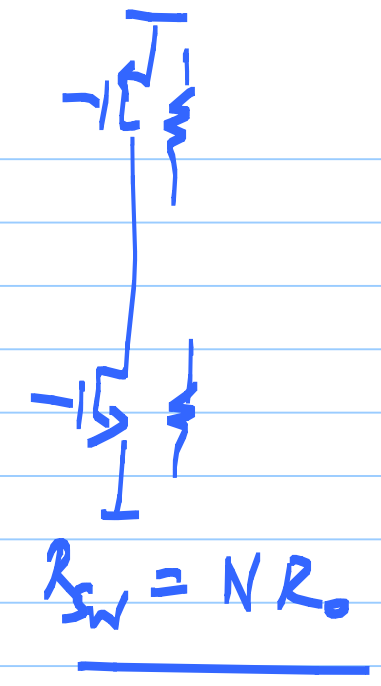
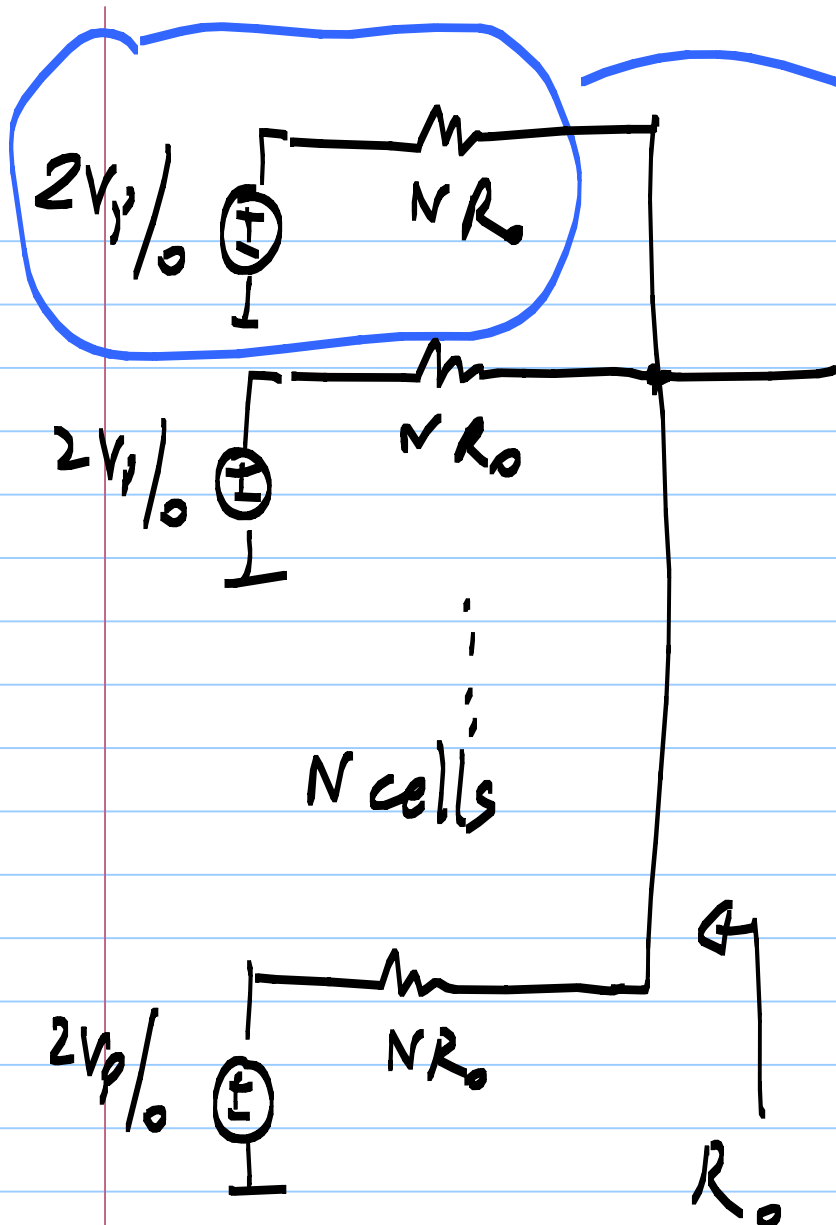
Smaller nMOS than pMOS for the same R_{sw}



$$\frac{V_1 G_1 + V_2 G_2 + \dots + V_N G_N}{G_1 + G_2 + \dots + G_N}$$







$(N+1)$ level DAC

Equalization in the 2^N domain

Voltage mode transmitters:

- More efficient than current-mode Tx
- Switch resistance = desired termination
 - ↳ Replica in feedback
 - Analog (control V_A)
 - Digital (control # transistors)
- P-over-N or N-over-N (smaller swings, smaller devices)
- Equalization — Multiple parallel units (taps) or (DAC)