

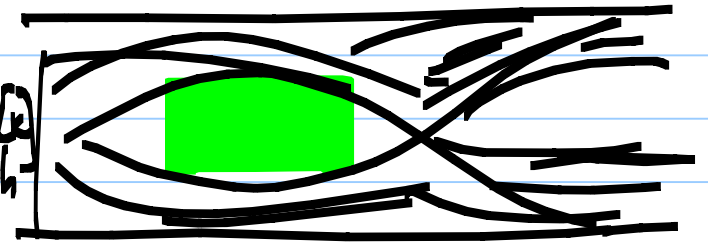
$\{1\}$

$h_{ch}[0] + h_{ch}[k]$  Sampled pulse response

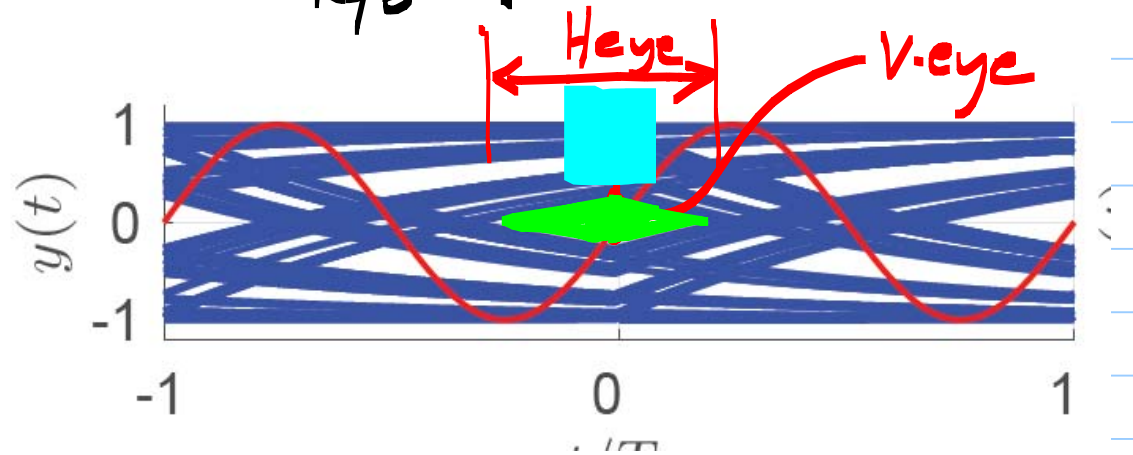
$\{a_k\}$

$h_{ch}[0]$

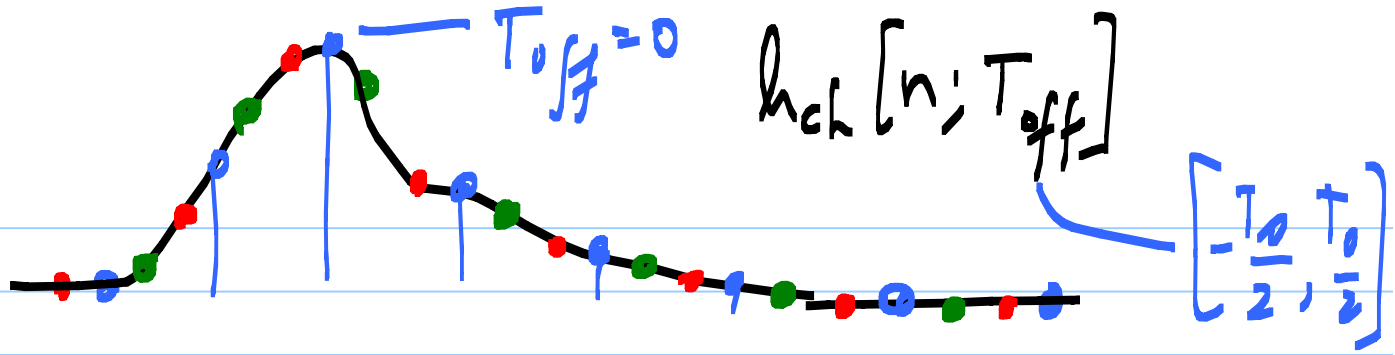
$h_{ch}[0] - \sum_{k \neq 0} h_{ch}[k]$



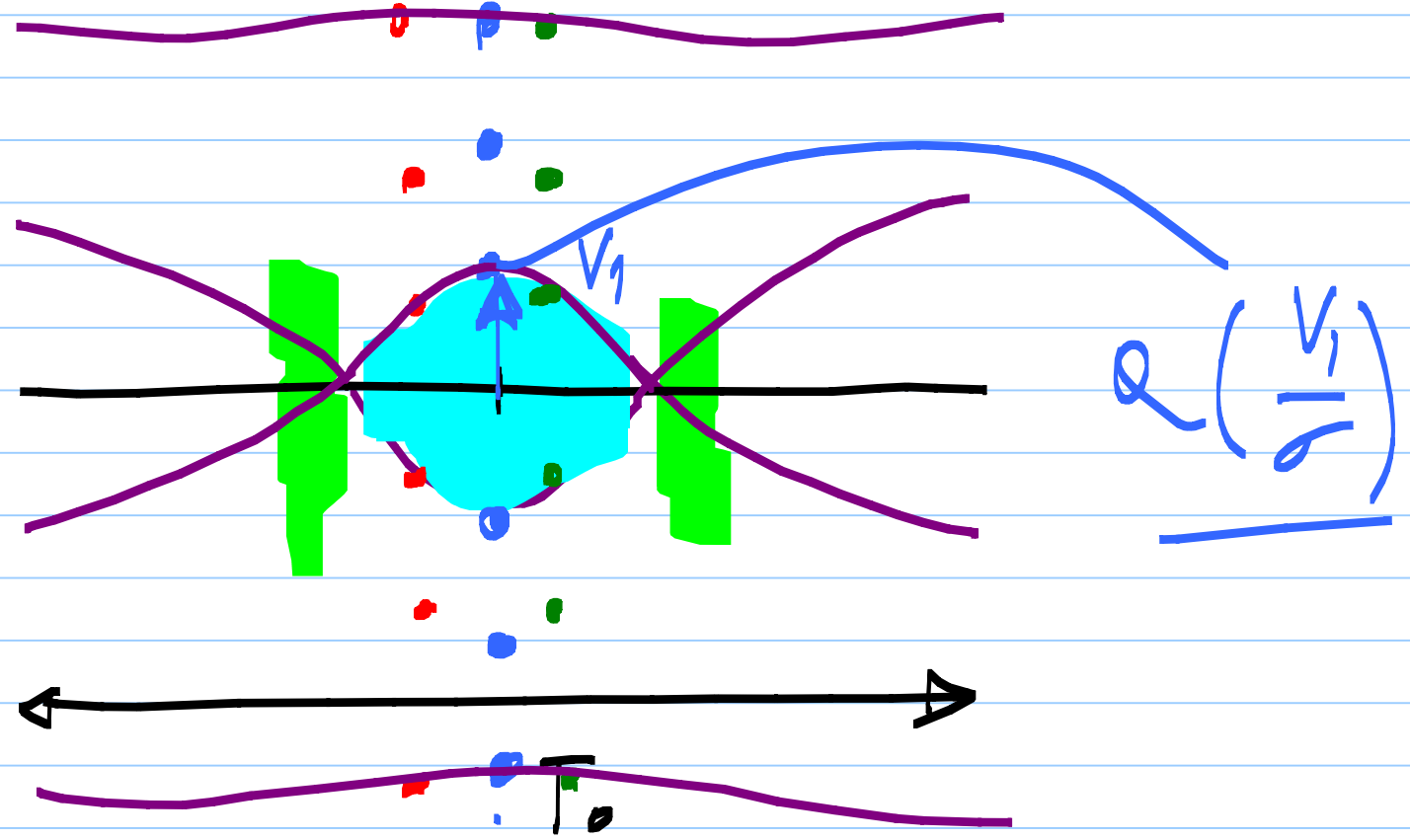
Random data



{1}



$2^2 - 1$



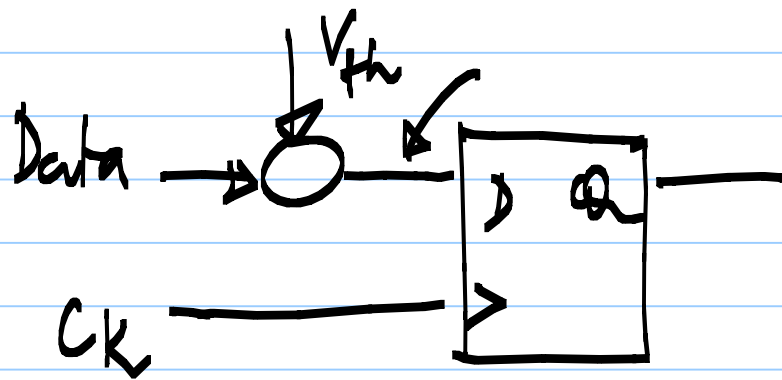
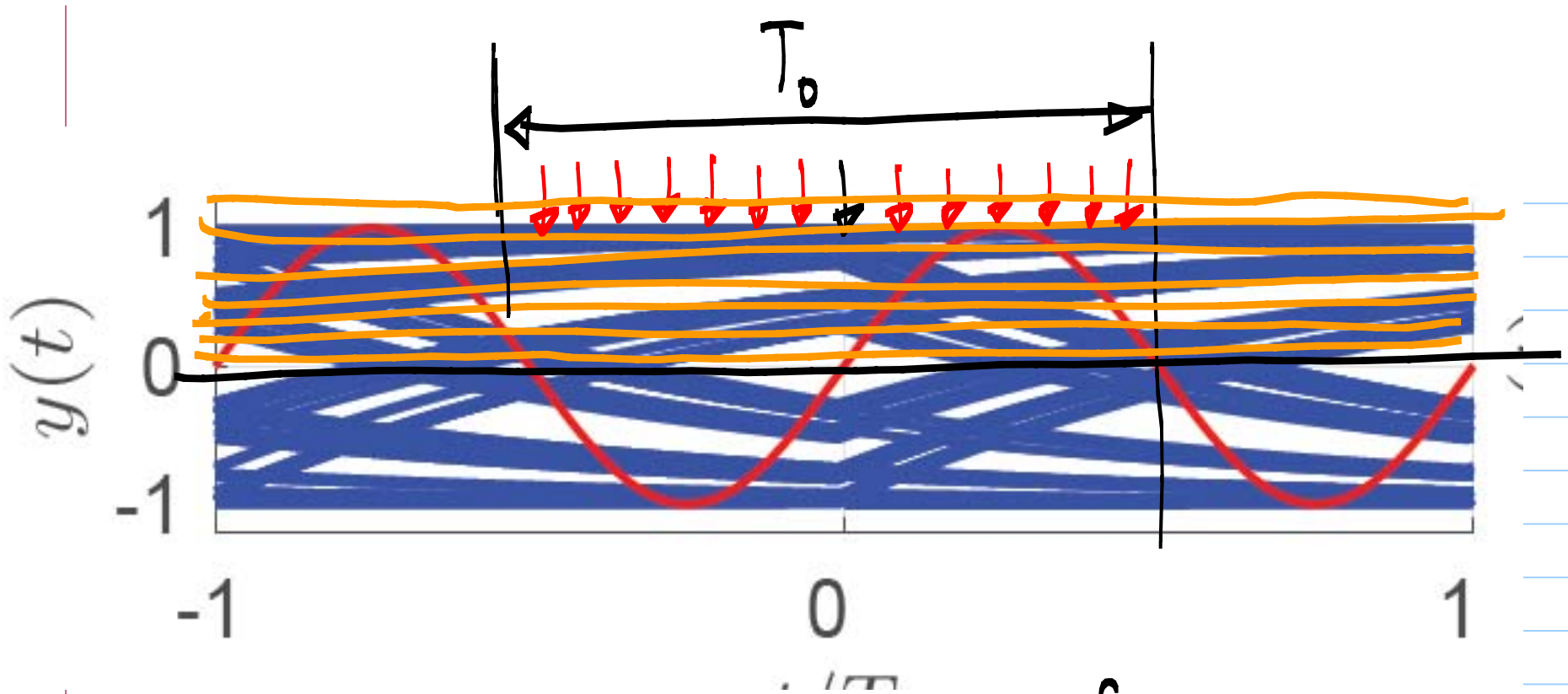
Horizontal eye opening: clock timing jitter

Vertical eye opening: sampling SNR

Statistical eye:  $h_{ch}[n; T_{SR}]$   $\frac{1}{4} \left[ Q\left(\frac{0.25}{\sigma_n}\right) \right.$

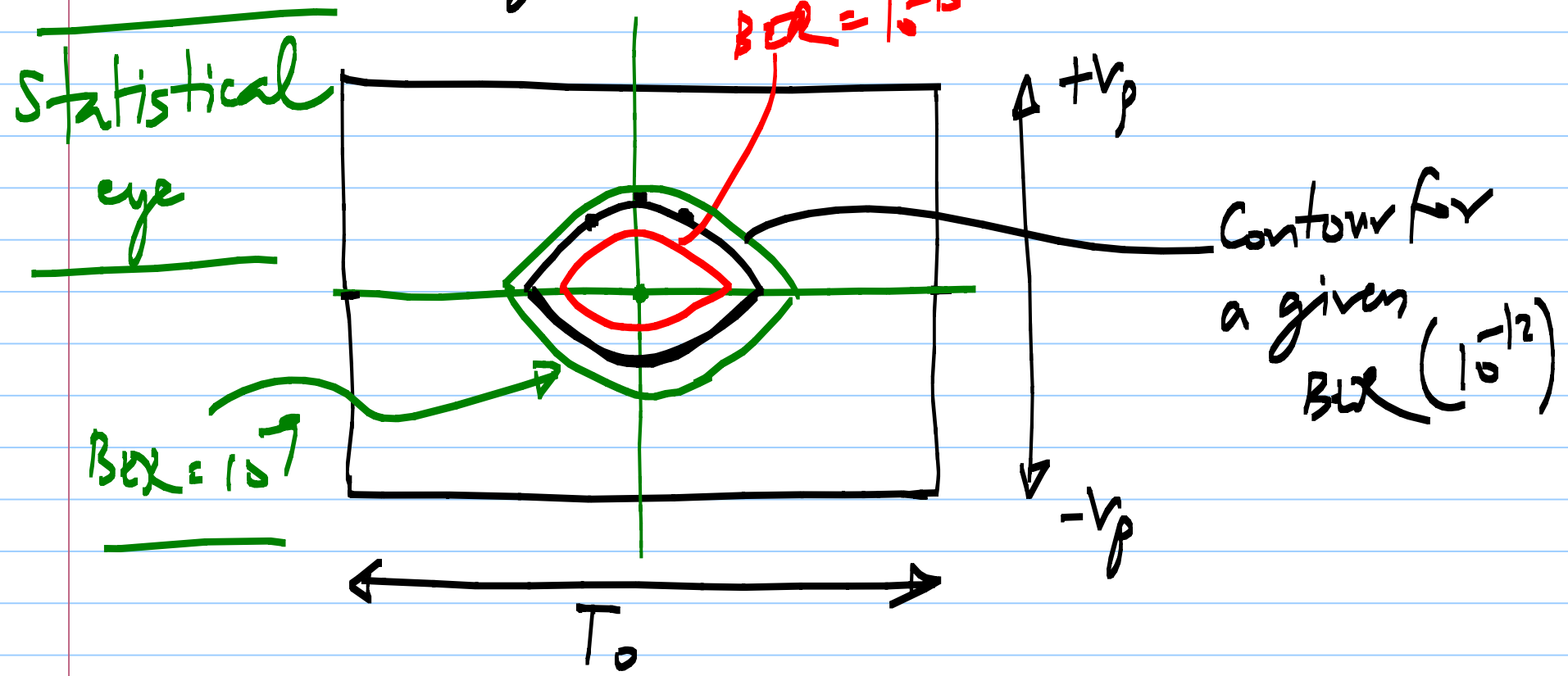
$h_{ch}[n] = \{ 0.25, 0.75, 0.25 \}$   $\xrightarrow{\text{BER}}$   $+ Q\left(\frac{0.75}{\sigma_n}\right)$

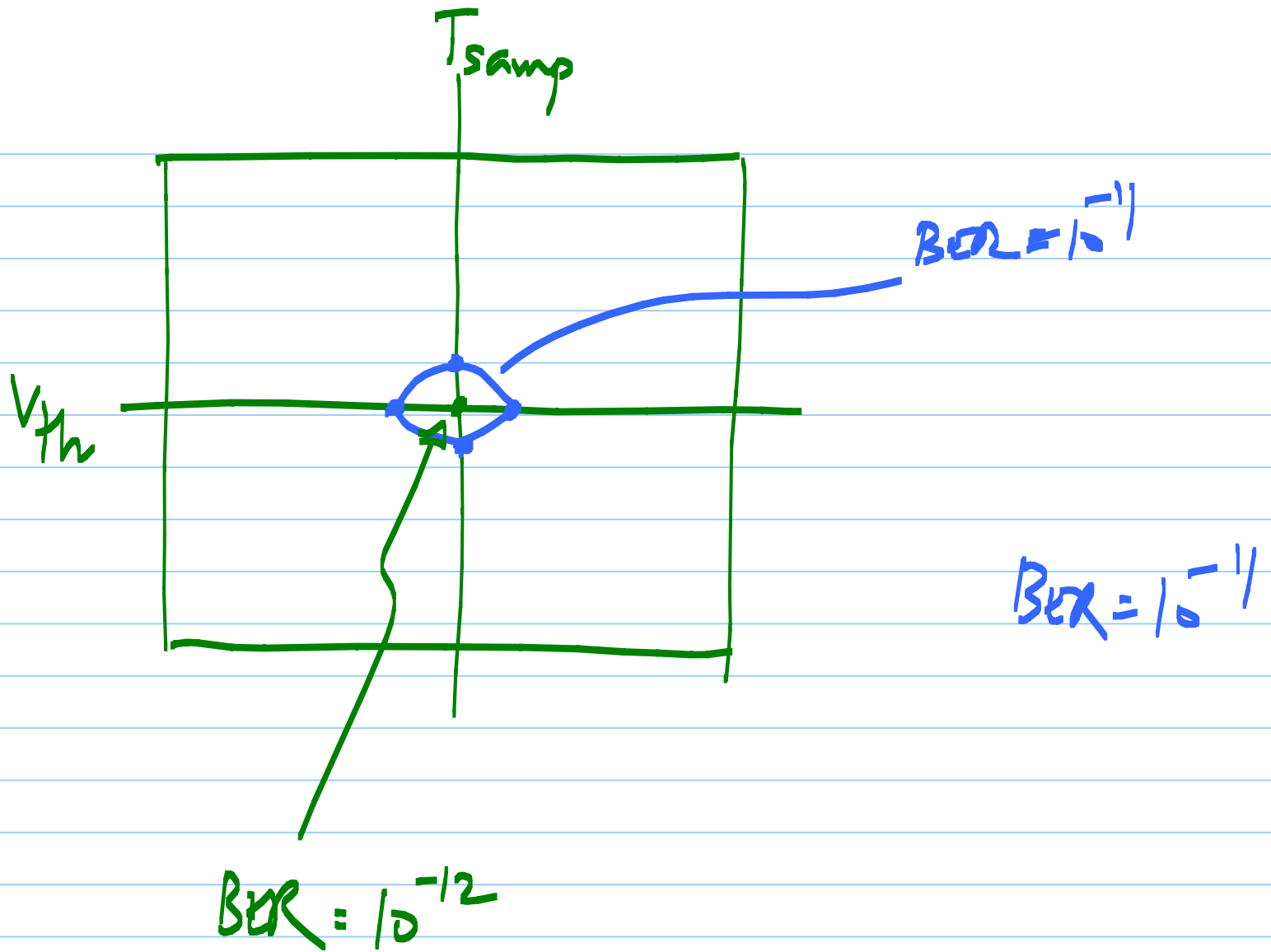
<u><math>2^N - 1</math> bit patterns</u>	-1	-1	0.25	+ Q	$\left(\frac{0.75}{\sigma_n}\right)$
	-1	+1	0.75		
	+1	-1	0.75	+ Q	$\left(\frac{1.25}{\sigma_n}\right)$
	+1	+1	1.25		

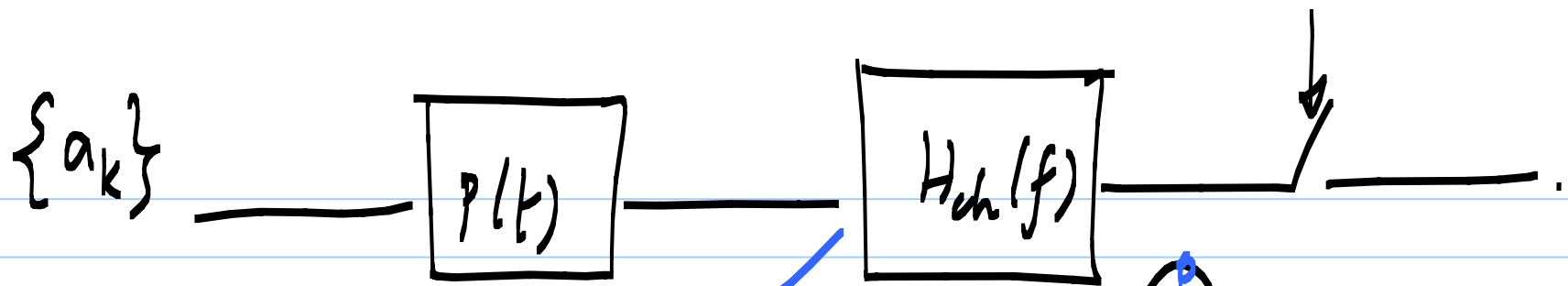


$$\frac{1}{2} \left[ Q \left[ \frac{V_i + V_{Th}}{\sigma_n} \right] + Q \left[ \frac{-V_i + V_{Th}}{\sigma_n} \right] \right]$$

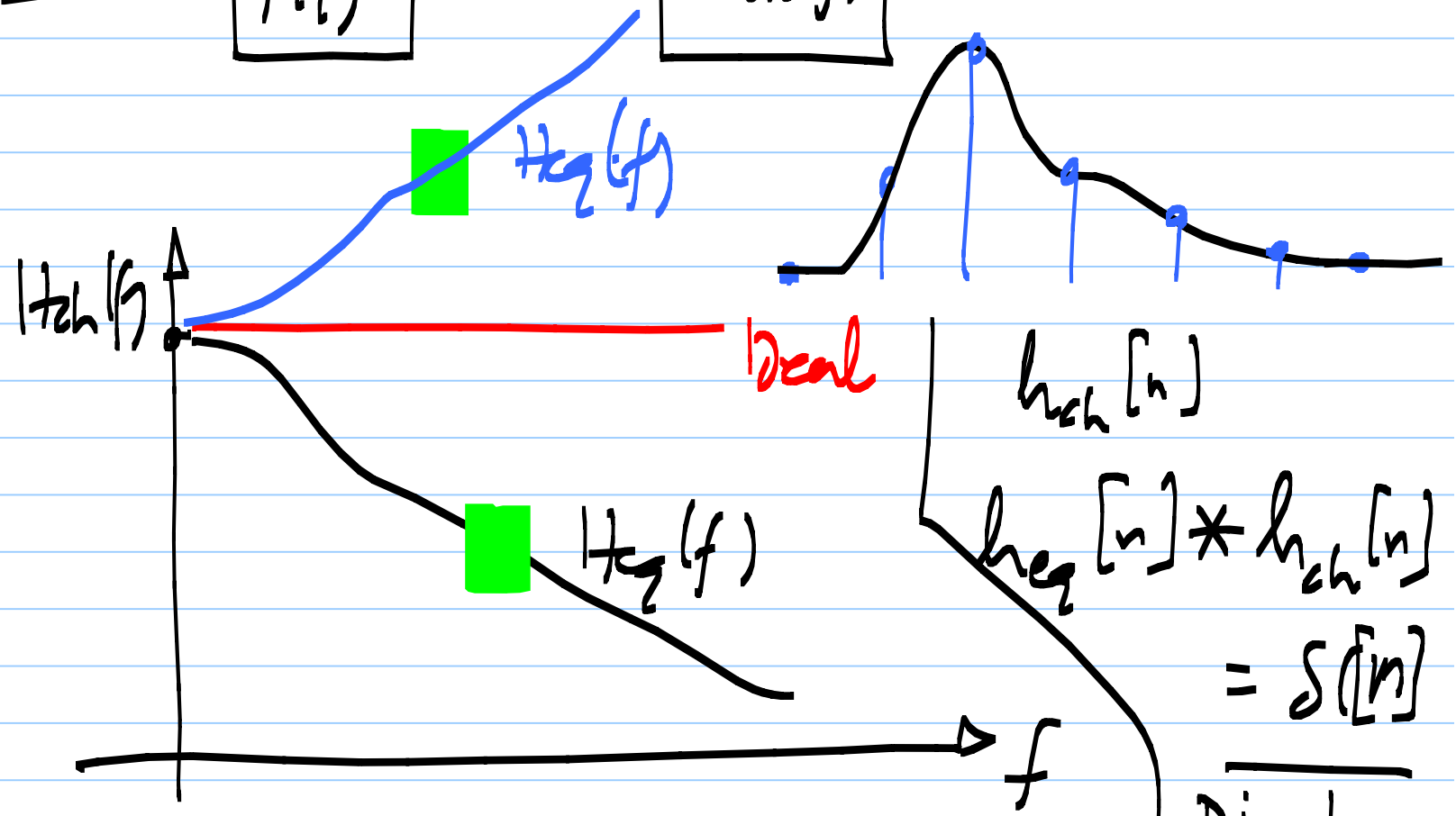
For every combination of timing offset and threshold voltage, there is a BER value







$\{1\}$



$$h_{eq}[n] * h_{ch}[n] = \delta[n]$$

Discrete domain time

**system**

