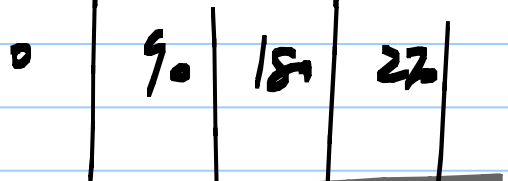
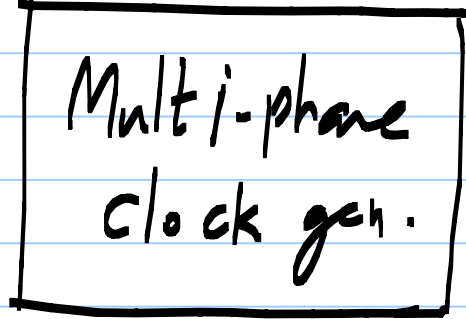
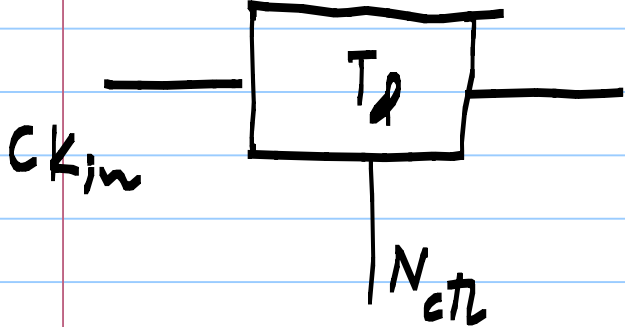
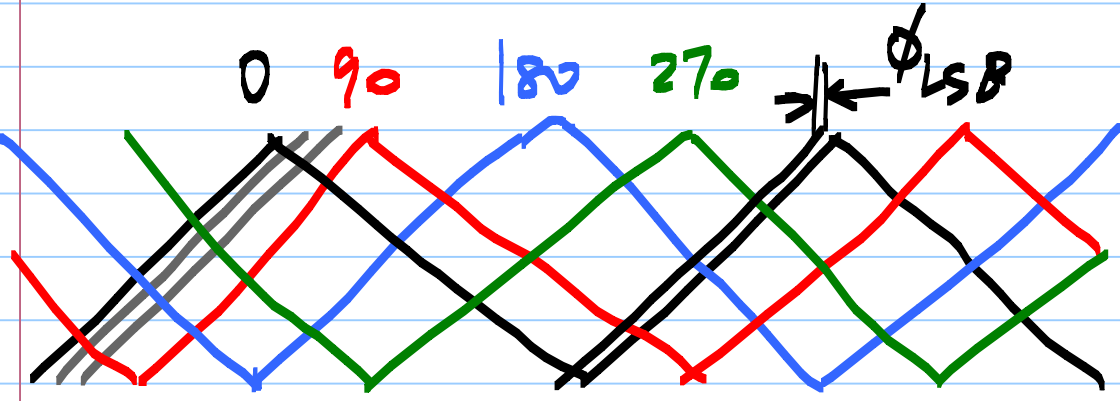
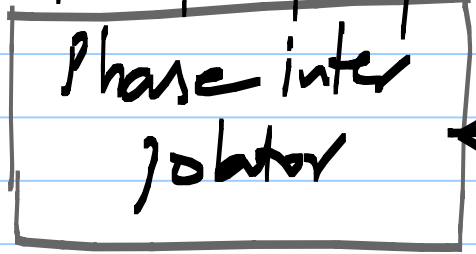


Digital CDR

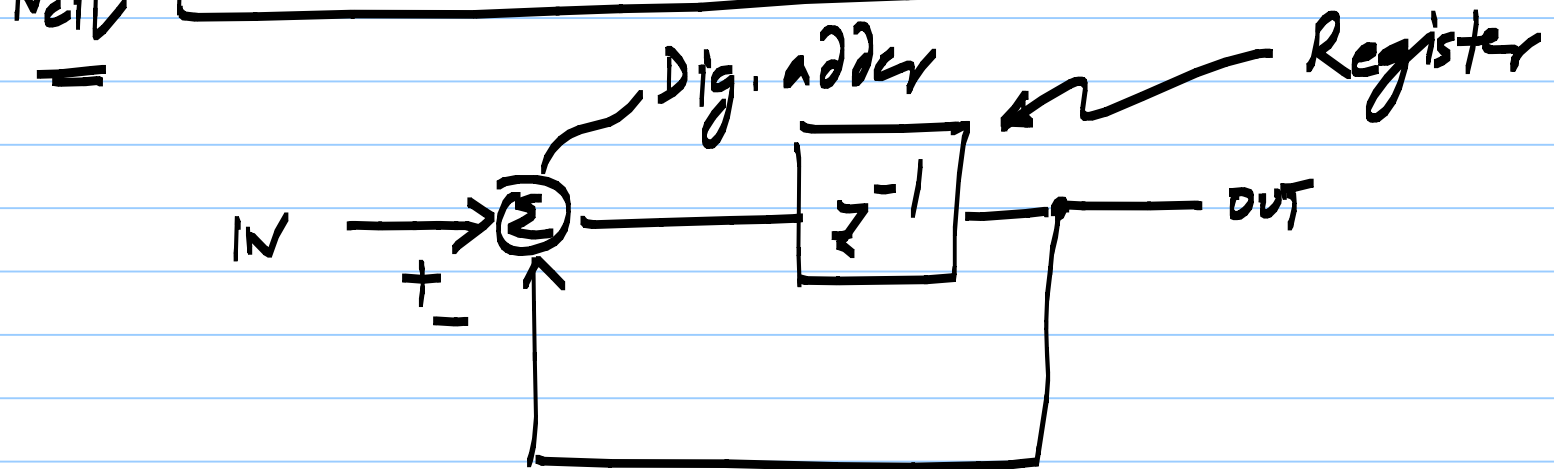
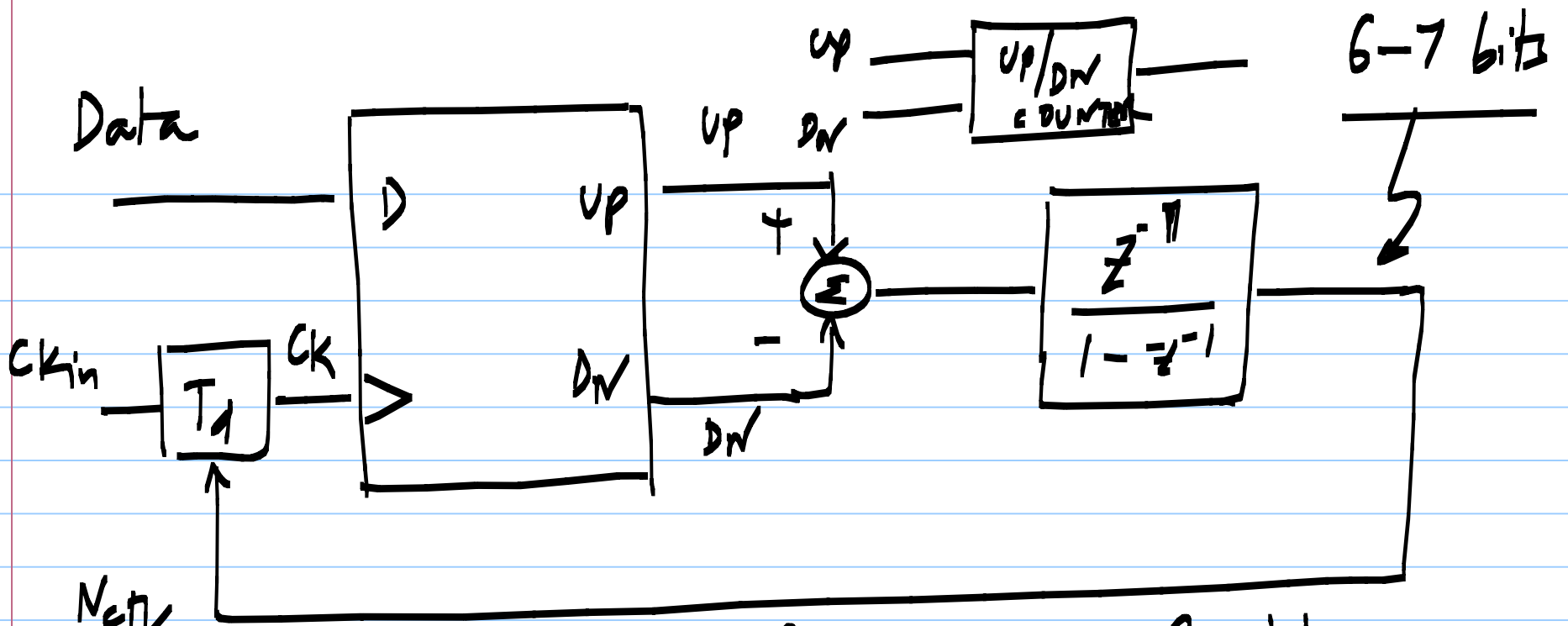
$$\alpha \cdot CK_0 + (1 - \alpha) \cdot CK_{90}$$



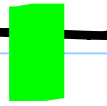
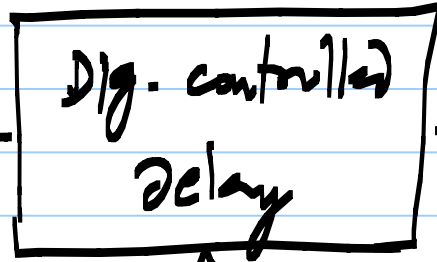
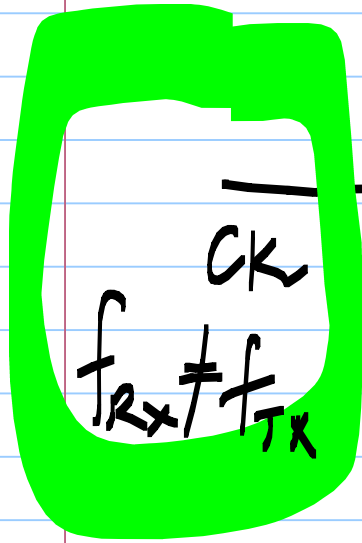
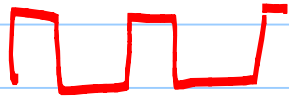
0 - N_max



All clock phases from 0-360
CK_p CK_m

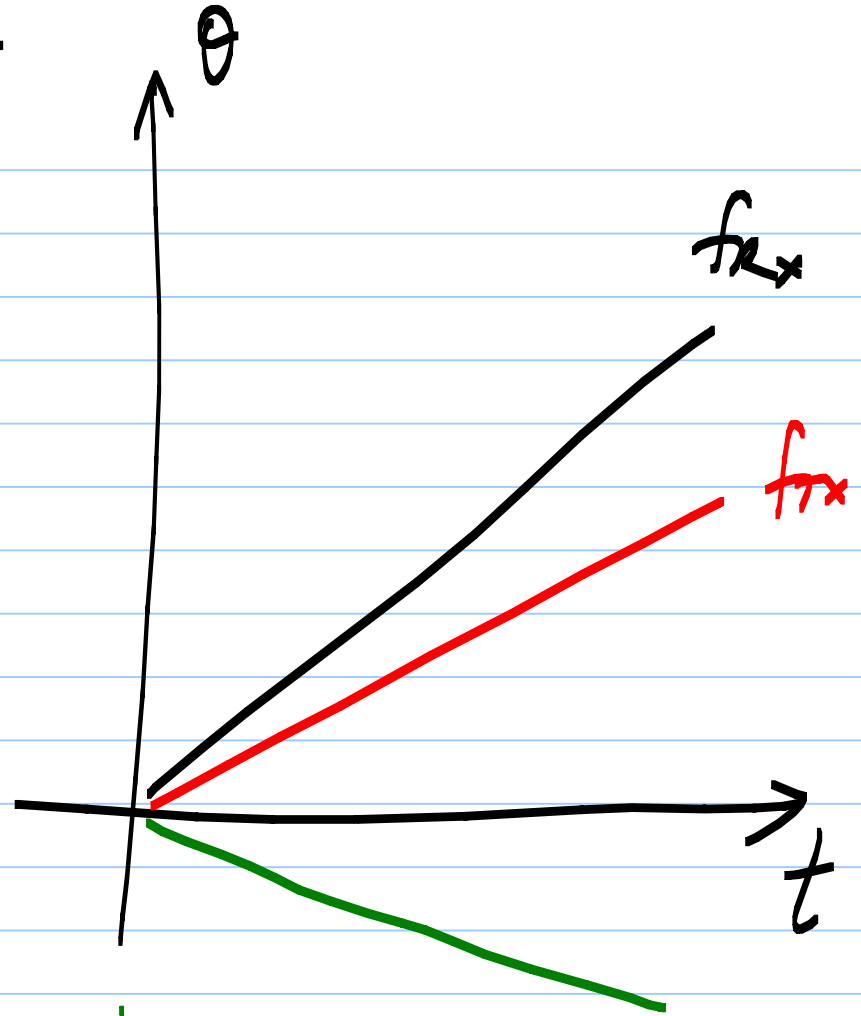


6-7 bits



N_{ctrl}

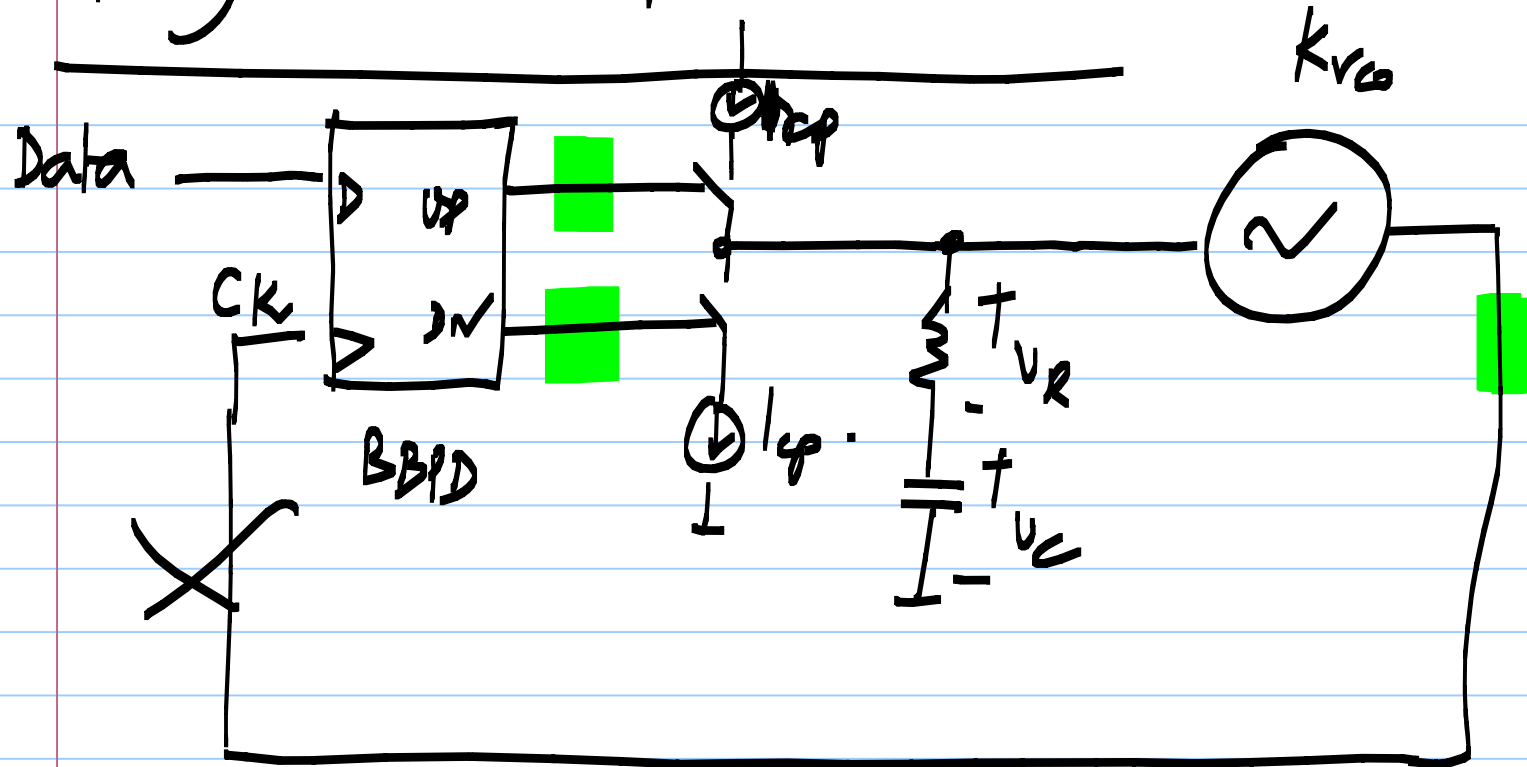
$0 - N_{max}$



Control word has to be a ramp to generate a different frequency.

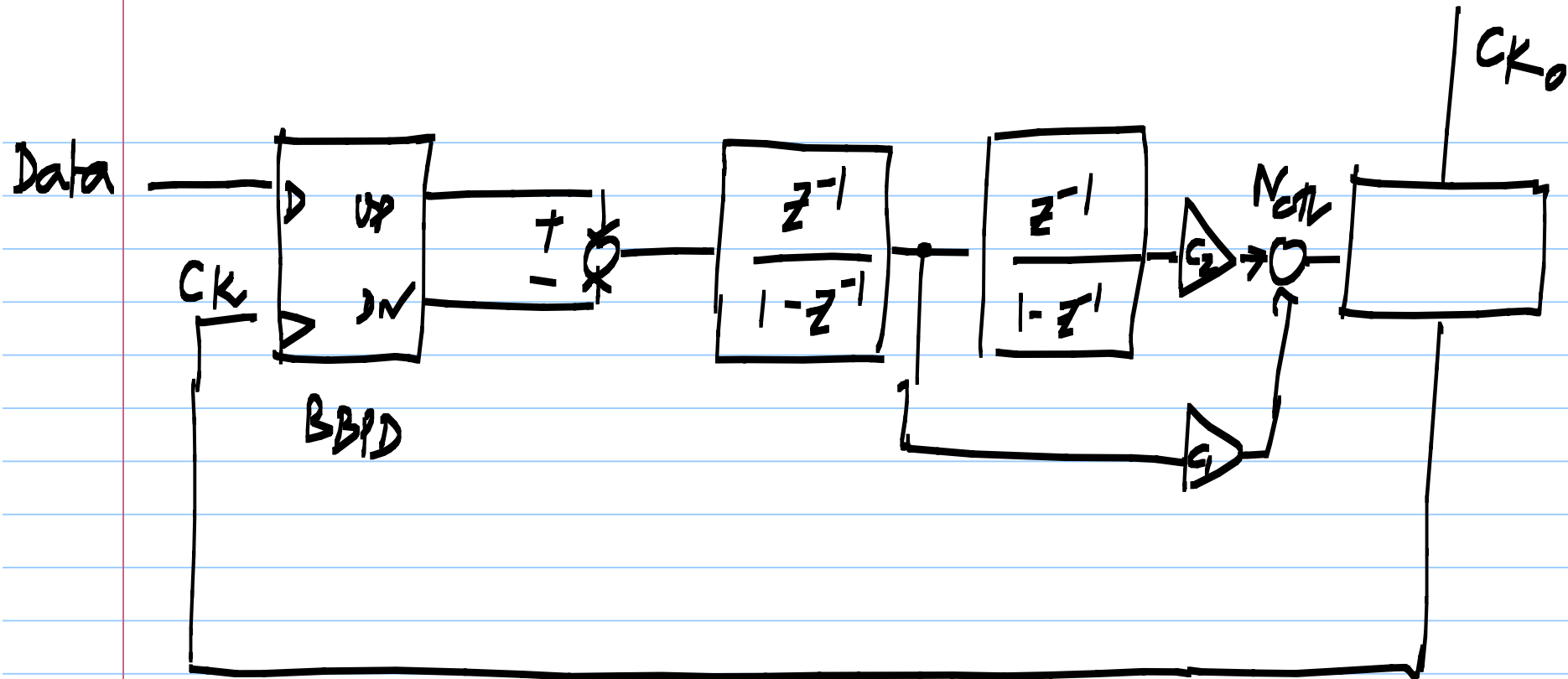
$f_{TX} - f_{RX}$

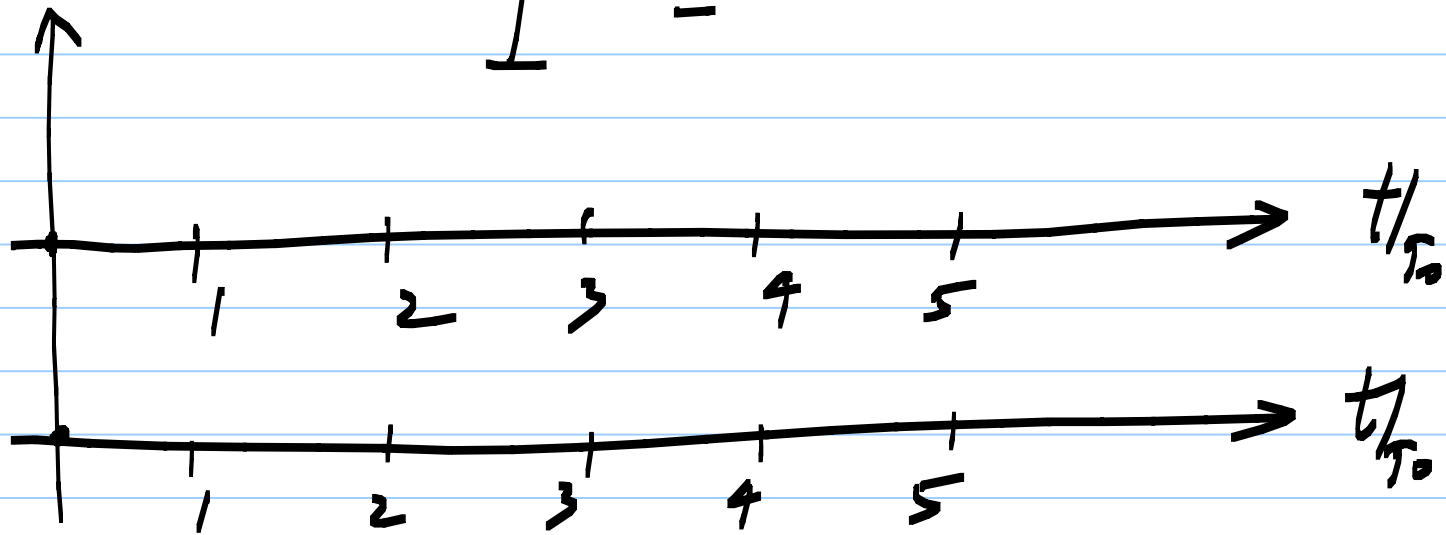
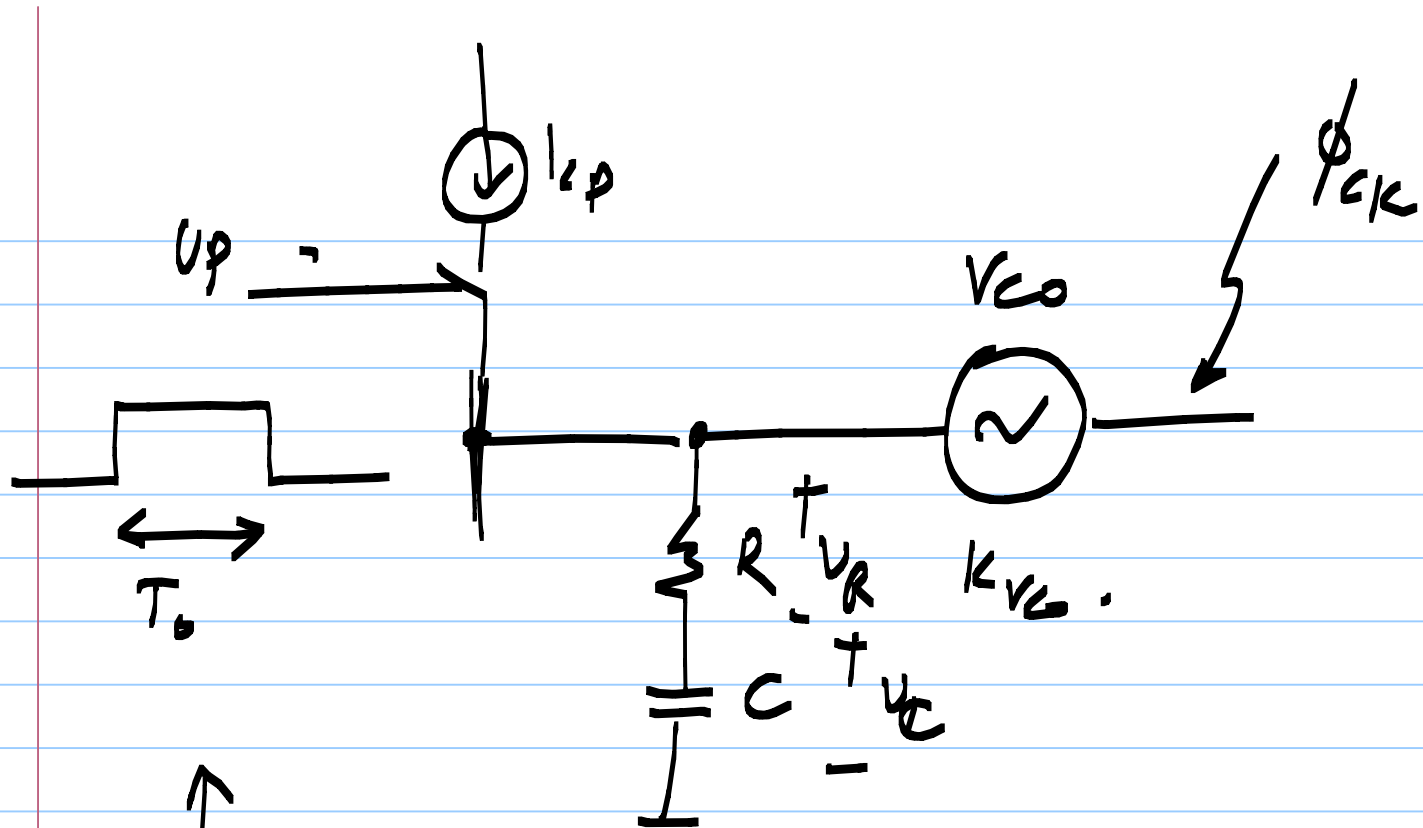
Analog VDR w/o formatted clock



Single v_p pulse;

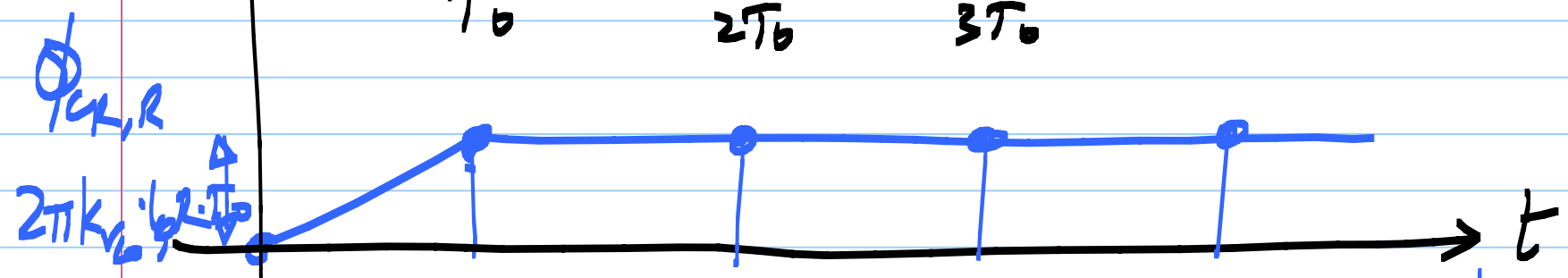
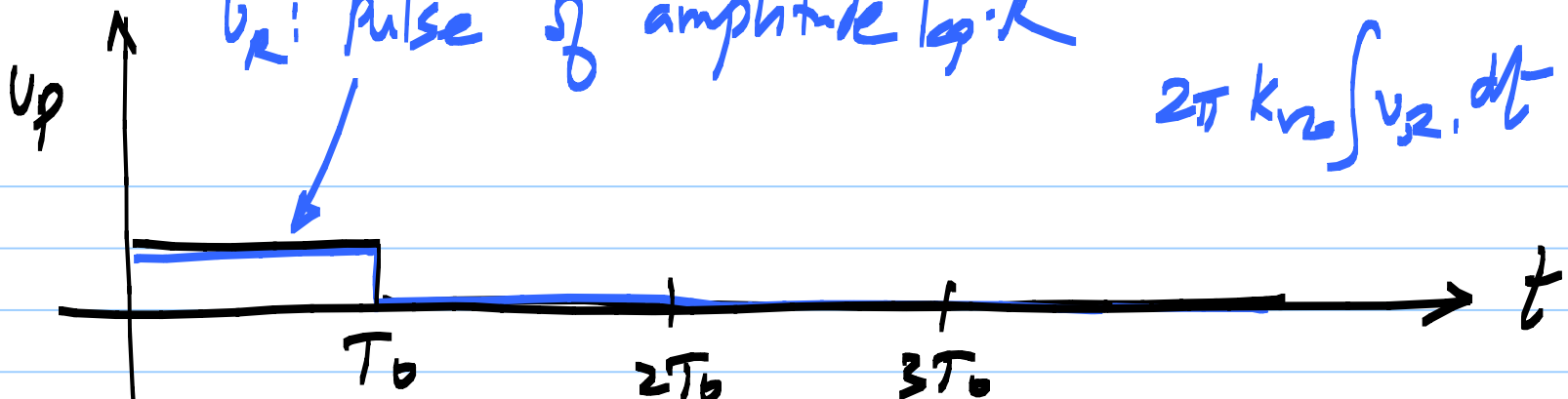
v_p : pulse $\rightarrow \phi_{ck}$:



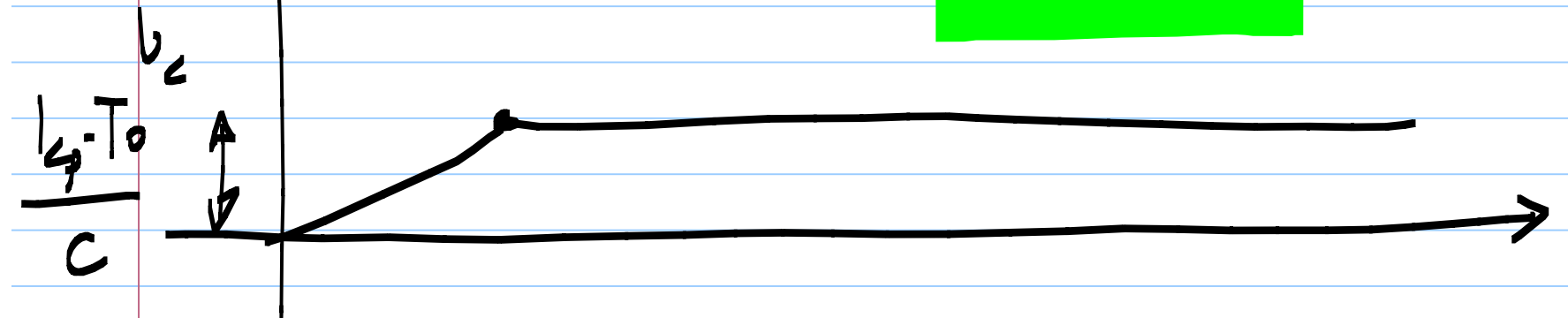


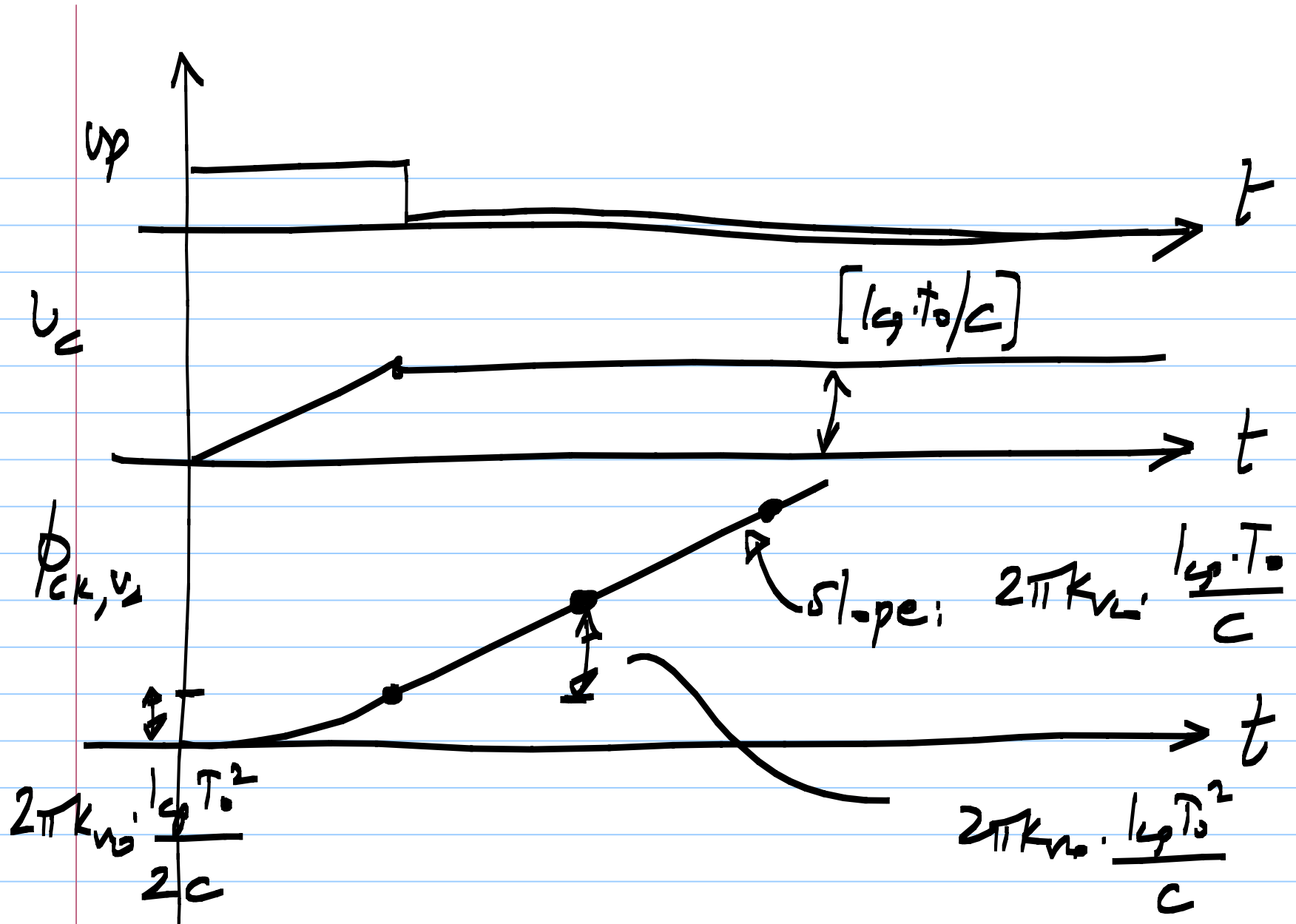
v_R : pulse of amplitude $\log R$

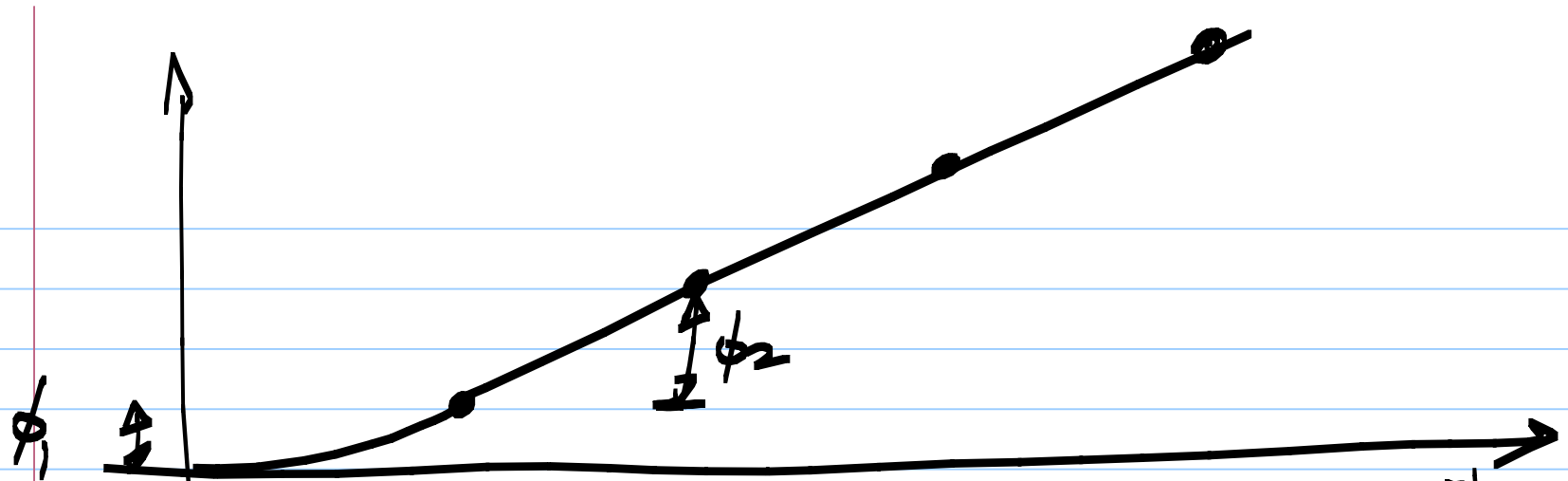
$$2\pi k_{v_0} \int v_R dt$$



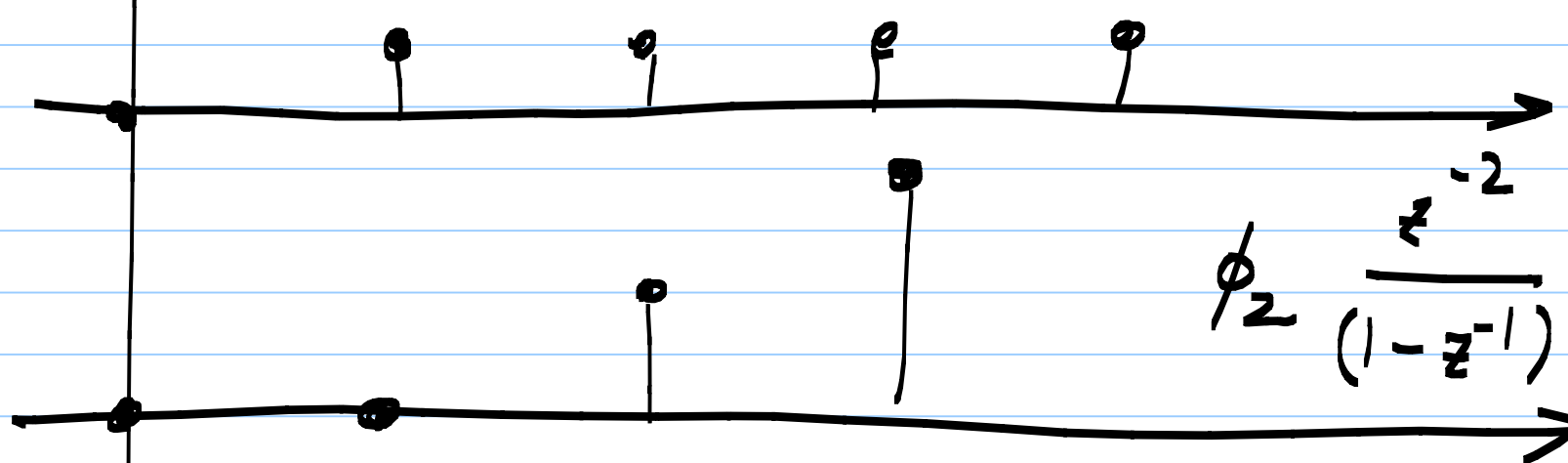
$$\frac{(2\pi k_{v_0} \log R \cdot T_0) z^{-1}}{1 - z^{-1}}$$



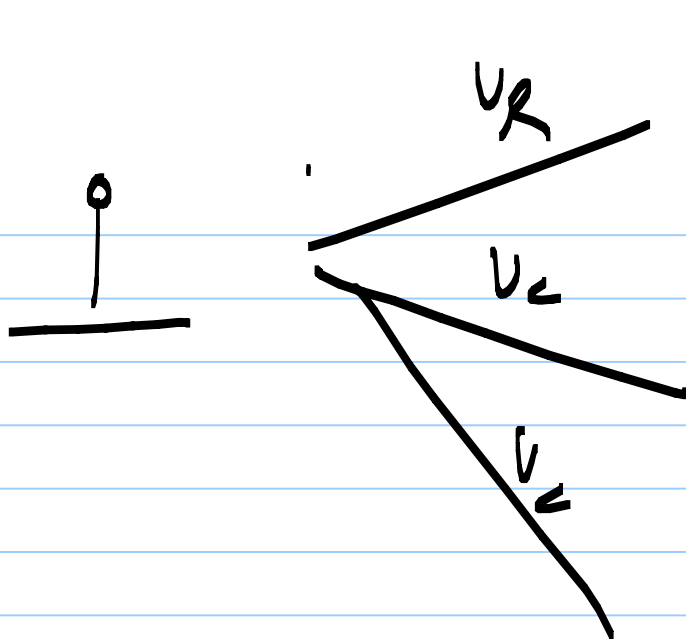




$$\phi_1 \cdot \frac{z^{-1}}{1-z^{-1}}$$



$$\phi_2 \cdot \frac{z^{-2}}{(1-z^{-1})^2}$$



$$(2\pi K_{v\omega} \cdot \frac{1}{4} R \cdot T_0) \cdot \frac{z^{-1}}{1-z^{-1}}$$

$$2\pi K_{v\omega} \cdot \frac{1}{2c} T_0^2 \cdot \frac{z^{-1}}{1-z^{-1}}$$

$$2\pi K_{v\omega} \cdot \frac{1}{c} T_0^2 \cdot \left(\frac{z^{-1}}{1-z^{-1}} \right)^2$$