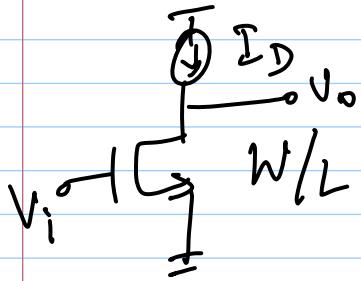


10/1/12

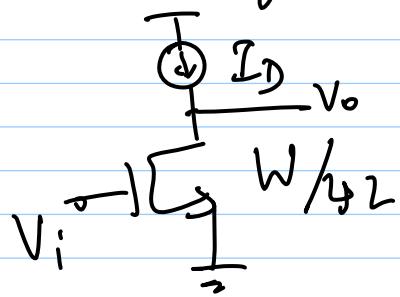
Lec 5

Compare with length increase!



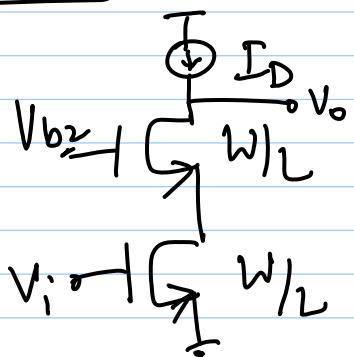
(a)

original



(b)

4X L



(c)

cascode

$$V_{DSAT_b} = 2 \times V_{DSAT_a} \quad \left. \begin{array}{l} \text{equal} \\ \text{voltage} \\ \text{constraints} \end{array} \right\}$$
$$V_{DSAT_c} = 2 \times V_{DSAT_a} \quad \left. \begin{array}{l} \text{equal} \\ \text{voltage} \\ \text{constraints} \end{array} \right\}$$

$$|A_{v_b}| = g_m r_{ds} = \sqrt{2 \mu C_ox \frac{W}{L} \frac{I_D}{2}} \cdot \frac{1}{\lambda_a I_D}$$

$$\lambda \propto \frac{1}{L} \Rightarrow \lambda_b = \frac{1}{4} \lambda_a$$

$$g_{m_b} = \frac{1}{2} g_{m_a}; \quad r_{ds_b} = 4 r_{ds_a}$$

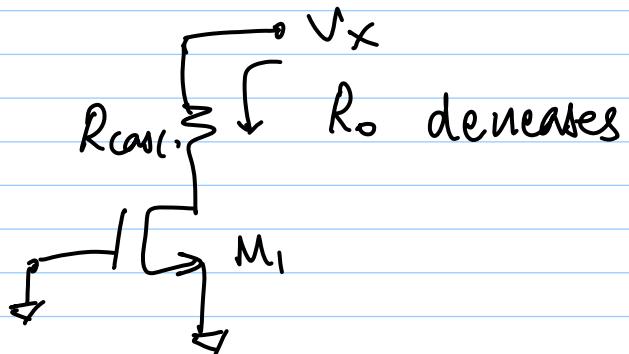
$$|A_{v_b}| = 2 \times |A_{v_a}|$$

$$|A_{v_c}| = (g_m r_{ds}) \times |A_{v_a}|$$

* Noise of b is also worse (move later)

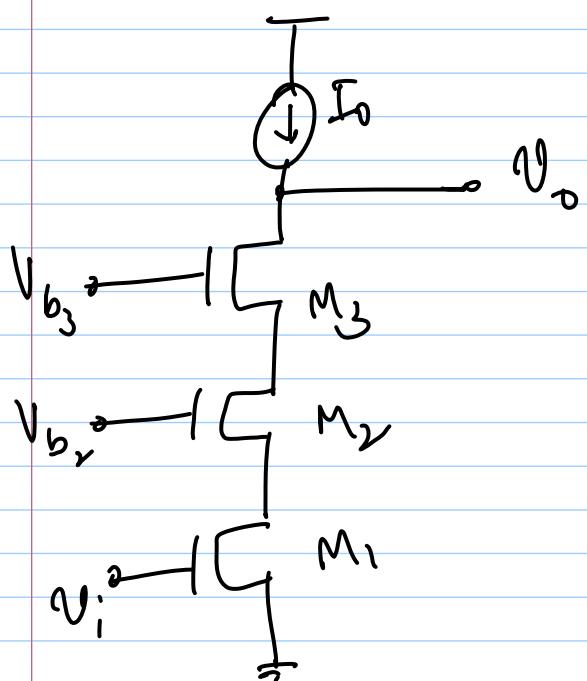
* Note: Cascode works well only if devices are in sat.

If cascode device is in non-sat:



as $V_x \downarrow$, eventually M_1 also enters triode

Triple Cascode:



$$* \frac{V_o}{V_i} = - (g_m r_{ds})_1 \cdot (g_m r_{ds})_2 \cdot (g_m r_{ds})_3$$

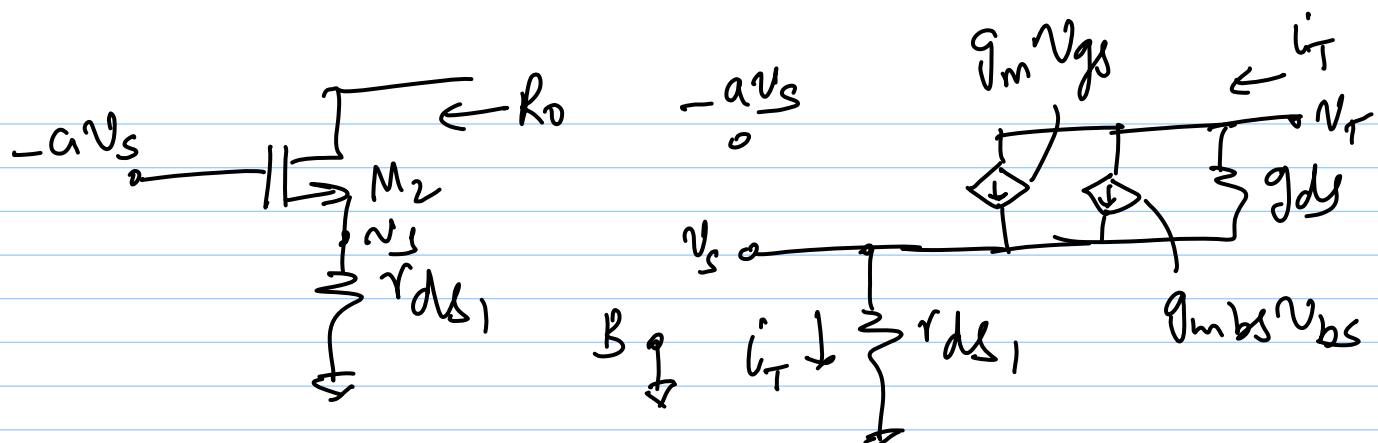
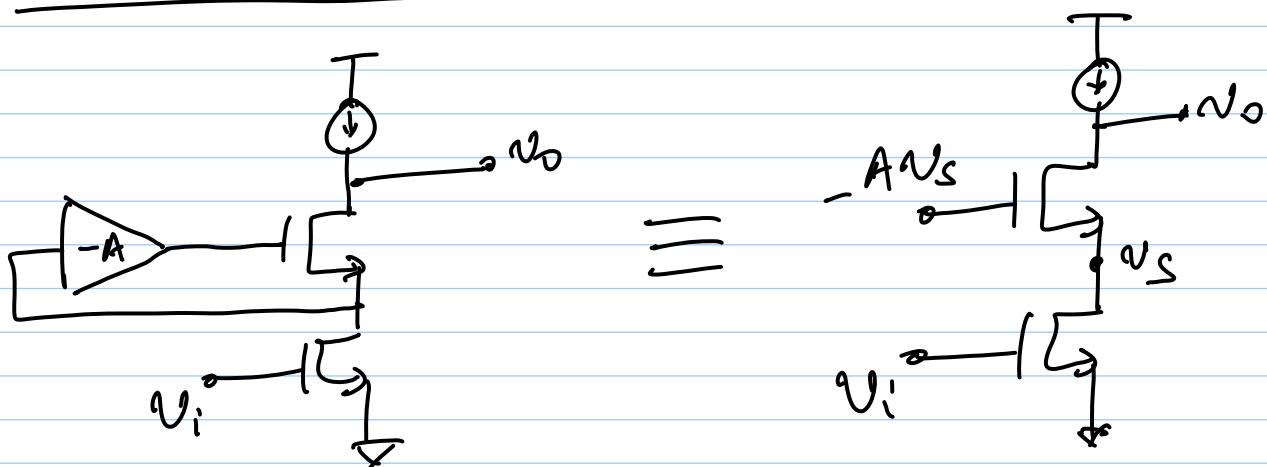
$$|A_V| \sim (g_m r_{ds})^3$$

* Voltage swings are limited
 \Rightarrow not used @ $V_{DD} < 3V$, typically

Can you do better than cascode
(with headroom constraint)?

\Rightarrow improve -ve f.b. further

Active Cascode



$$v_o = \frac{i_T}{g_{dss_1}}$$

$$i_T = g_{m2}(-av_s - v_s) + g_{mb}s_2(0 - v_s)$$

$$+ g_{dss_2}(v_T - v_s)$$

$$i_T \left\{ 1 + (a+1) \frac{g_{m_2}}{g_{ds_1}} + \frac{g_{mbs_2}}{g_{ds_1}} + \frac{g_{ds_2}}{g_{ds_1}} \right\}$$

$$= g_{ds_2} \cdot v_T$$

$$R_o = \frac{v_T}{i_T} = \frac{1}{g_{ds_2}} \left[1 + \frac{(a+1) g_{m_2} + g_{mbs_2} + g_{ds_2}}{g_{ds_1}} \right]$$

$$\approx a \cdot g_{m_2} r_{ds_2} \cdot r_{ds_1}$$

even higher
than regular
cas code!

$$a_v = - g_{m_1} R_o$$

$$= \boxed{- g_{m_1} r_{ds_1} \cdot g_{m_2} r_{ds_2} \cdot a}$$

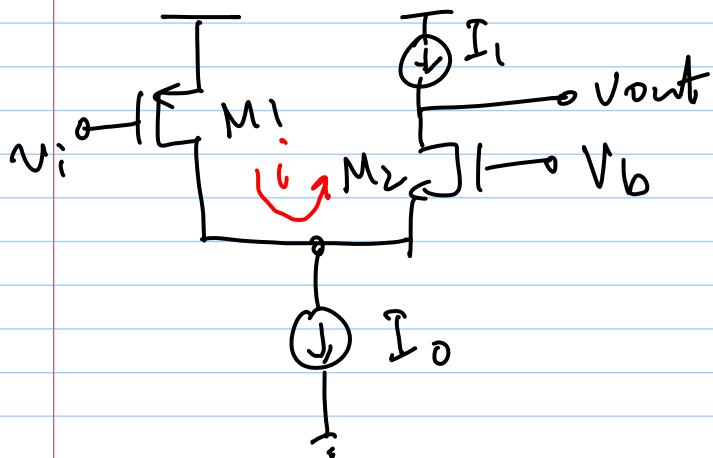
very high gain ckt!

\Rightarrow also called "gain-boosted"
cas code

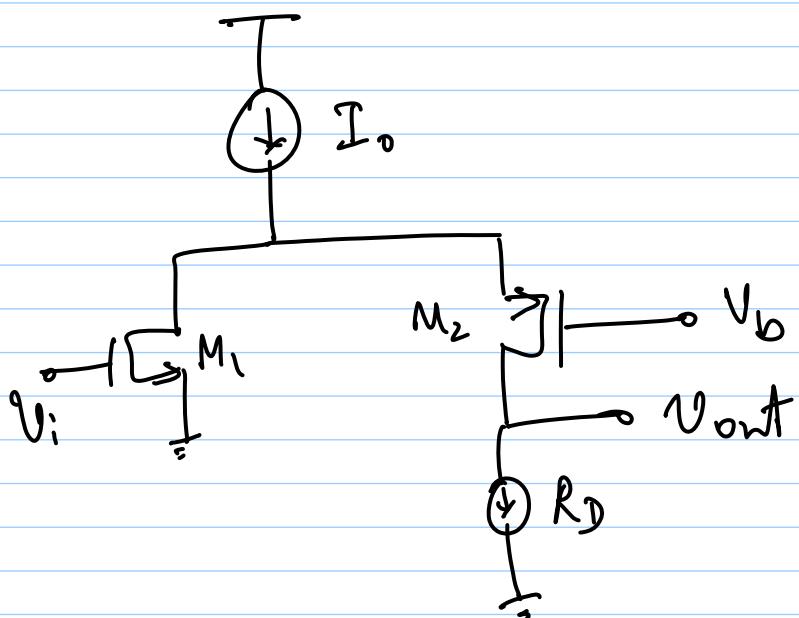
Folded Cascode

Normal cascode: input & cascode devices are same type.

Folded: NMOS - PMOS or PMOS - NMOS



- * small signal current is folded up & down
- * total bias current higher than normal cascode



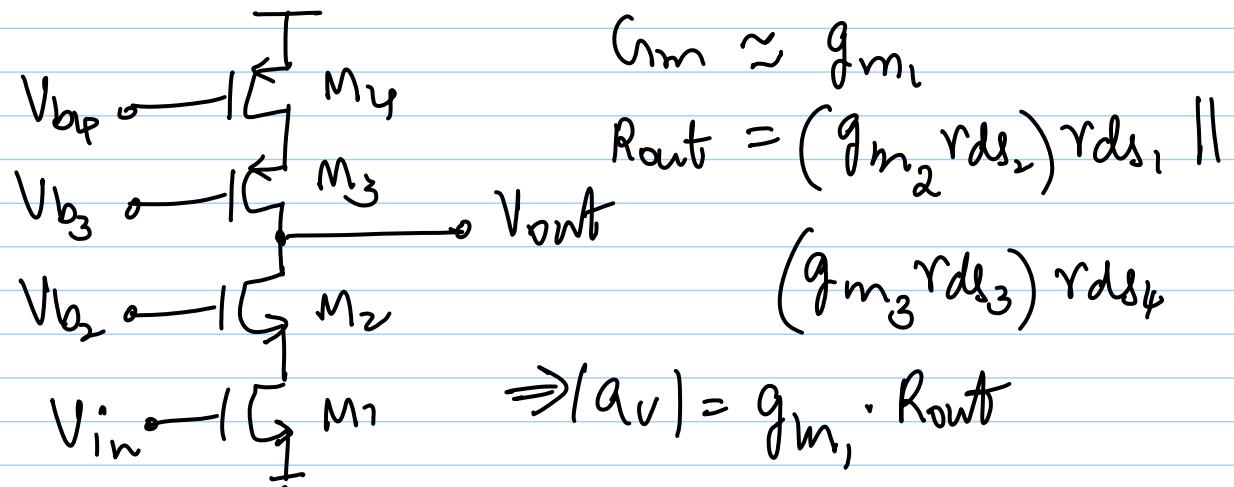
$$R_{out} \approx (g_m r_{ds2}) \cdot r_{ds1} \quad \left\{ \begin{array}{l} \text{same as} \\ \text{traditional} \end{array} \right\}$$

$$A_V \approx -(g_m r_{ds1})(g_m r_{ds2})$$

Cascode Current Source

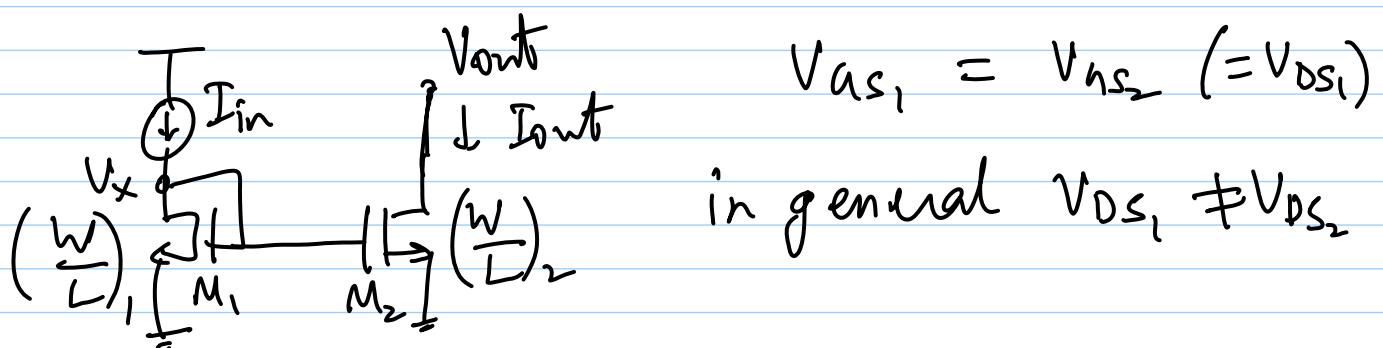
high R_{out} \Rightarrow closer to ideal current source

\rightarrow more headroom required



Cascode Current Mirrors

Simple CM:



$$\frac{I_{\text{out}}}{I_{\text{in}}} = \frac{(w/l)_2}{(w/l)_1} \cdot \frac{1 + \lambda V_{\text{ds},2}}{1 + \lambda V_{\text{ds},1}}$$

choose $w_1 = w_2$ $\underline{\underline{=}}$ $l_1 = l_2$

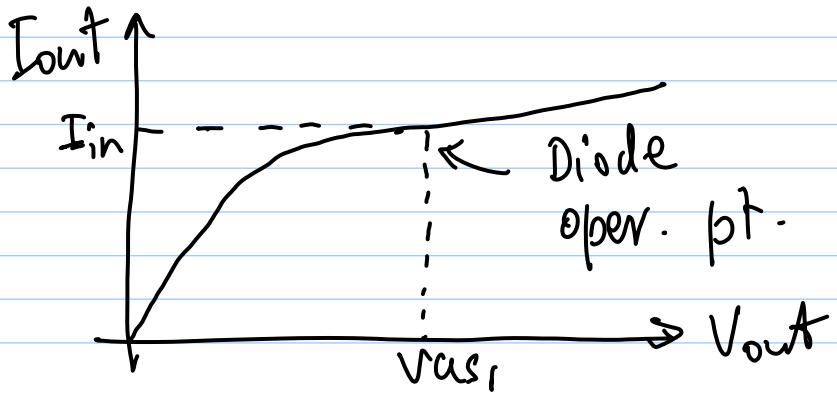
$$\frac{I_{out}}{I_{in}} = \frac{1 + \lambda V_{out}}{1 + \lambda V_x}$$

$$\approx (1 + \lambda V_{out})(1 - \lambda V_x)$$

$$\approx 1 + \lambda (V_{out} - V_x)$$

$$\approx 1 + \lambda (V_{out} - V_{as_1})$$

for small
 λ (= large
 r_{ds})



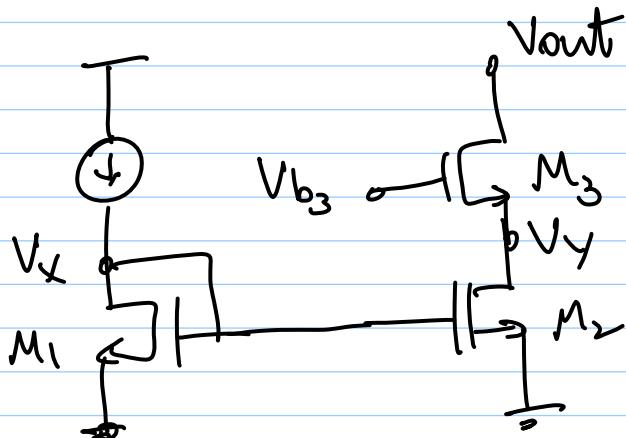
$$V_x = V_{as_1} = V_{T_1} + V_{DSAT_1}$$

$$V_{out}(\text{min.}) = V_{DSAT_2}$$

$$R_x \approx 1/g_{m_1} \quad \leftarrow \text{low } R_{in}$$

$$R_{out} = r_{ds2} \quad \leftarrow \text{high } R_{out}$$

Cascode : suppresses effect of CLM



DC balance:

choose V_b such

that $V_y = V_x$

$$\Rightarrow \frac{I_{D2}}{I_{D1}} \approx \frac{(W/L)_2}{(W/L)_1}$$

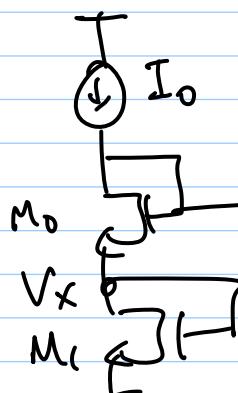
* cascode device "shields" bottom device from V_{out} variations

(HW): p.t. $\Delta V_y = \Delta V_p / (g_m M_3 + g_m M_b) \cdot r_{os3}$

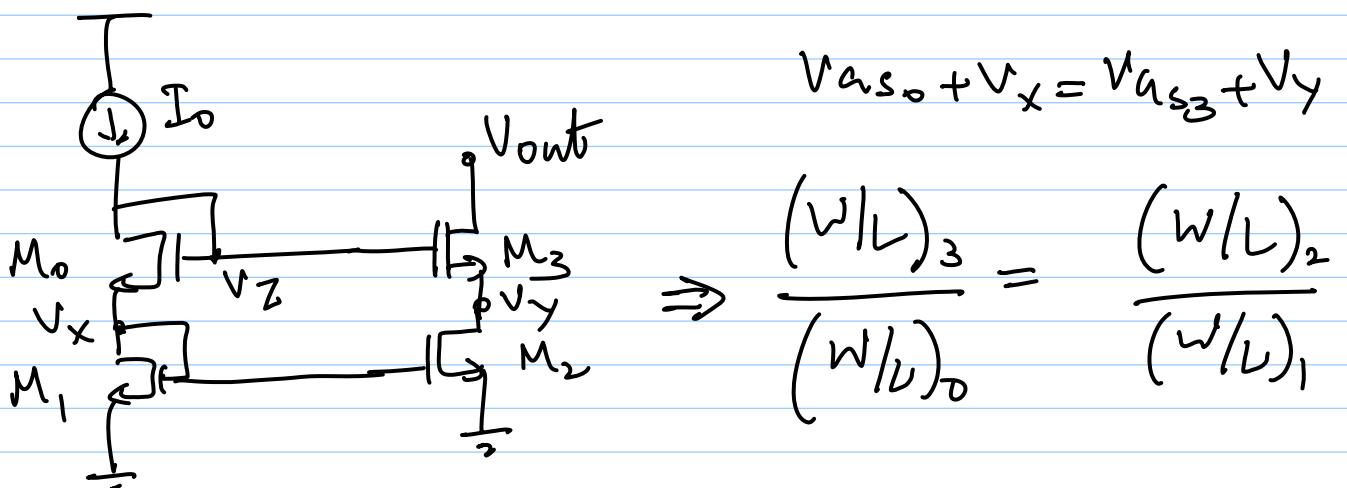
Note: $L_1 = L_2$, but L_3 can be different

$$\underline{V_{b3}} \quad V_{b3} - V_{as3} = V_x = V_x$$

$$\Rightarrow V_{b3} = V_x + V_{as3}$$



$$V = V_{as0} + V_x \Rightarrow \text{choose } V_{as0} = V_{as3}$$



$$V_{AS_0} + V_x = V_{AS_3} + V_y$$

$$\Rightarrow \frac{(W/L)_3}{(W/L)_0} = \frac{(W/L)_2}{(W/L)_1}$$

* Result holds even in presence of body effect for M_0 & M_3

$$R_{in} = \frac{1}{g_{m_0}} + \frac{1}{g_{m_1}} \simeq \frac{2}{g_{m_0}} \text{ (series)}$$

$$R_{out} = (g_{m_3} r_{ds_3}) r_{ds_4} \text{ (cascode)}$$

headroom: (neglect body effect)

$$V_{out\min.} = V_Z - V_T$$

$$= V_{AS_0} + V_{AS_1} - V_T$$

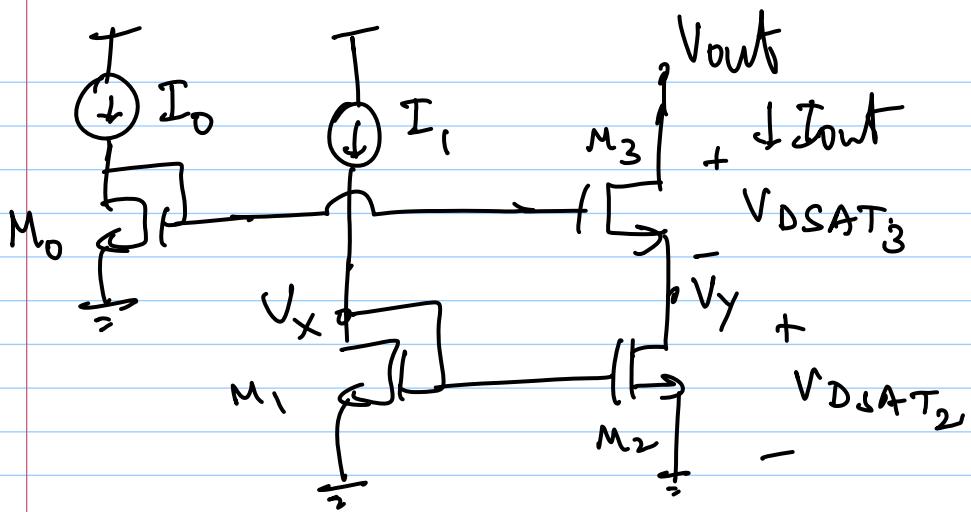
$$= (V_{AS_0} - V_T) + (V_{AS_1} - V_T) + V_T$$

$$= V_{DSAT_0} + V_{DSAT_1} + V_T$$

* headroom of V_T is "wasted"

* V_{b_3} can be chosen to be lower such that

$$V_y = V_{AS} - V_T$$



- * $V_y = V_{DSAT_2} - M_2$ still is sat.
- * $I_{out} \neq I_0$ because $V_x \neq V_y$
(DC imbalance)

$$V_{AS_0} - V_{AS_3} = V_{DS_2} = V_{DSAT_2}$$

$$(V_{T_0} + V_{DSAT_0}) - (V_{T_3} + V_{DSAT_3}) = V_{DSAT_2}$$

neglect body effect

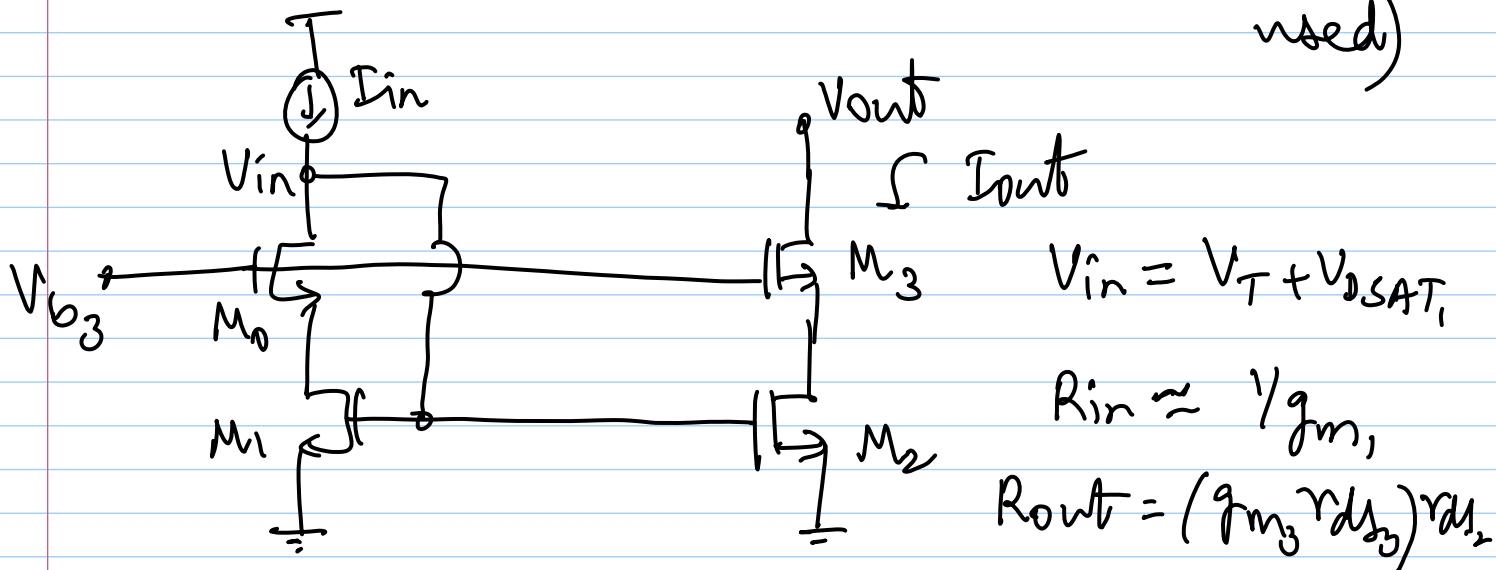
$$\Rightarrow V_{DSAT_0} = V_{DSAT_2} + V_{DSAT_3}$$

$$= 2 V_{DSAT_{2,3}} \text{ typically}$$

$$V_{DSAT_0} = \sqrt{\frac{2 I_0}{\mu_n C_{ox} (W/L)_0}}$$

$$I_0 = I_1 = I_{out} \Rightarrow \left(\frac{W}{L}\right)_0 = \frac{1}{4} \left(\frac{W}{L}\right)_{2,3}$$

"Over the shoulder" cascode : (widely used)



$$V_{b_3} = V_T + 2 V_{DSAT}$$

$$V_{DS_1} = V_{D1S_2} = V_{b_3} - V_{AS} = V_{DSAT}$$