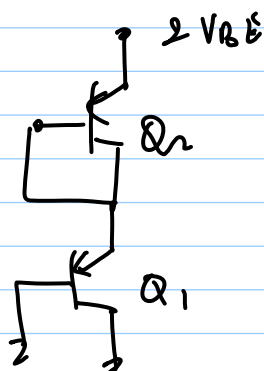


3-4-12

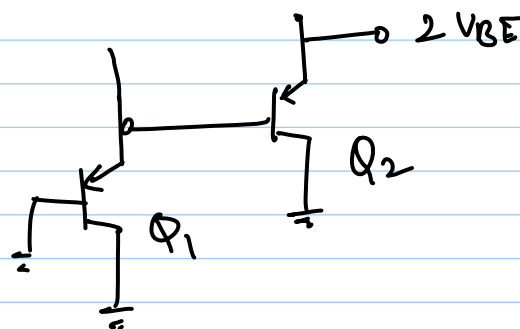
Lec 37



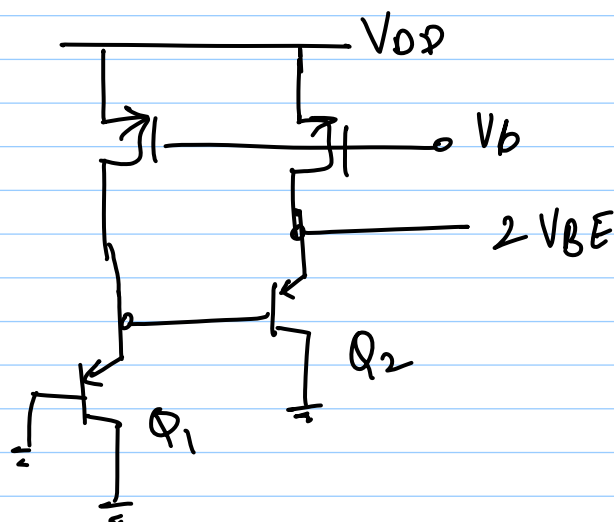
← not possible in CMOS
(collector to be grounded)



⇒



* Bias currents must behave the same with temp.



* replace resistors w/ pmos current source
* I_B 's of pnps would still cause errors

