EE6320 Project 2: Mixer Design – due Tuesday 31/03/2026 (11:59pm)

In this project, you are asked to design a fully differential double-balanced gilbert cell active mixer with I and Q outputs to be used in a direct-conversion receiver, for the specifications given below. The basic circuit topology should be that discussed in class. However, you can modify it with circuit techniques to improve its performance, as long as you support it with analytical and simulation results. The baseband (BB) should have at least a 1-pole response as per the below specifications . Use the IBM 130 nm CMOS process parameters supplied to you through the class website (Vdd = 1.2 V, Wmin = 0.16 µm, Lmin = 0.12 µm). There are two sections in this project:

(a) In this portion of the project, design the mixer for the following specs:

- RF Frequency f_{RF} = 3.45 to 3.55 GHz; LO frequency f_{LO} = 3.5 GHz.
- RF BW of desired signal = 20 MHz (i.e. Baseband BW = 10MHz). i.e. f_{RF} occupies the spectrum f_{LO} ± 10MHz for a given f_{LO}. Maximum baseband droop allowed at +10MHz is 1dB.
- Mixer conversion gain > 20dB
- SSB NF ≤ 12dB (Integrate BB output of mixer from 1 kHz to 10 MHz)
- IIP2 ≥ +20dBm; use two tones at 3.5 GHz and 3.51 GHz and apply a systematic mismatch of 0.5% between all devices (active or passive) in the two differential halves
- IIP_3 ≥ −5dBm {Use the same two tones as above; choose the extrapolation point carefully}
- Minimise power consumption (i.e. total DC current drawn from supply); V_{DD} = 1.2 V
- Try to maintain the input capacitance of the mixer to be lower than the total fixed capacitance at the LNA output in project 1. If it turns out to be larger, you may need to change the LNA drain inductor for section (b) below. If it is smaller, you can add a fixed ideal capacitance to set the resonance at LNA output to the original value.

(b) Now, combine the mixer from (a) above with the LNA you designed in Project 1. Determine the overall Gain, NF and IIP3 and compare your results to hand calculations.

Notes:

1) No ideal inductors are allowed! Add a resistor in parallel with each of the inductors in your circuit so that it has a Q of 20 at 3.5 GHz). All capacitors can be assumed to be ideal.
2) It is expected that the IM2 and IM3 curves be well-behaved with normal linear behaviour at low power levels and gain compression at high powers. Some gain-expansion is ok (say ~1dB), but too much is not good. Make sure your IM2 and IM3 curves do not have any unexpected non-linearities at lower power levels. Choose the extrapolation point carefully.
3) Include and discuss the expected characteristics of the LO waveform in your report.\
4) You may use an ideal VCVS with gain of 100 as an opamp in the CMFB loop of active load.