EE2019–Analog Systems and Lab: Tutorial 7

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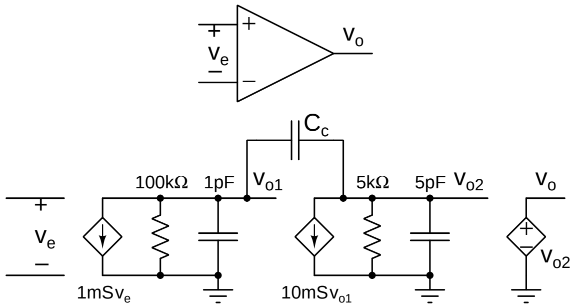

Figure 1: Circuit for problem 1

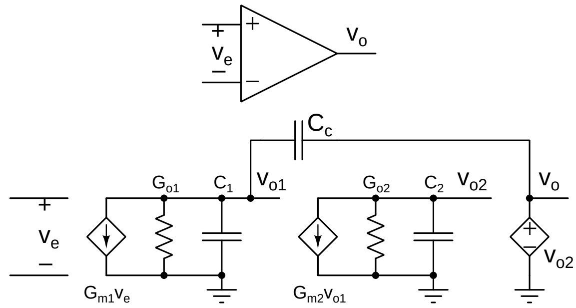

Figure 2: Circuit for problem 2

1. Fig. 1 shows the internal schematic of a Miller-compensated opamp. This opamp is used to realize a unity gain, non-inverting amplifier.
   - What is the phase margin?
   - Determine C_c so that the phase margin is 60°.
   - If the same opamp is used without any change to realize an inverting amplifier of gain -4, what are the phase margin and the closed loop bandwidth?
   - Re-design the opamp (value of C_c) so that when an inverting amplifier of gain -4 is realized using it, the phase margin is 60°. Compare the three cases wrt the following aspects: (a) Closed loop bandwidth, (b) phase margin, (c) phase lag contributed by the right-half-plane zero at the unity loop gain frequency.
   - Compare the bandwidths you obtain to the ones in the previous tutorial in which you simply increased C_1.

   While determining the unity loop gain frequency, phase margin, and C_c, do the calculations with and without the approximation $C_c \gg C_{1,2}$.

2. Determine the transfer function of the opamp in Fig. 2. How does it differ from the conventional Miller compensated opamp in the previous problem?

3. It is common to approximate the unity loop gain frequency as $\omega_{u,loop} \approx L_0 p_1$ where L_0 is the dc loop gain and p_1 is the dominant pole. If the loop gain is a second order function $L(s) = L_0/(1 + s/p_1)(1 + s/p_2)$, determine the exact unity loop gain frequency and the phase margin for the following cases: (a) $p_2 = 4L_0p_1$, (b) $p_2 = 2L_0p_1$, and (c) $p_2 = L_0p_1$. Compare them to the values obtained using the approximation above. $L_0 \gg 1$.

   (This approximation is very commonly used for hand calculations, but you should know how much error you end up with while doing so.)