

IEEE Solid-State Circuits Society Distinguished Lecture Series

also a Special SMDP-II Instruction Enhancement Programme¹ on

“Analog/Mixed Signal Design and Industry Challenges”

The lectures are intended to present educational and tutorial content².

Central Lecture Theatre
Humanities and Sciences Block
IIT Madras

Date	Venue	Contact Person
December 9 2011 (Friday)	IIT Madras at Chennai ESB 244, Electrical Sciences Division Block	Prof. Nagendra Krishnapura nagendra@iitm.ac.in
December 10 2011 (Saturday)	IISc Bangalore, ECE Golden Jubilee Seminar Hall	Prof. Bharadwaj Amrutur subhashini@ece.iisc.ernet.in
December 12 2011 (Monday)	IIT Delhi,	Prof. Shouri Chatterjee shouri@gmail.com

Topics and Speakers:

Time	Topic	Speaker
10-11:15am	Transistor sizing in deep submicron CMOS - matching hand calculations and SPICE	Professor Bernhard Boser, University of California, Berkeley President, IEEE SSCS (2010-11)
11:15-11:30pm	Break	
11:30-12:45pm	Noise Analysis in Switched-Capacitor Circuits	Professor Boris Murmann, Stanford University
12:45-2pm	Lunch	
2-3.15pm	Power reduction in mixed signal circuits using feed forward techniques	Professor Shanthi Pavan, IIT Chennai
3:15-3.30pm	Break	
3:30-4:45pm	Semiconductor Industry Challenges and Opportunities	Dr. Rakesh Kumar, TCX Technology Connexions Incoming President, IEEE SSCS (2012-13)

¹ SMDP participants can contact host venue RC coordinator for help with accommodation

² Same lectures will be given on all three days at these different venues.