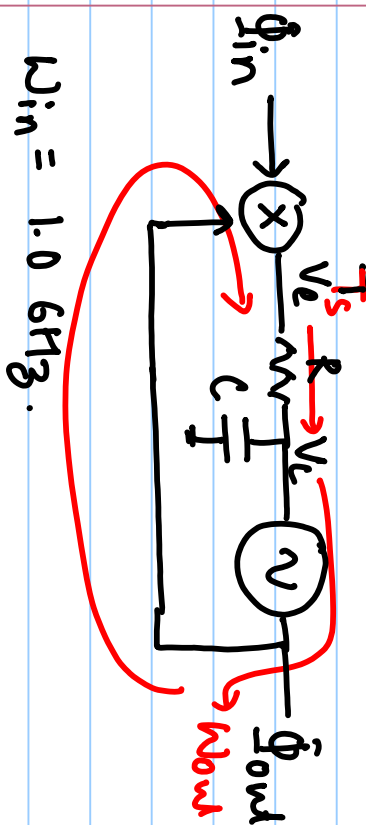


# Frequency Acquisition for Type-I PLL



$$\omega_{in} = 1.0 \text{ GHz}$$

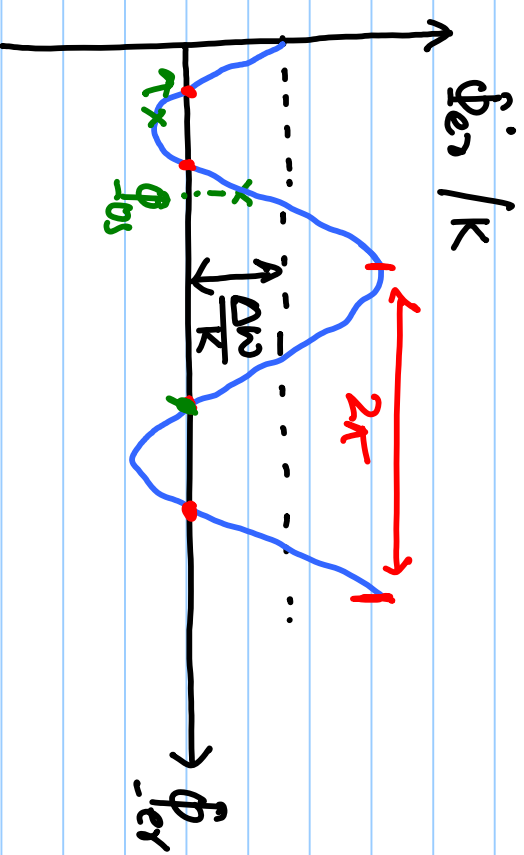
$$\omega_{out} = \omega_{free} + K_{vco} \cdot V_c$$

$$\Delta\omega = \omega_{in} - \omega_{free}$$

$$\dot{\Phi}_{er} = \Phi_{in} - \Phi_{out}$$

$$= \Delta\omega \cdot t - \int K_{vco} \cdot K_{pd} \sin(\Phi_{er}) dt$$

— There is a locking point  $\dot{\Phi}_{er} = 0$  for  $\Phi_{er} \in [0, 2\pi]$ .



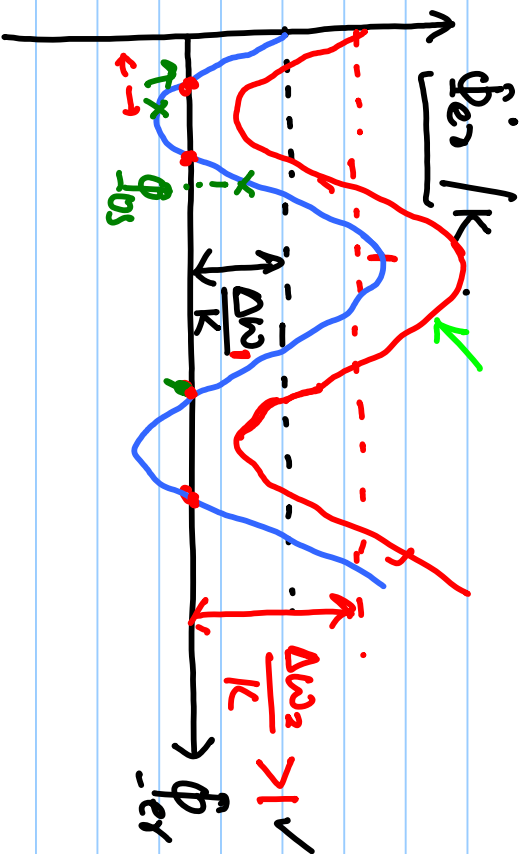
$$\frac{d\dot{\Phi}_{er}}{dt} = 0 \Rightarrow \dot{\Phi}_{er} = \Delta\omega - \underbrace{K_{vco} \cdot K_{pd}}_K \sin(\Phi_{er}) = 0$$

$$= K \left( \frac{\Delta\omega}{K} - \sin(\Phi_{er}) \right)$$

$\omega_{in} : \text{input freq, } \omega_{out} = \omega_{free}, \quad \Delta\omega_j = \omega_{in} - \omega_{free}$

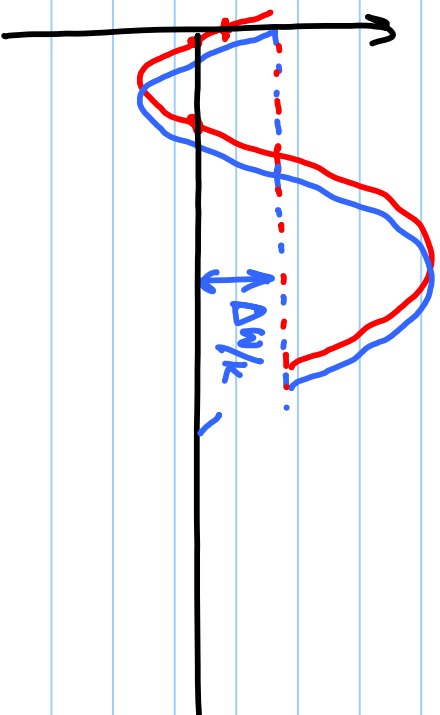
Lock-in range:  $\Delta\omega_L$

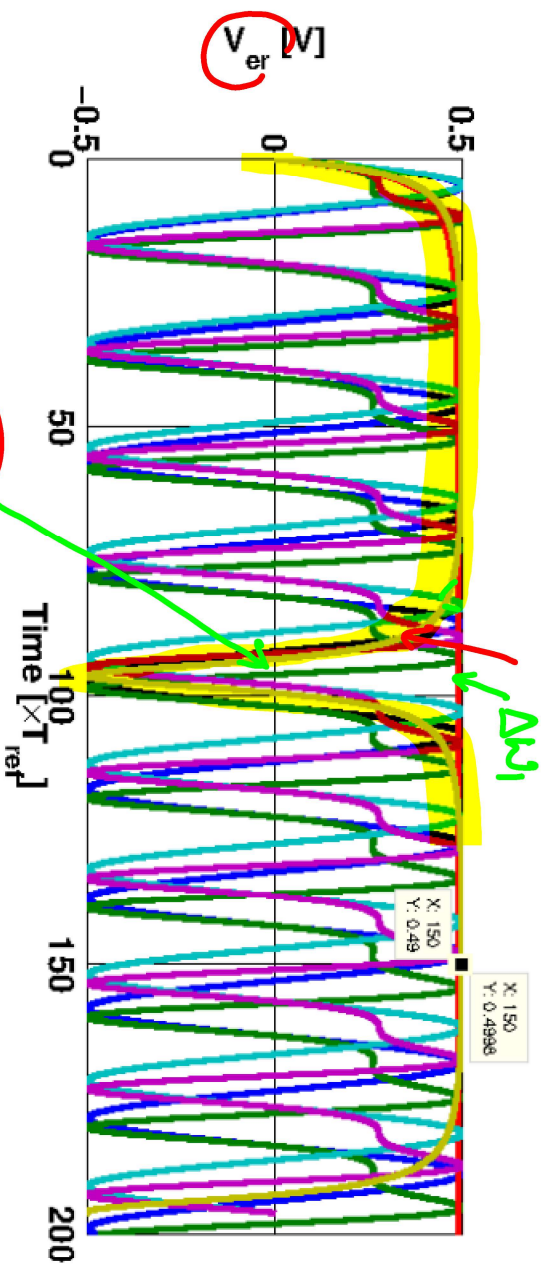
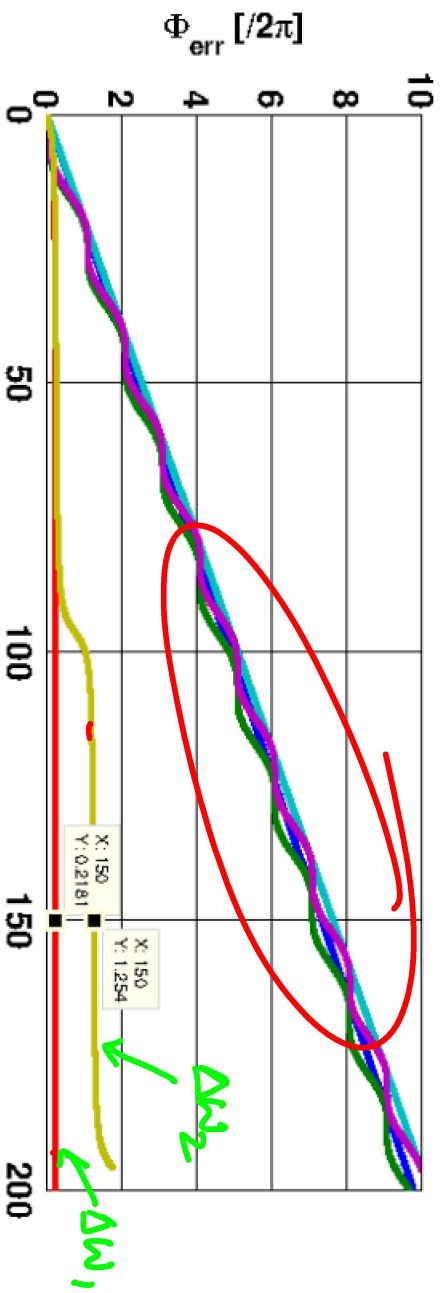
Frequency range the PLL locks without the phase error exceeding  $2\pi$ .



$$\Delta\omega_L = K$$

$$\{ \Phi_{os}, \Phi_{os} + 2\pi \}$$

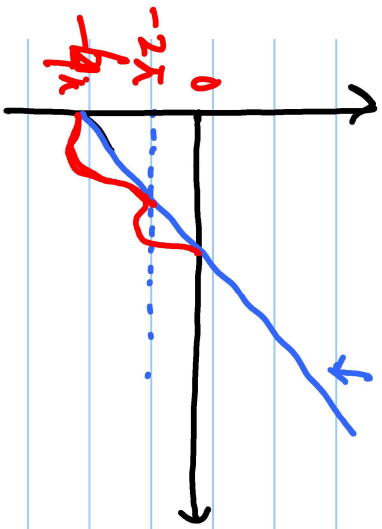


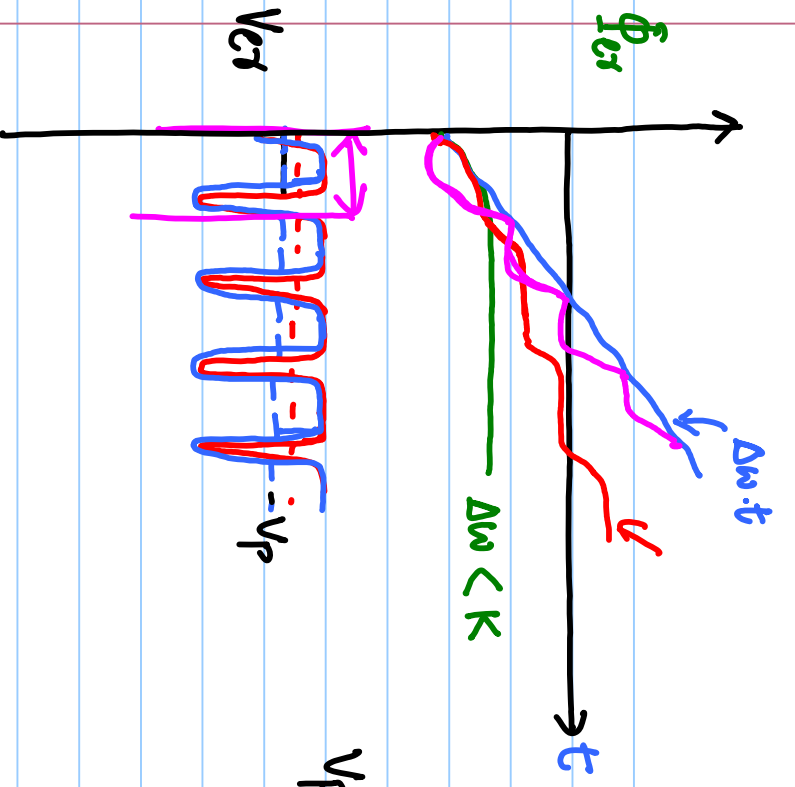


$$\Delta\omega_L = K_{VCO} \cdot K_{PD} \approx 50 \text{ MHz}$$

$$\Delta\omega_1(0) = 49 \text{ M}$$

$$\Delta\omega_2(0) = 51 \text{ M}$$





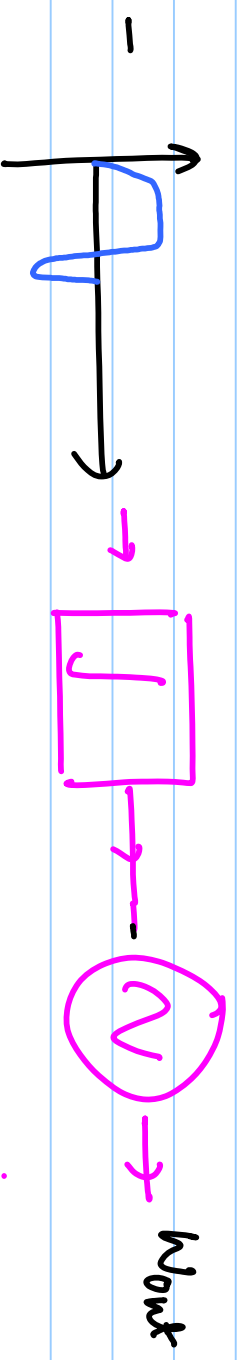
$$V_e = \sin(\Phi_e) = \sin(\Delta\omega \cdot t)$$

$$V_p = K_{PD} \left[ \frac{\Delta\Omega}{K} - \sqrt{\left(\frac{\Delta\Omega}{K}\right)^2 - 1} \right]$$

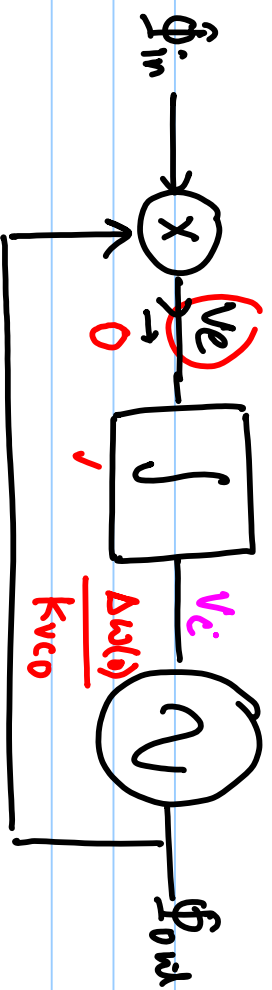
$$\Delta\Omega = \omega_{in} - \omega_{out}$$

- For  $\Delta\omega > K$ ,  $\Phi_{err} \neq 0$ ,  $V_p$  ( $V_e$  during one beat period) is proportional to frequency error.

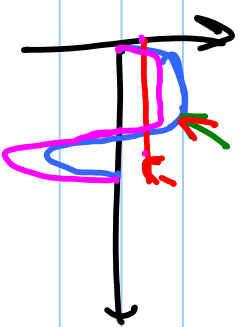
$$0 \rightarrow 2\pi$$



$$V_c = V_{dc} + V_{ac}$$



at  $t=0$ ,  $\Delta\omega = \Delta\omega(0) > K$



$$V_c = \int V_{dc} + V_{ac} \cdot dt$$

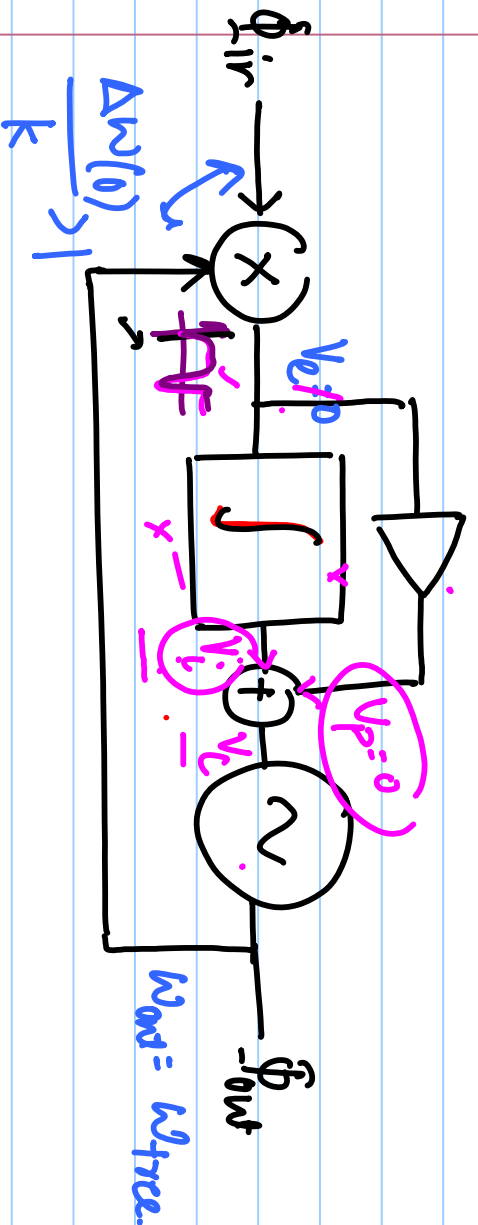
#2:  $\Delta\omega(1) < \Delta\omega(0)$

$$\Delta\omega(1) > K$$

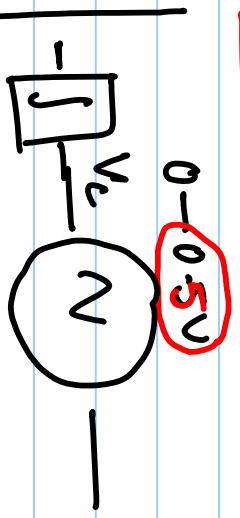
Pull-in range:  $(\Delta\omega_p)$

Max. Frequency to which the PLL can lock

while the  $\phi_{err}$  exceeds  $2\pi$ .



$$\Delta\omega(n) < K$$



$$V_c = \frac{1}{2} \sin(\Delta\omega(0) \cdot t)$$

$$\bar{V}_c = 0$$

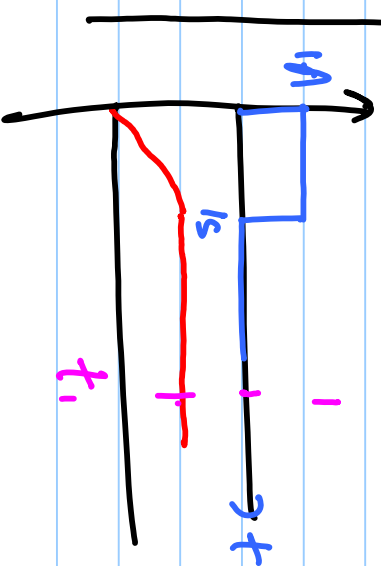
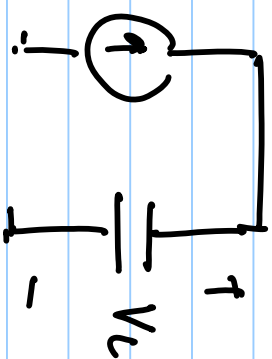


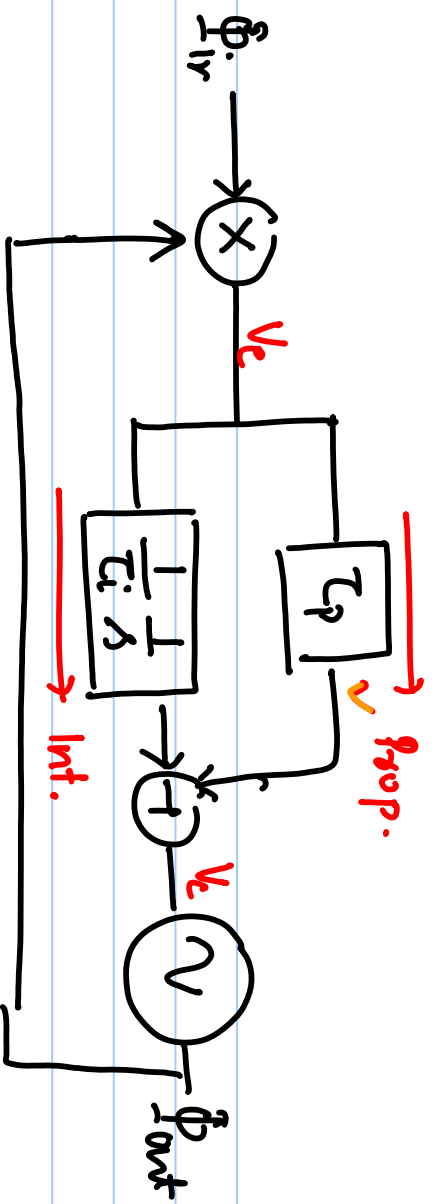
$$\int \sin(\Delta\omega \cdot t) dt = \frac{-\cos(\Delta\omega \cdot t)}{\Delta\omega} \Big|_0^t = \frac{1}{\Delta\omega} (1 - \cos(\Delta\omega \cdot t))$$

$$V_c = \frac{1}{2} \sin(\Delta\omega \cdot t - k_{vco} \int V_c \cdot dt) \quad \checkmark$$

$$V_c = \int V_c dt$$

$$V_c = \int \frac{1}{2} \sin(\Delta\omega \cdot t - k_{vco} \int V_c \cdot dt) dt$$

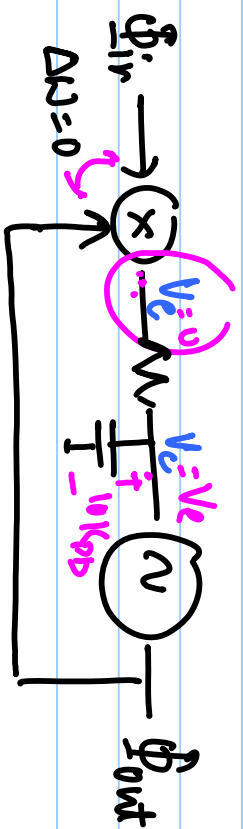




lock-in Range: ( $\Delta\omega_L$ ) w/o  $\Phi_e$  exceeding  $2\pi$  (w/o cycle slipping)

Pull-in Range ( $\Delta\omega_P$ ) w or w/o  $\Phi_e$  exceeding  $2\pi$  will

Hold-in Range ( $\Delta\omega_H$ ) range of frequencies where the PLL remain in lock.



$$\Delta\omega_L = K_{VCO} \cdot K_{PD}$$

$$\Delta\omega_P = K_{VCO} \cdot K_{PD}$$

$$\Delta\omega_H > \Delta\omega_P > \Delta\omega_L$$

$$\Phi_e = \Delta\omega - K_{VCO} \cdot K_{PD} \sin(\Phi_e) = 0$$

$$\frac{\Delta\omega}{K} = 10$$

$$V_c = \frac{10K}{K_{VCO}} = \frac{10 \cdot K_{PD} K_{VCO}}{K_{VCO}} = 10K_{PD}$$

