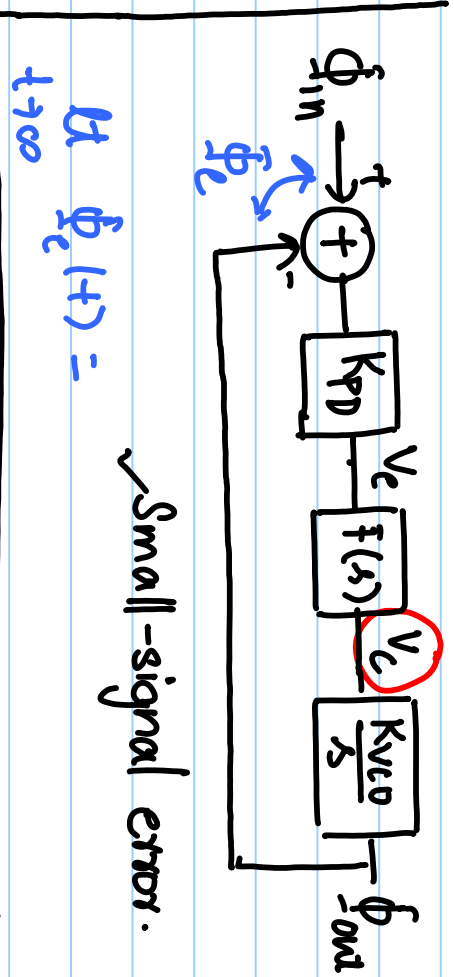
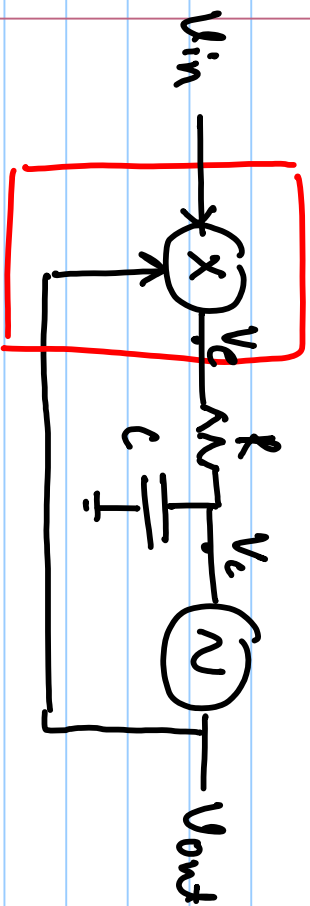


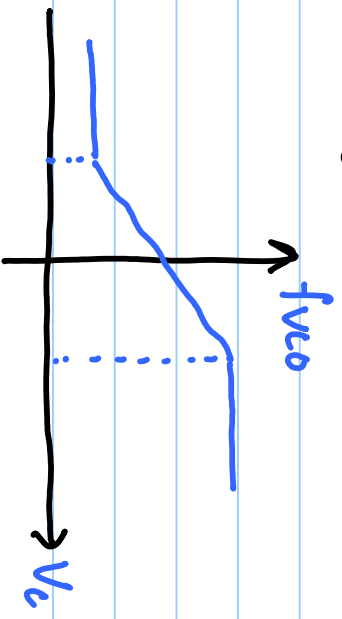
Basic PLL

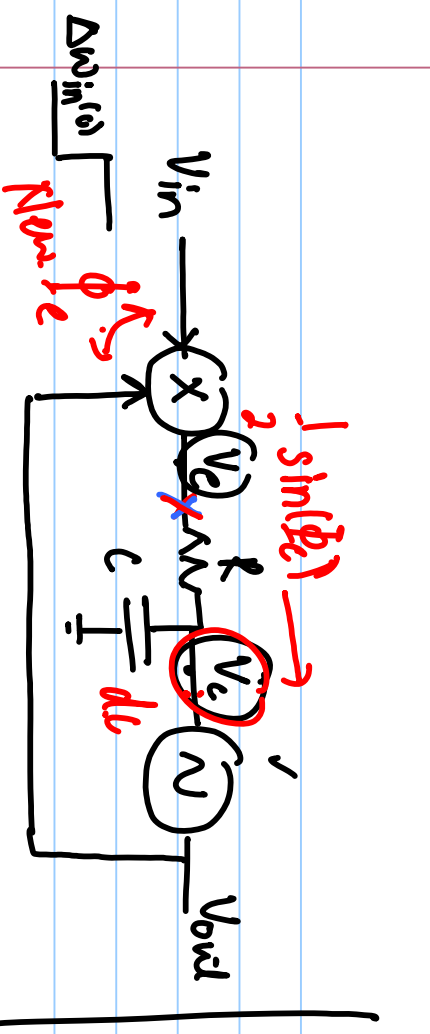


$\Delta\phi_{in}(0) u(t), \Delta\omega(0) u(t), \Delta\omega(0) \cdot t u(t)$

$|V_c = \frac{1}{2} \sin(\phi_c)| \leq \frac{1}{2}$

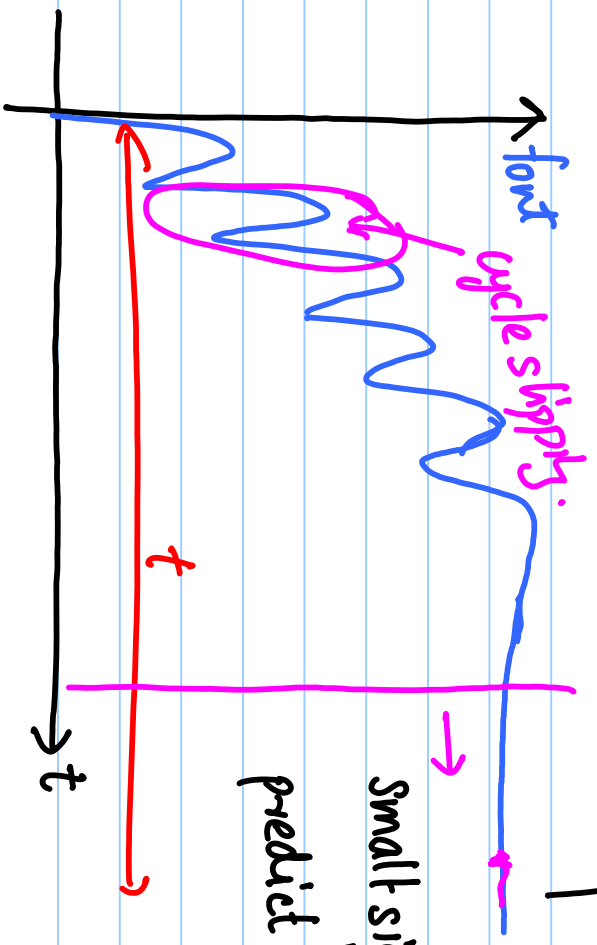
$V_c \leq V_e$





$$F(s) = \frac{1}{1+sRC} \quad \checkmark$$

$$F(s) = \frac{(1+s\tau_p)}{s\tau_i} \quad \checkmark$$



small signal model of PLL can't predict the large signal setting of PLL.

Ex. $\omega_{min} = 1 \text{ GHz}$.

$$\omega_{out} = \omega_{free} + K_{vco} \cdot V_c$$

$$K_{vco} = 2\pi \times 100 \text{ Mrad/s}$$

at $t=0$, $V_c=0$, $\omega_{out} = \omega_{free} = \underline{0.9995 \text{ GHz}}$.

at $t=0$, $\Delta\omega = \omega_{in} - \omega_{out} = 5 \text{ MHz}$.

$$v_{in} = \sin(\omega_{in} \cdot t)$$

$$v_{out} = \sin(\omega_{out} \cdot t + \phi_{out}(0))$$

$$\phi_{in}(t) = \omega_{in} \cdot t$$

$$\phi_{out}(t) = \int \omega_{out} dt = \int \omega_{free} + K_{vco} \cdot v_c \cdot dt$$

$$= \omega_{free} \cdot t + K_{vco} \cdot \int v_c \cdot dt$$

$$\phi_{err}(t) = \phi_{in}(t) - \phi_{out}(t)$$

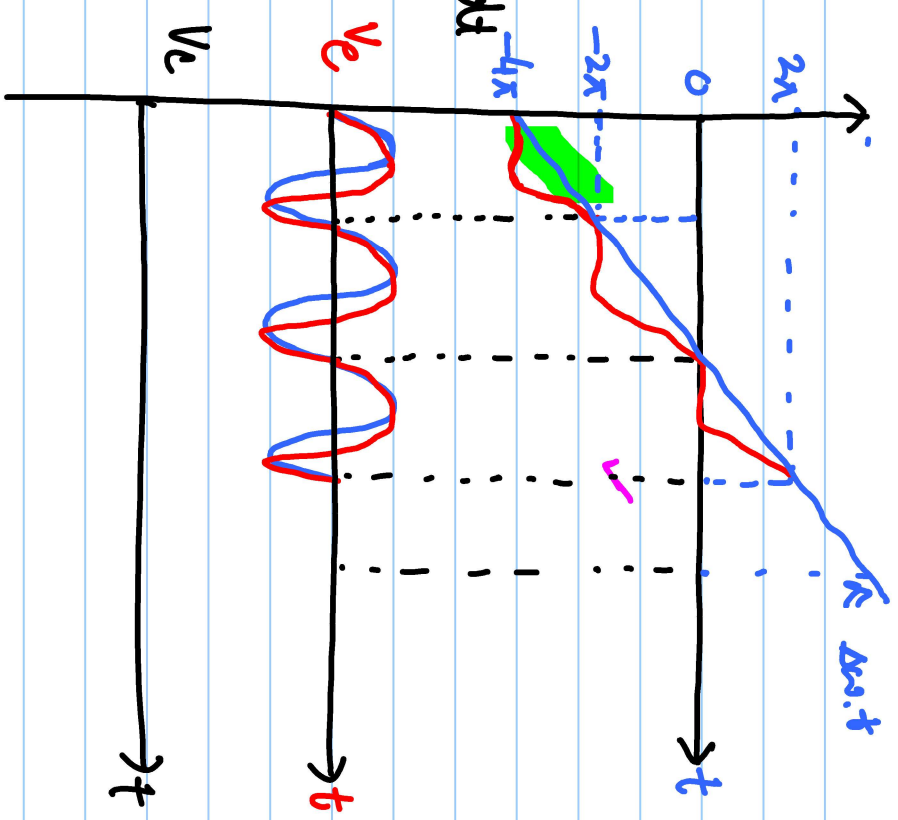
$$= \Delta\omega \cdot t - K_{vco} \int v_c \cdot dt$$

ϕ_{err} / loop

$$v_c(t) = \frac{1}{2} \sin(\phi_{err}) + \frac{1}{2} \sin(\omega_{in} t + \omega_{out} t + K_{vco} \int v_c \cdot dt)$$

$$\phi_{err}(t) = \Delta\omega \cdot t - K_{vco} \int v_c \cdot dt$$

$$= \Delta\omega \cdot t - K_{vco} \cdot K_{pd} \int \sin(\phi_{err}) dt$$

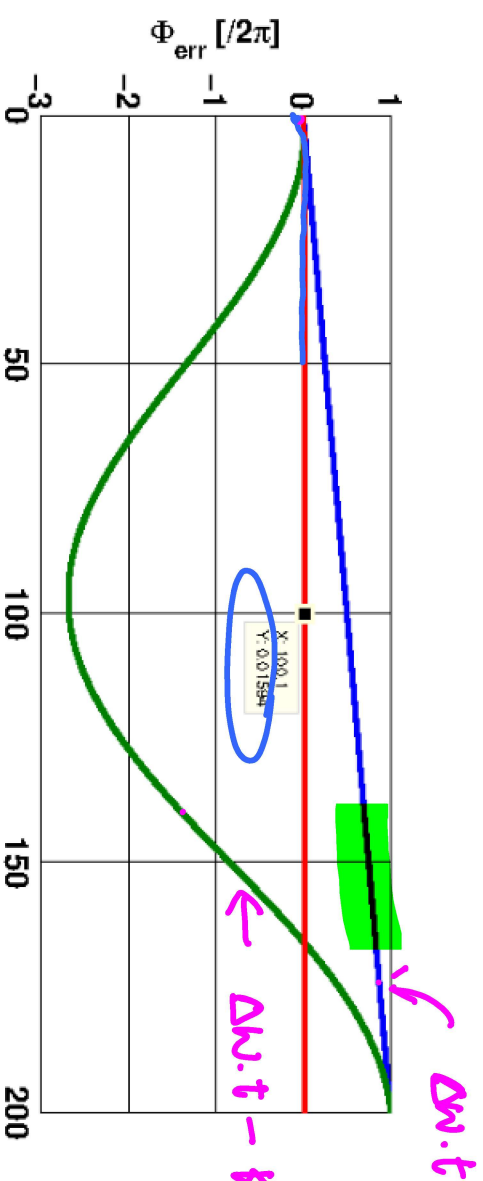


Assume v_c is passed as much to v_c

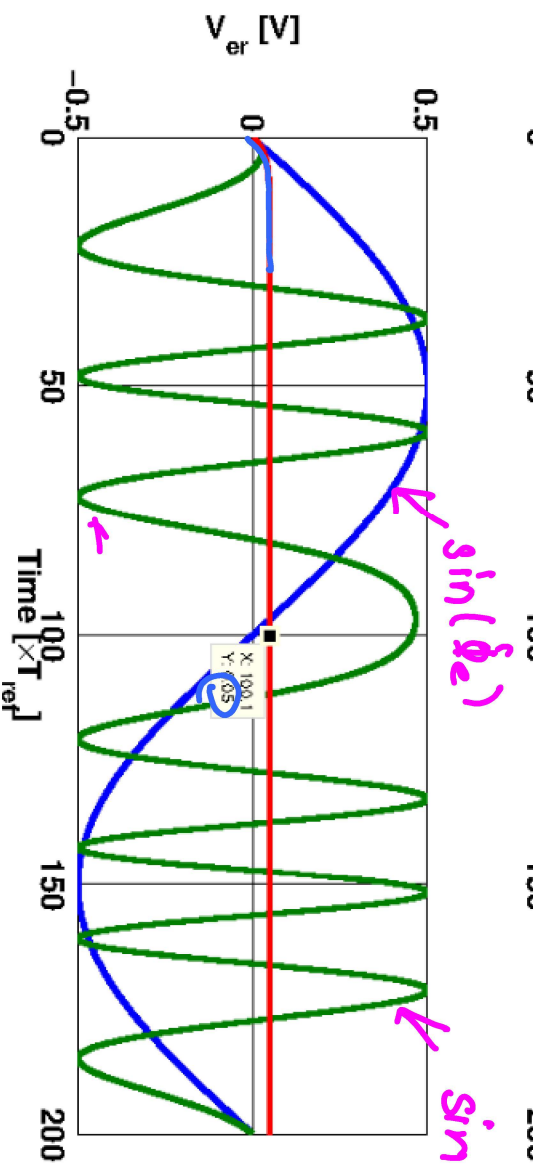
$$= \Delta\omega \cdot t - K_{VCO} K_{PD} \int_0^t \sin(\Delta\omega \cdot t) dt$$

$$= \Delta\omega \cdot t - \frac{K_{VCO} \cdot K_{PD}}{\Delta\omega} (1 - \cos(\Delta\omega \cdot t))$$

$$0.01594 \times 2\pi$$

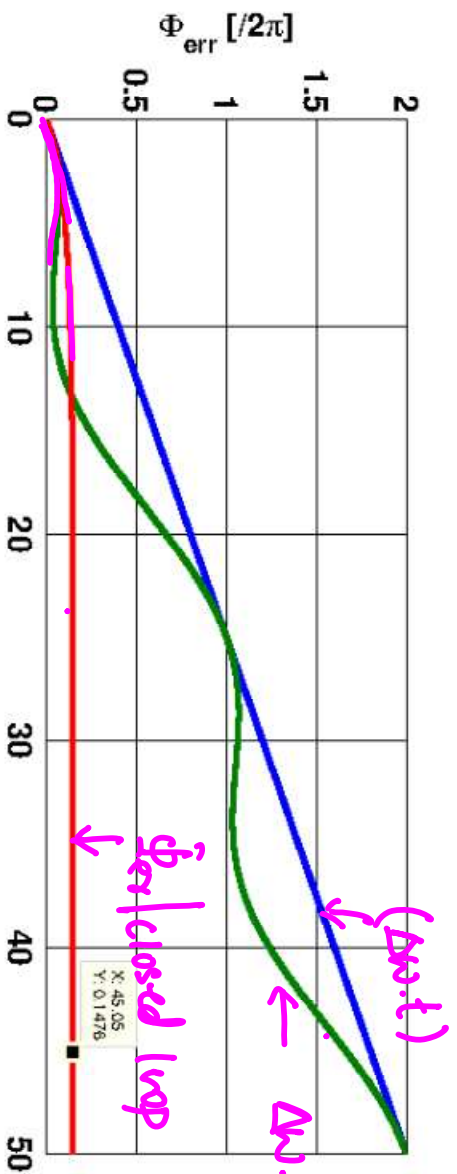


$$\Delta\omega \cdot t - K_{VCO} K_{PD} \int \sin(\phi_e) dt = \phi_{e, rms}$$

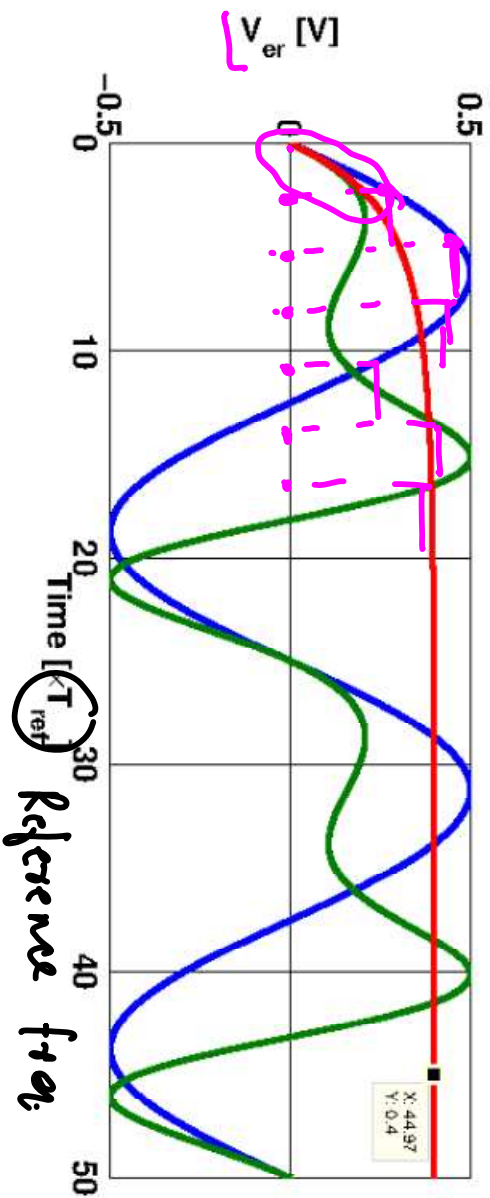


$$100 \text{ MHz/V} \times 0.05$$

$$= 5 \text{ MHz}$$



$$\Delta\omega \cdot t - K_{VCO} \cdot K_{FD} \int \sin(\Delta\omega \cdot t) dt$$



$$= \int_0^{T_S} V_C dt$$

$$= V_C(0) \cdot T_S$$

at $t = T_S$

$$\Phi_C(T_S) = \Delta\omega \cdot T_S - K_{VCO} \int_0^{T_S} V_C(t) dt$$

$$V_C(T_S) =$$

$$V_C(T_S) =$$

$$W_{in} = 1643$$

$$W_{free} = 0.9956$$

at 0 $V_C = 0, V_e = 0$

at $t = 2T_s$

$$\phi_e(2T_s) = \Delta\omega \cdot 2T_s - K_{VCO} \sum_{k=0}^1 V_c(kT_s) \cdot T_s$$

for $\Delta\omega = 5 \text{ MHz}$ $\xrightarrow{K_{VCO} = 100 \text{ MHz/V}}$ $V_c = 0.05 \text{ V}$ ✓

$\Delta\omega = 40 \text{ MHz}$ $\xrightarrow{\quad}$ $V_c = 0.4 \text{ V}$.

$\Delta\omega$

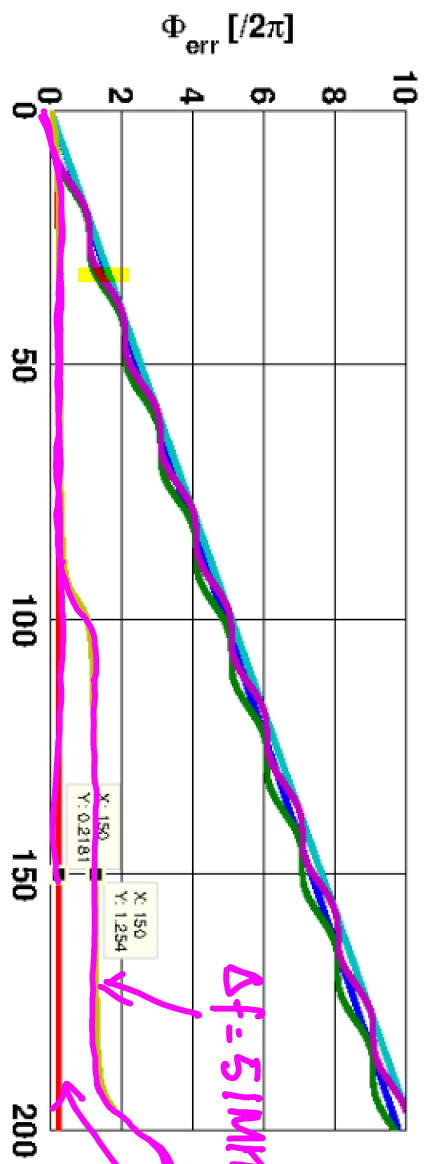
$$\left. \begin{aligned} &K_{VCO} = 2\pi \times 100 \text{ Mrad/V} \\ &\Rightarrow \frac{d\omega_{out}}{dV_c} = 100 \text{ MHz/V} \\ &\frac{d\omega_{out}}{dV_c} = 2\pi \times 100 \text{ Mrad/V} \end{aligned} \right\}$$

$$|V_c| < \frac{1}{2}$$

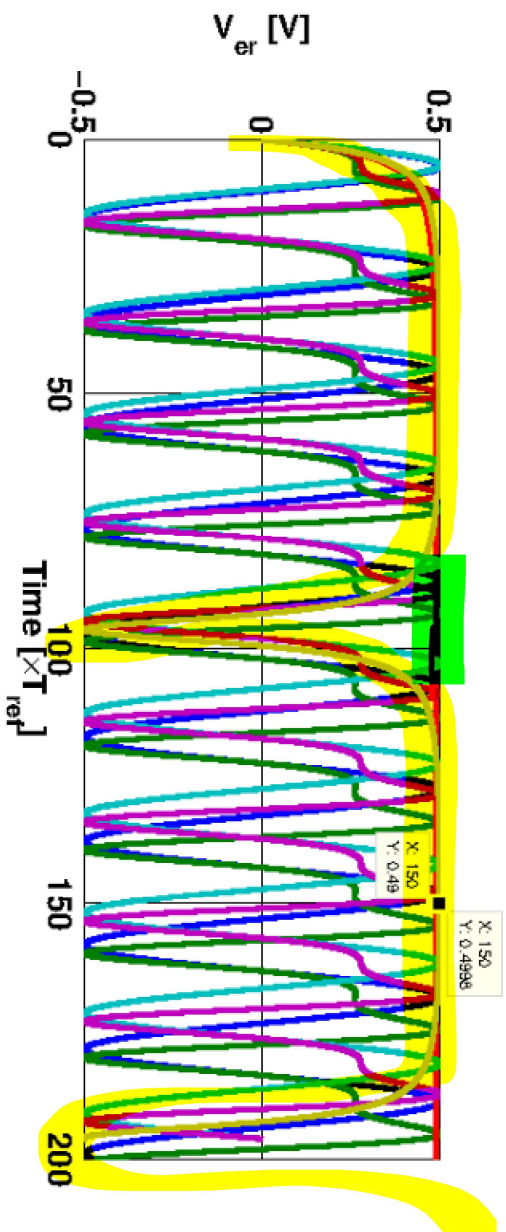
$$V_c = V_e$$

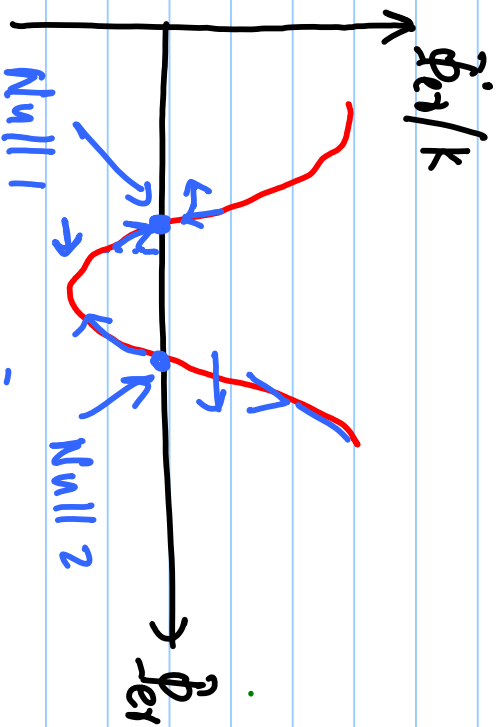
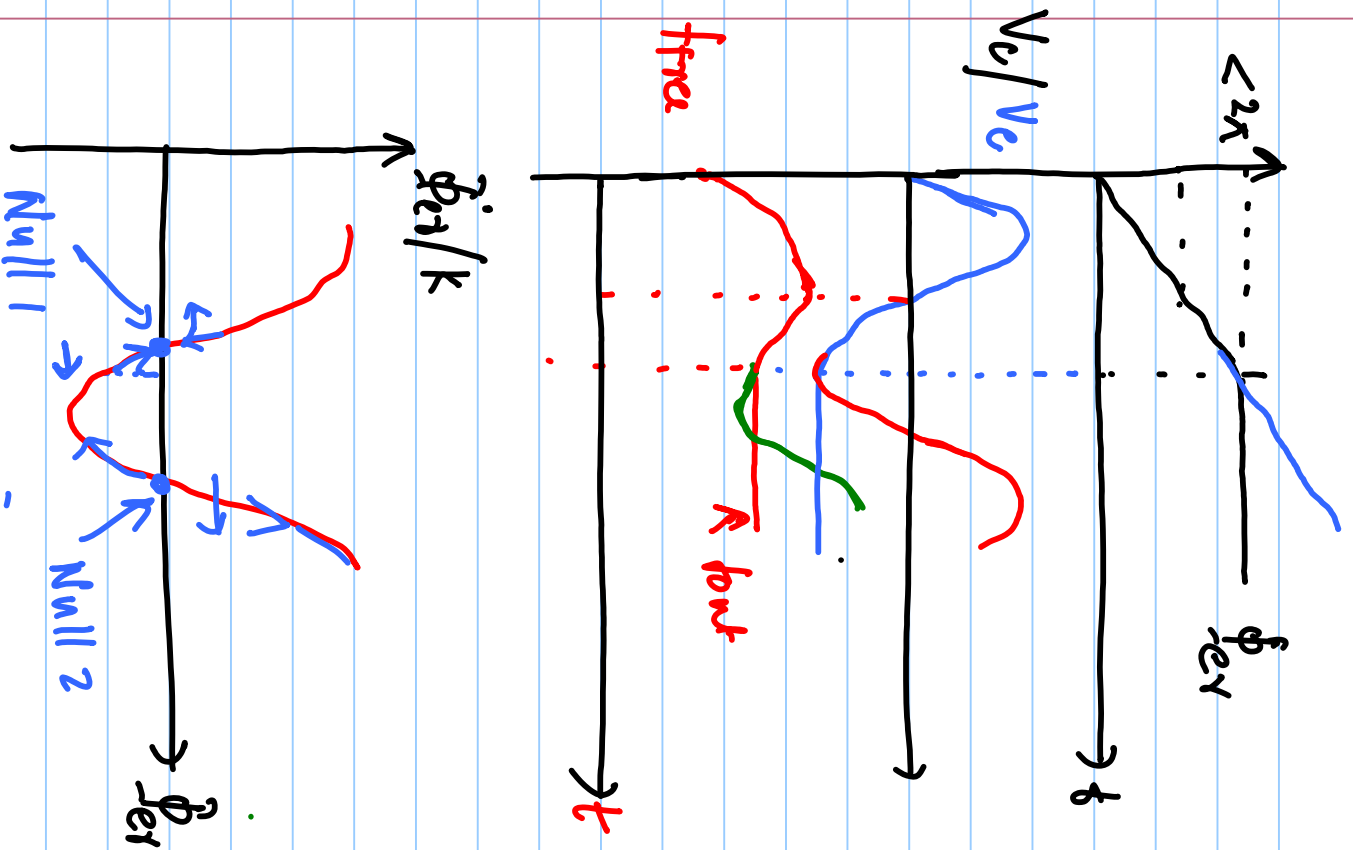
$$\Delta\omega = \frac{1}{2} K_{VCO} = \underline{50 \text{ MHz}}.$$

How PLL locks to frequency error before ϕ_e exceeds 2π ✓
if ϕ_e doesn't exceed 2π then V_c will not cycle slip.



$$\frac{d\phi_{err}}{dt} \neq 0$$





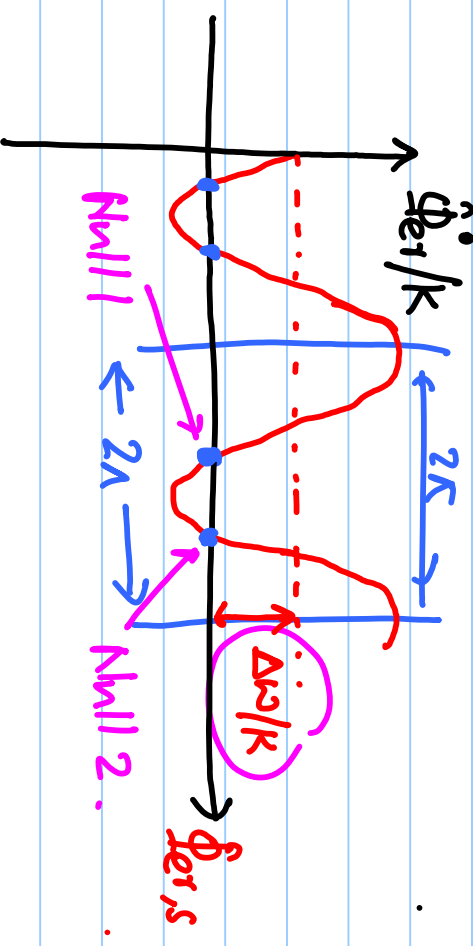
$$\Phi_{er}(t) = \Delta\omega \cdot t - K_{ve} \int V_c \, dt$$

$$= \Delta\omega \cdot t - K_{ve} \int K_{pd} \sin(\Phi_{er}) \, dt$$

$$\frac{d\Phi_{er}}{dt} = \Delta\omega - K_{ve} \cdot K_{pd} \sin(\Phi_{er})$$

$$= \Delta\omega - K \sin(\Phi_{er}) = 0$$

$$\dot{\Phi}_{er} = \frac{\Delta\omega}{k} - \sin(\Phi_{er}) \quad \checkmark$$



- For $\Delta\Phi_{er}$ towards right
 $\dot{\Phi}_{er} < 0 \Rightarrow \Phi_{er}$ will decrease.