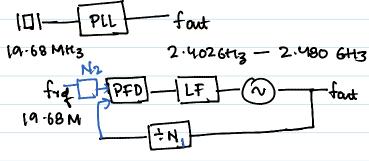
Fractional -N PLL.

Clock Source fout = 2.402 GHz + Rx 1 MHz Coystal Source, fory = 19.68 MHz



$$N = \frac{2 \cdot 402 \times 10^9}{19.68 \times 10^6} = 122.08 - 1$$

$$\frac{\text{fret}}{\text{N}_2} = \text{free}$$
 fowt = N,x free
 $\frac{\text{free}}{\text{N}_2} = \frac{19.68 \, \text{M}}{\text{N}_1}$, fowt = 2.402 6Hz , $\Delta F = \frac{1}{\text{M}_1}$ Hz.
 $\frac{\text{N}_1 = 60.050}{\text{N}_2} = \frac{492}{\text{M}_2} \Rightarrow \frac{1}{\text{free}} = \frac{40.5 \, \text{M}_2}{\text{M}_2}$

$$N_1 = 60050 + 25 \text{ Mz}$$

$$R = 0, 1, 2, 3 - - \cdot$$

$$\Delta F = D_1 IM_1 2M_1 - \cdot$$

Remarks!

- Small fref > lower bandwidth > briger VCO phase noise - Channel Switching time increases

fractional -divider

