

$$\text{Total Integrated Noise} = \int S_{\text{total}}(f) df \quad [\text{rad}^2]$$

$$\text{R.M.S Jitter [UI]} = \frac{1}{2\pi} \sqrt{\int S_{\text{total}}(f) df} \quad [\text{rad}]$$

$$S_{\text{total}} = \sum_i S_i |NTF_i|^2 \quad ; \quad i \text{ is the } i^{\text{th}} \text{ noise source.}$$

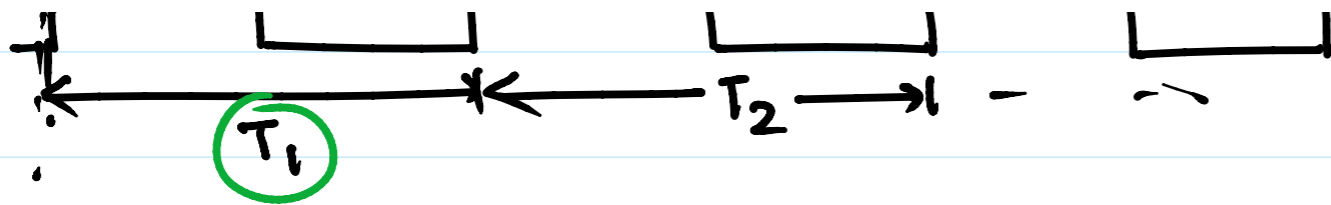
$$\text{R.M.S Jitter [s]} = \frac{T}{2\pi} \sqrt{\int S_{\text{total}}(f) df}$$

Jitter : performance of a clock (timing)



Period Jitter





$$\text{Mean value of period} = \frac{1}{N} \sum_{i=1}^N T_i = T_p$$

$$\text{Period Jitter (mean square)} = E[(T' - \bar{T})^2] = E[(\Delta t_p)^2]$$

T' : time period measured

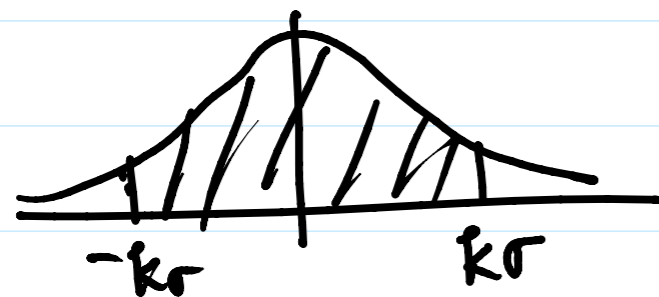
$$\Delta t_p[i] = T_i - T$$

$$\text{R.M.S period jitter } (\sigma) = \sqrt{E[(\Delta t_p)^2]}$$

$$N=1000, \sigma_m$$

$$N=10000$$

$$P(|\Delta t_p| \leq k\sigma) = \frac{2}{\sqrt{2\pi}} \frac{1}{\sigma} \int_0^{k\sigma} \exp\left(-\frac{x^2}{2\sigma^2}\right) dx$$



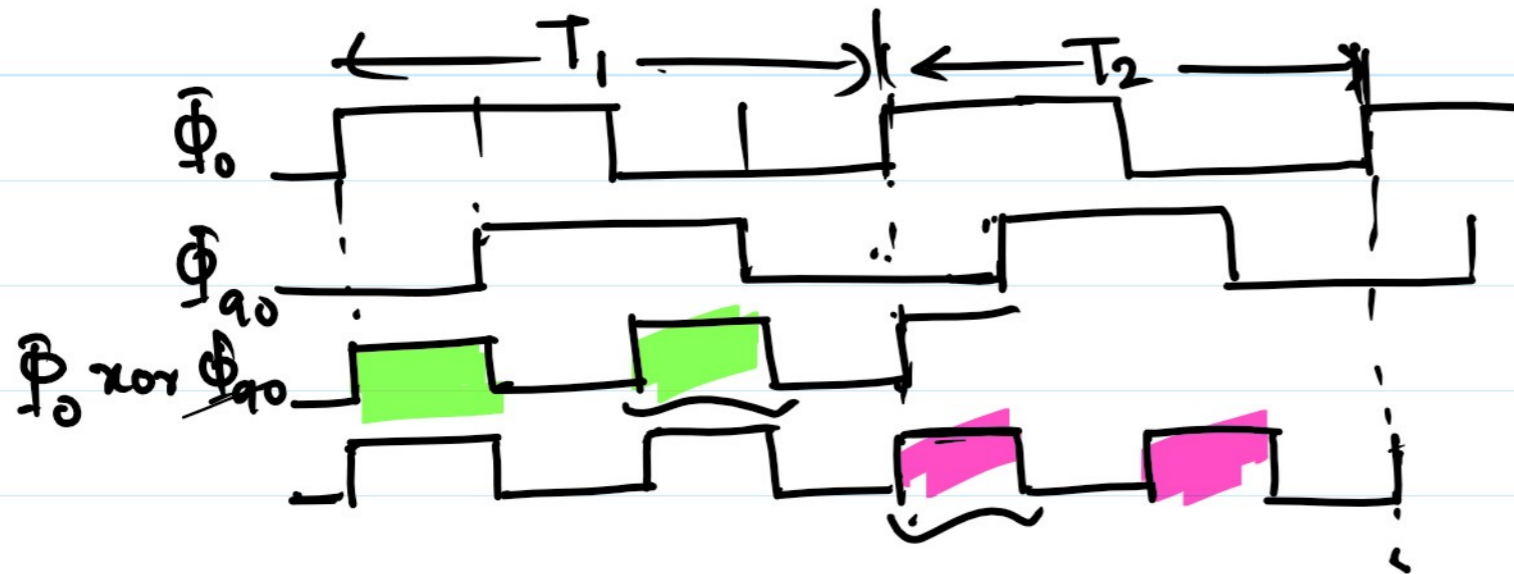
$$= \text{erf}\left(\frac{k}{\sqrt{2}}\right)$$

$$\text{For } P(|\Delta t_p| \leq 3\sigma) = \text{erf}\left(\frac{3}{\sqrt{2}}\right) = 0.9973$$

$$BER = 10^{-12} \rightarrow \text{Noise, } (V_n) > T\sigma$$

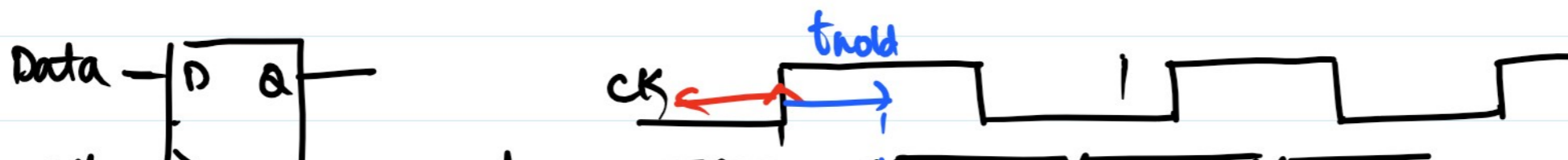
$$BER = 10^{-9} \Rightarrow k \rightarrow \sigma$$

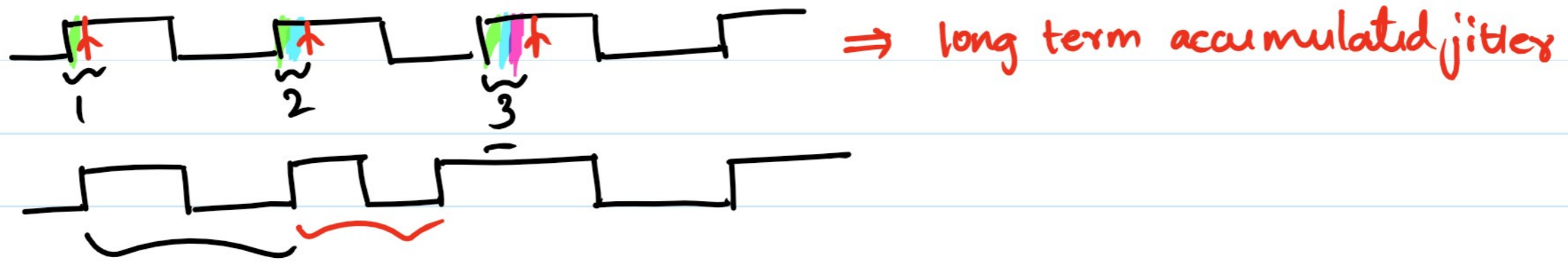
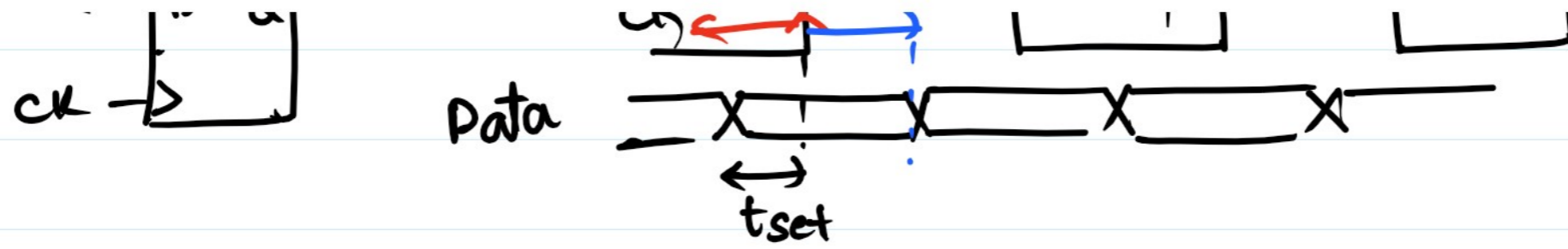
$$\left. \begin{array}{l} N = 1000 \\ \rightarrow \sigma_m \text{ (measured)} \end{array} \right\} \pm 3\sigma \Rightarrow \text{Peak-to-peak jitter} = 6\sigma_m \text{ } (\pm 3\sigma)$$



PWM equalization.

Ex. Period jitter \rightarrow Setup/hold time violations in digital circuits.





2) Cycle-to-cycle Jitter



$$\Delta t_{cc}[i] = T_i - T_{i-1}$$

Peak Cycle-to-cycle jitter = $\max. |T_i - T_{i-1}| ; i = 1, 2, \dots$

Spread spectrum clocking:

