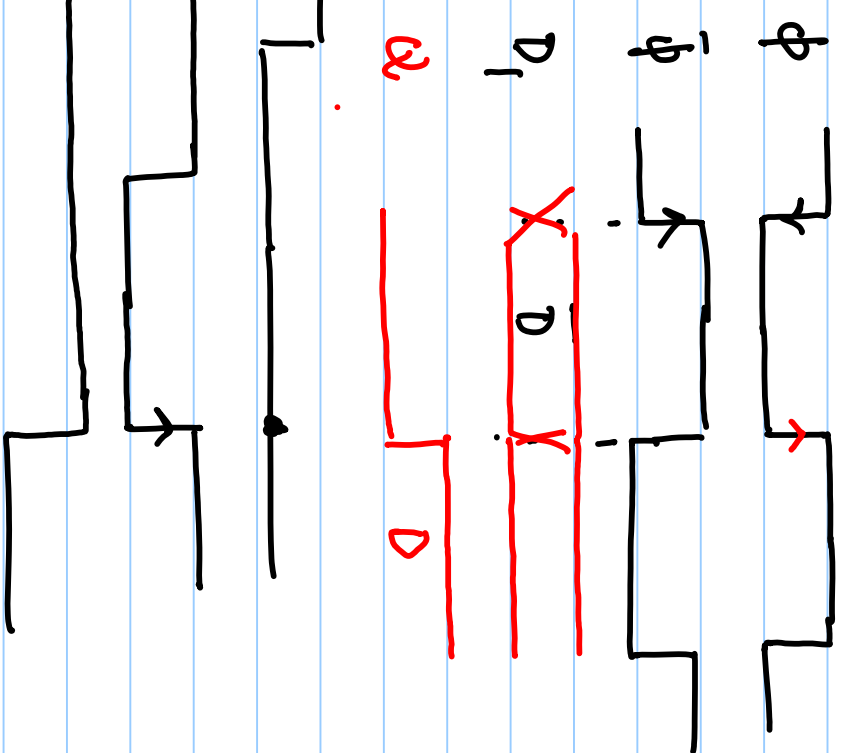
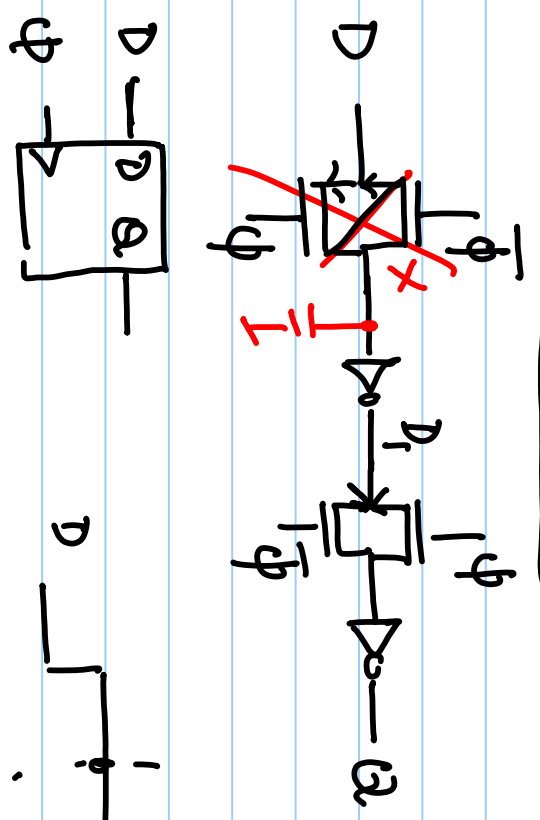
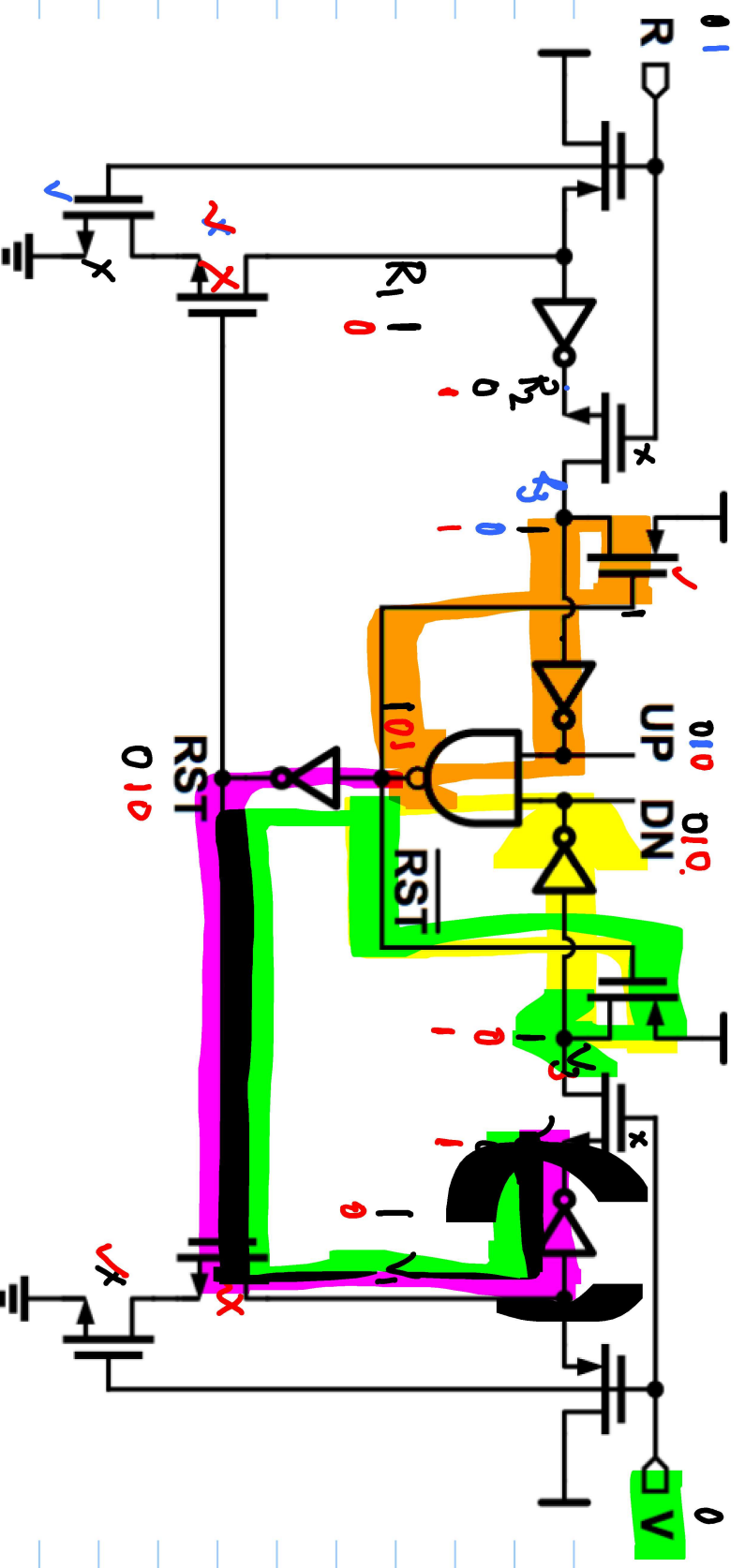


lecture #37

Pass-transistor PFD





$$T_{RST} = 2T_{nand2} + T_{inv}$$

$$T_{RST} = 2T_{nand2} + T_{nand4} \quad (\text{Nand PFD})$$

Glitch-latch ff

