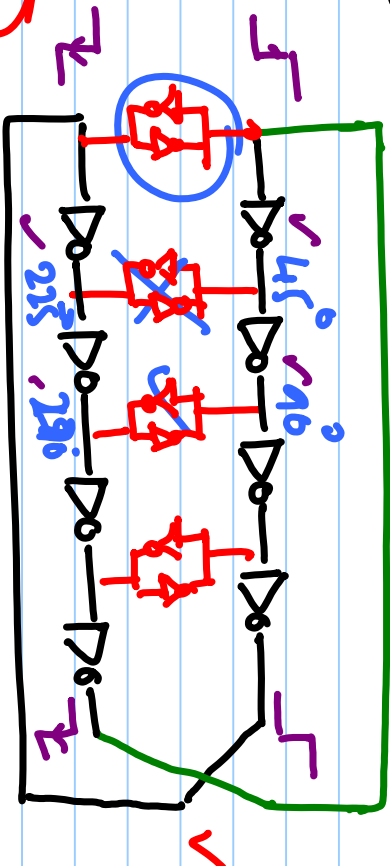
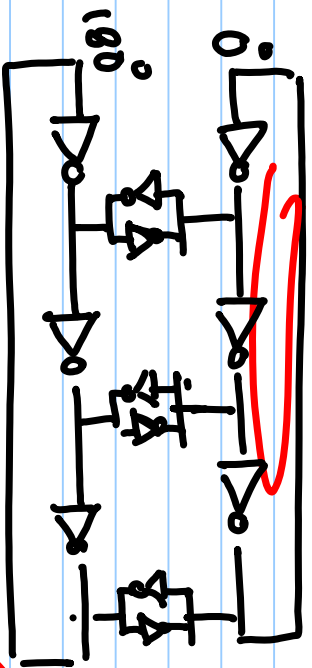


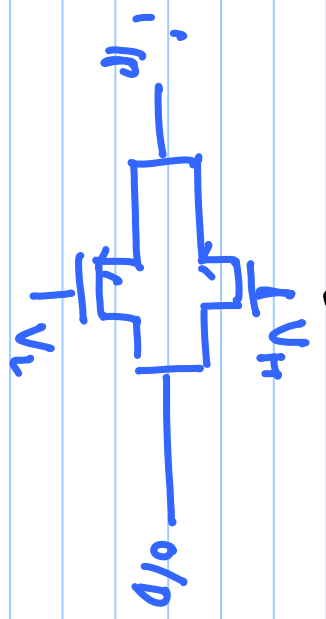
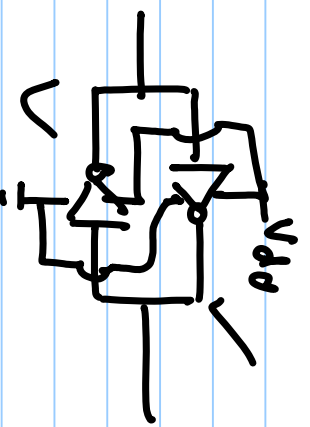
Lecture # 35

Large-swing ring oscillators

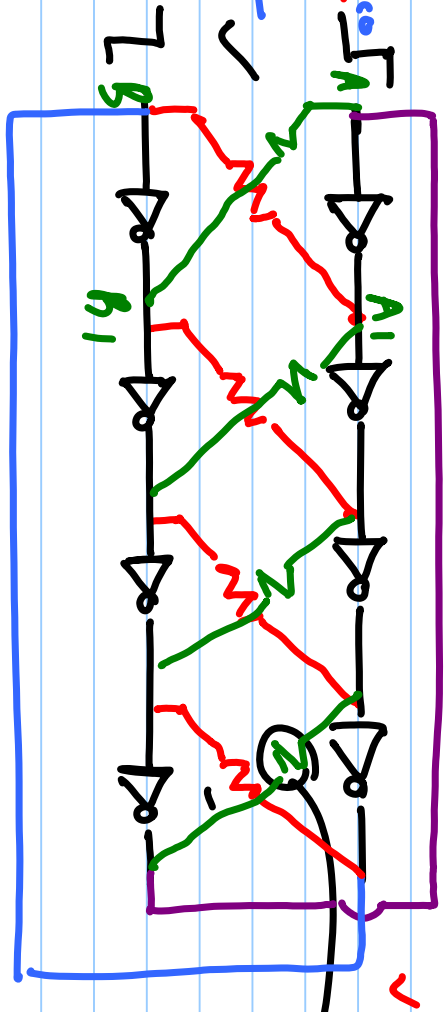


0, 90°, 180°, 270°

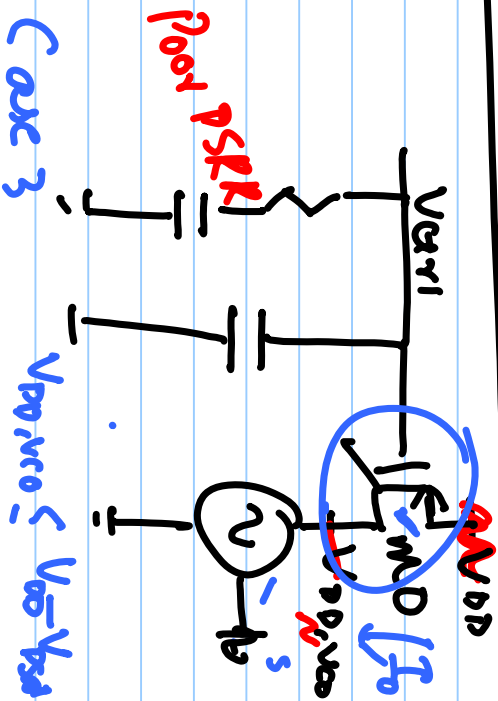
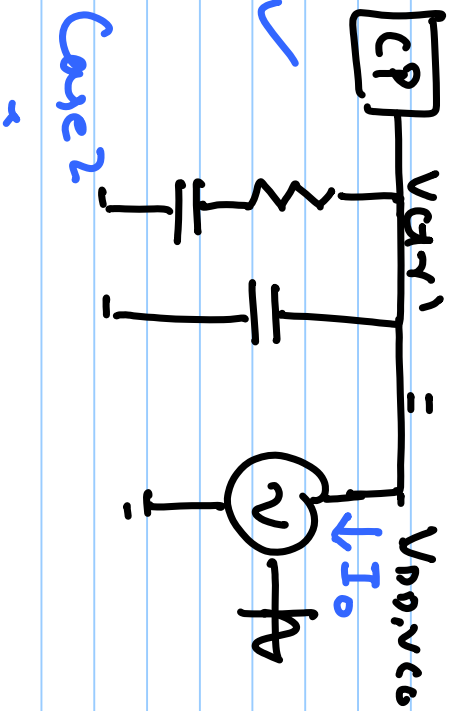
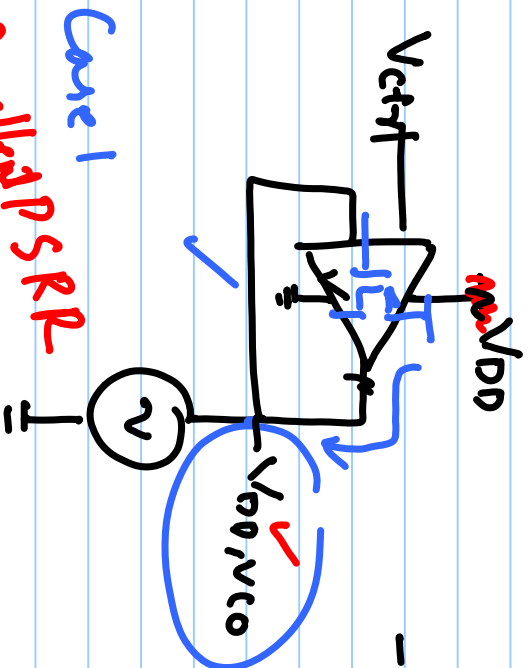
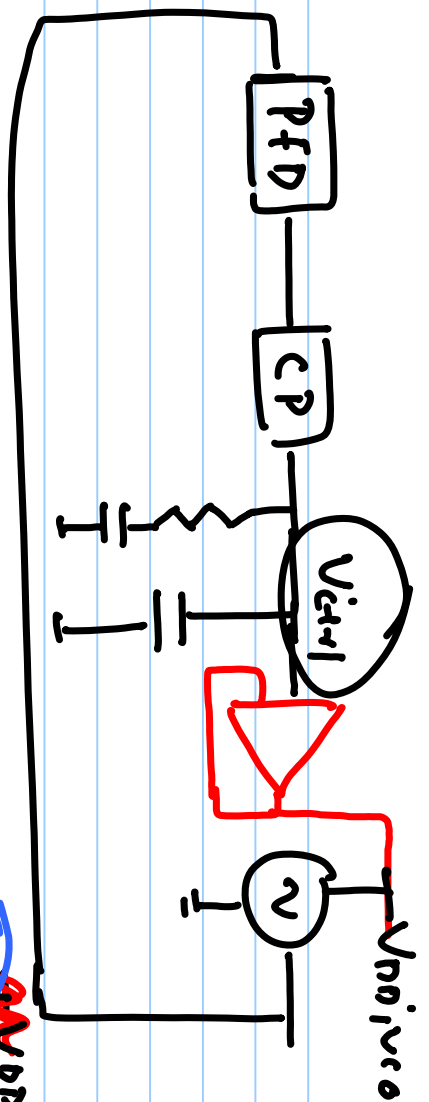
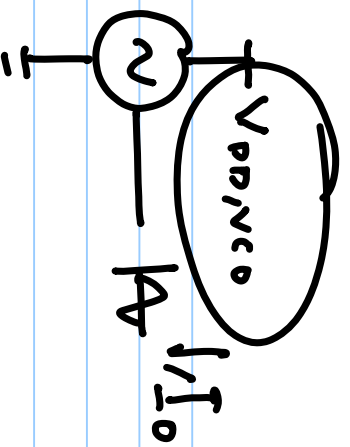
(SUNN)



$V_L = 0$
 $V_H =$

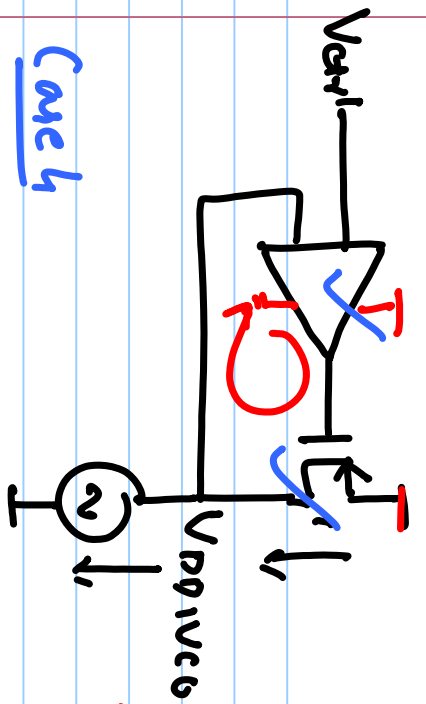


Poly res.
Transmission gate

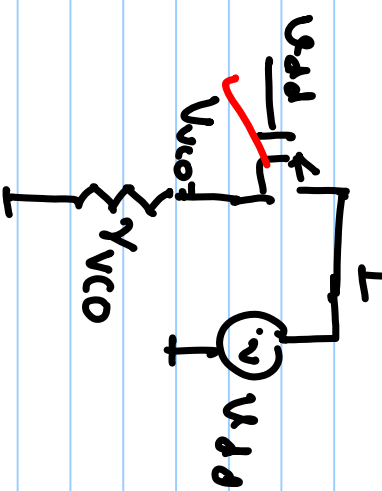
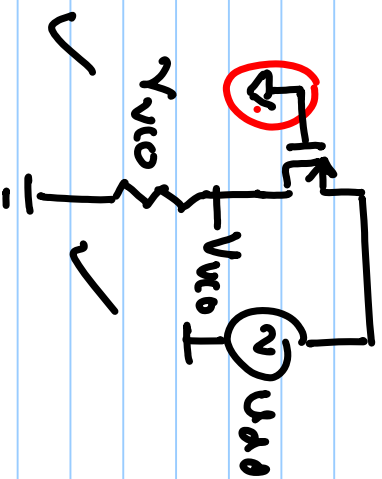
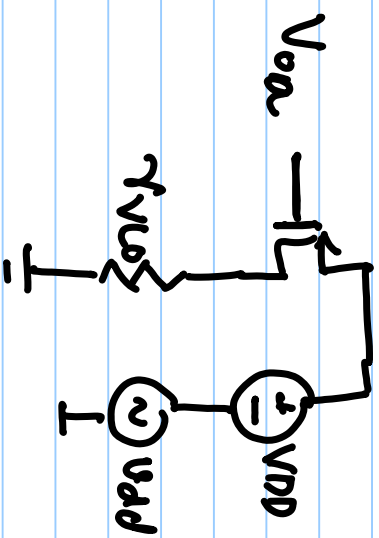
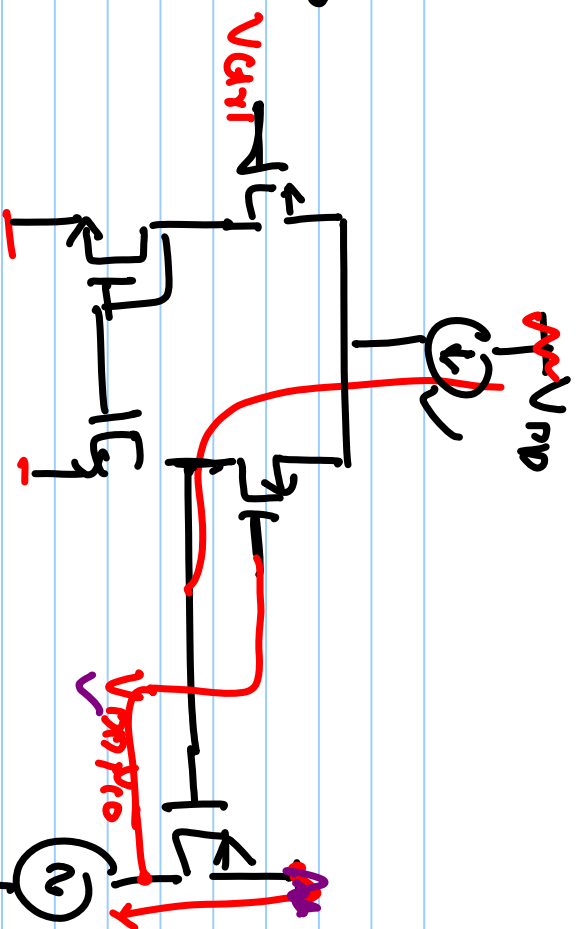


Case 1
Excellent PSRR
- $f_{vco} = K_{vco} \cdot V_{DQ/VCO}$

Case 3
 $I_0 = g_m V_{ctrl}$
 $f_{vco} = K_{vco} \cdot I_0$

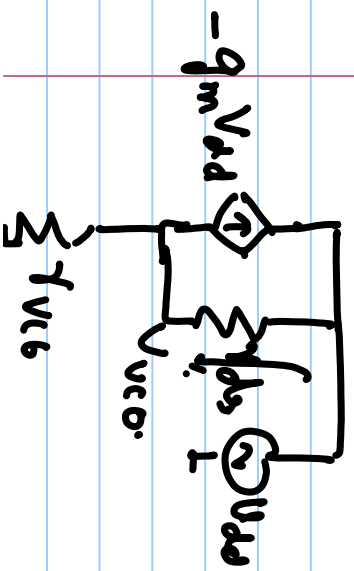
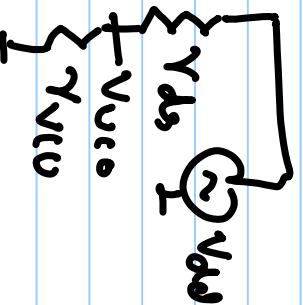


Case 4



$$V_{DQ/V_{CE}} = \frac{V_{in1}}{V_{DD}}$$

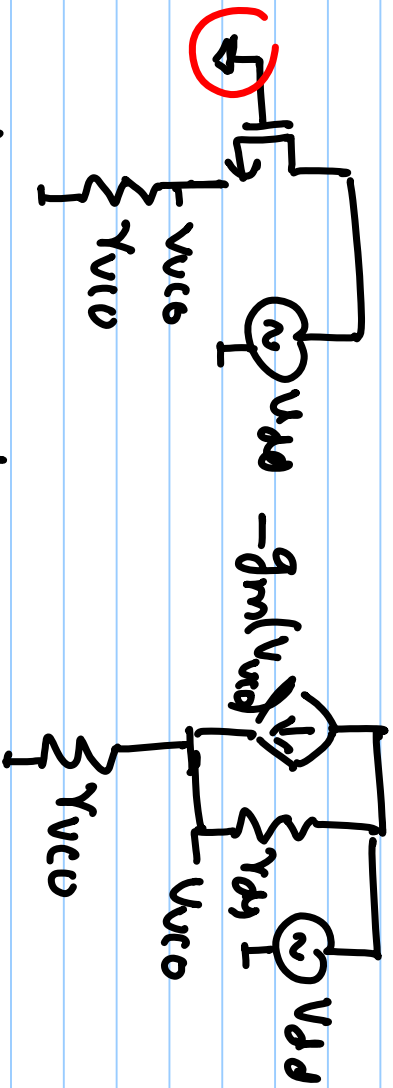
$$V_{DQ/V_{CE}} = \frac{V_{in1}}{V_{DD}}$$



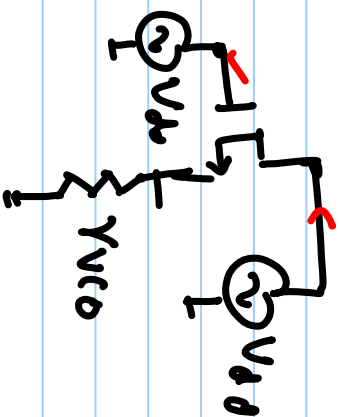
$$(V_{DD} - V_{V_{IO}}) \frac{1}{r_{ds}} + g_m V_{DD} = \frac{V_{V_{IO}}}{r_{V_{IO}}} \Rightarrow V_{DD} \left(\frac{1}{r_{ds}} + g_m \right) = V_{V_{IO}} \left(\frac{1}{r_{ds}} + \frac{1}{r_{V_{IO}}} \right)$$

$$\frac{V_{V_{IO}}}{V_{DD}} = \frac{\left(\frac{1}{r_{ds}} + g_m \right)}{\left(\frac{1}{r_{V_{IO}}} + \frac{1}{r_{ds}} \right)} \approx g_m r_{V_{IO}} \quad \text{if } g_m \gg \frac{1}{r_{ds}}$$

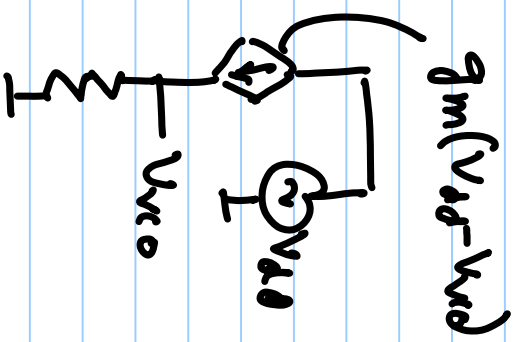
NMOS current source

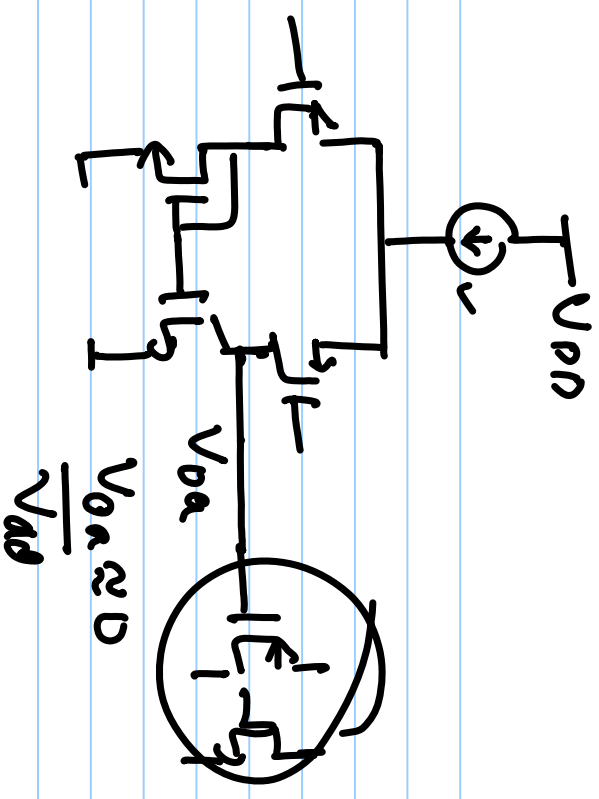
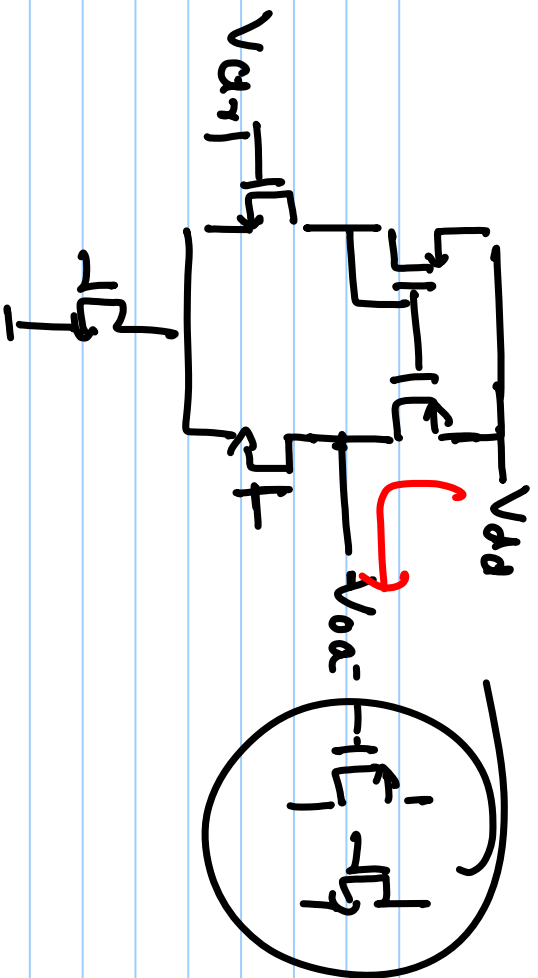


$$\frac{V_{V_{IO}}}{V_{DD}} = \frac{1}{g_m r_{ds}}$$



$$\frac{V_{V_{IO}}}{V_{DD}} = 1$$





a) PMOS i/p OPAMP + PMOS Current Source

b) + NMOS Current Source ✓

c) NMOS i/p OPAMP + PMOS Current Source ✓

NMOS Current Source

Supply-regulated VCD

