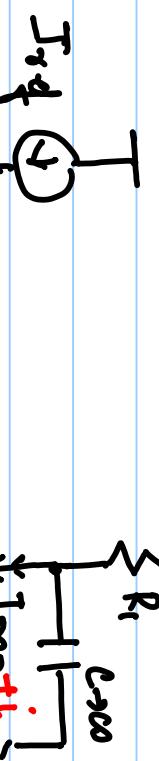


Lecture # 15

Note Title

02-09-2021



$$I_{DS1} = \frac{\mu_n C_{ox}}{2} \left(\frac{W}{L} \right)_1 (V_{th1} - V_{th1})^2 = I_{ref}$$

$$I_{DS2} = \frac{\mu_n C_{ox}}{2} \left(\frac{W}{L} \right)_2 (V_{th} - V_{th2})^2$$

$$\text{if } \left(\frac{W}{L} \right)_2 = \left(\frac{W}{L} \right)_1 \quad \text{and} \quad V_{th2} = V_{th1}$$

$$U_c: \frac{R_s}{\dot{I}_{DS2}} = \frac{R_s}{\dot{I}_{DS1}} = \frac{R_s}{\dot{I}_{ref}}$$

$$I_{DS2} = I_{DS1} = I_{ref}$$

$$\hat{V}_{th2} = V_{th1} + \Delta V_{th}$$

$$I_{DS2} = \underbrace{\frac{\mu_n C_{ox}}{2} \left(\frac{W}{L} \right)}_K (V_{th1} - V_{th1} - \Delta V_{th})^2$$

$$= \frac{k \omega}{2L} \left[(V_{th1} - V_{th1})^2 + (\Delta V_{th})^2 - 2 \Delta V_{th} (V_{th1} - V_{th1}) \right]$$

$$= I_{DS1} + \frac{k \omega}{2L} \left[-2(V_{th1} - V_{th1}) \cdot \Delta V_{th} + (\Delta V_{th})^2 \right]$$

$$= I_{ref} + \frac{k_w}{L} (V_{a1} - V_{th1}) \times (-\Delta V_{th}) + \frac{k_w}{2L} (\Delta V_{th}^2) \quad \checkmark$$

$$\approx I_{ref} + \frac{k_w}{L} (V_{a1} - V_{th1}) \times (-\Delta V_{th})$$

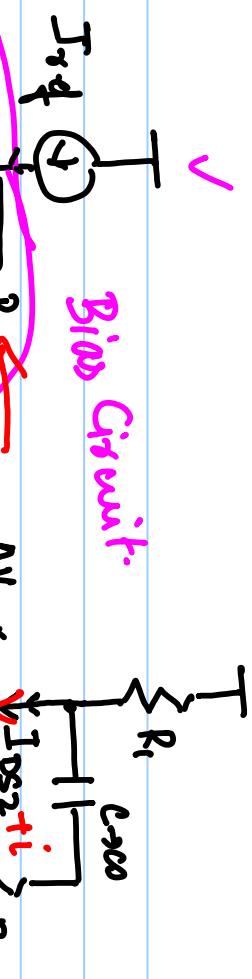
$$I_{DS2} = I_{ref} + g_m (-\Delta V_{th})$$

$$g_{m2} = \sqrt{2 I_{DS2} \mu_n C_o \frac{W}{L}} = \sqrt{(I_{ref} - g_m \Delta V_{th}) \mu_n C_o \frac{W}{L}}$$

$$V_{a2} - V_{th2} = V_{a1} - (V_{th1} + \Delta V_{th}) = (V_{a1} - V_{th1}) - (\Delta V_{th})$$

$$= (V_{a1} - \Delta V_{th}) - V_{th1}$$

$$\left(\frac{W}{L}\right)_1 = \left(\frac{W}{L}\right)_2$$



$$V_{th1} = V_{th2}$$

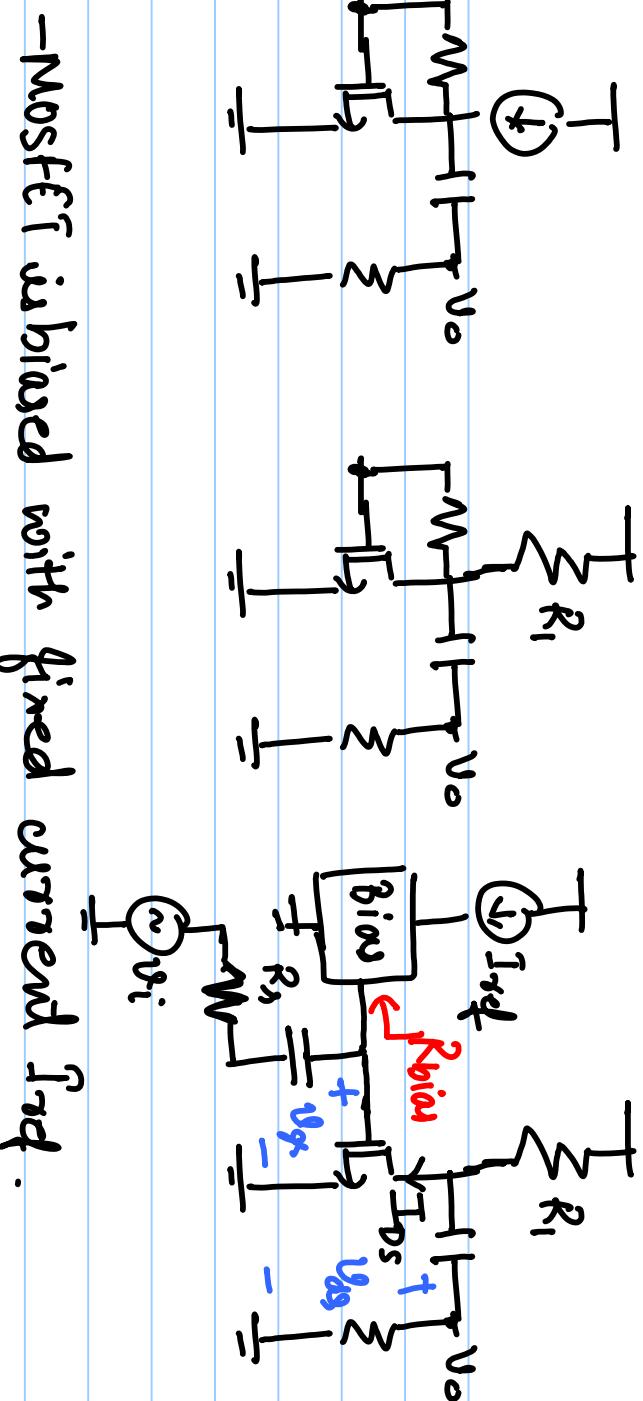
$$V_{th2} = V_{th1} - \Delta V_{th2}$$

if $\Delta V_{th2} = 0$ then $I_{D_s2} = I_{D_s1} + I_{ref}$

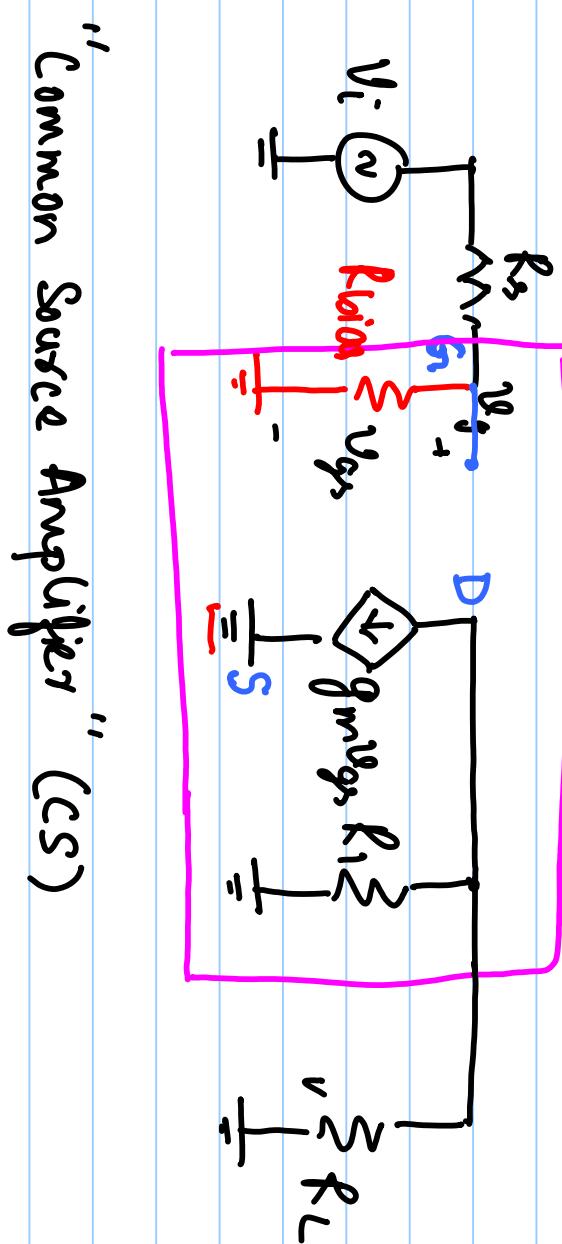
if $\Delta V_{th2} \neq 0$ then $I_{D_s2} = I_{D_s1} + g_{m2} (-\Delta V_{th2})$

$$J_{D_s2} = J_{ref} - g_{m2} \cdot \Delta V_{th2}$$

- Change in threshold voltage can be represented as change in gate voltage w/ -ve sign.



-MOSFET is biased with fixed current I_{ref} .

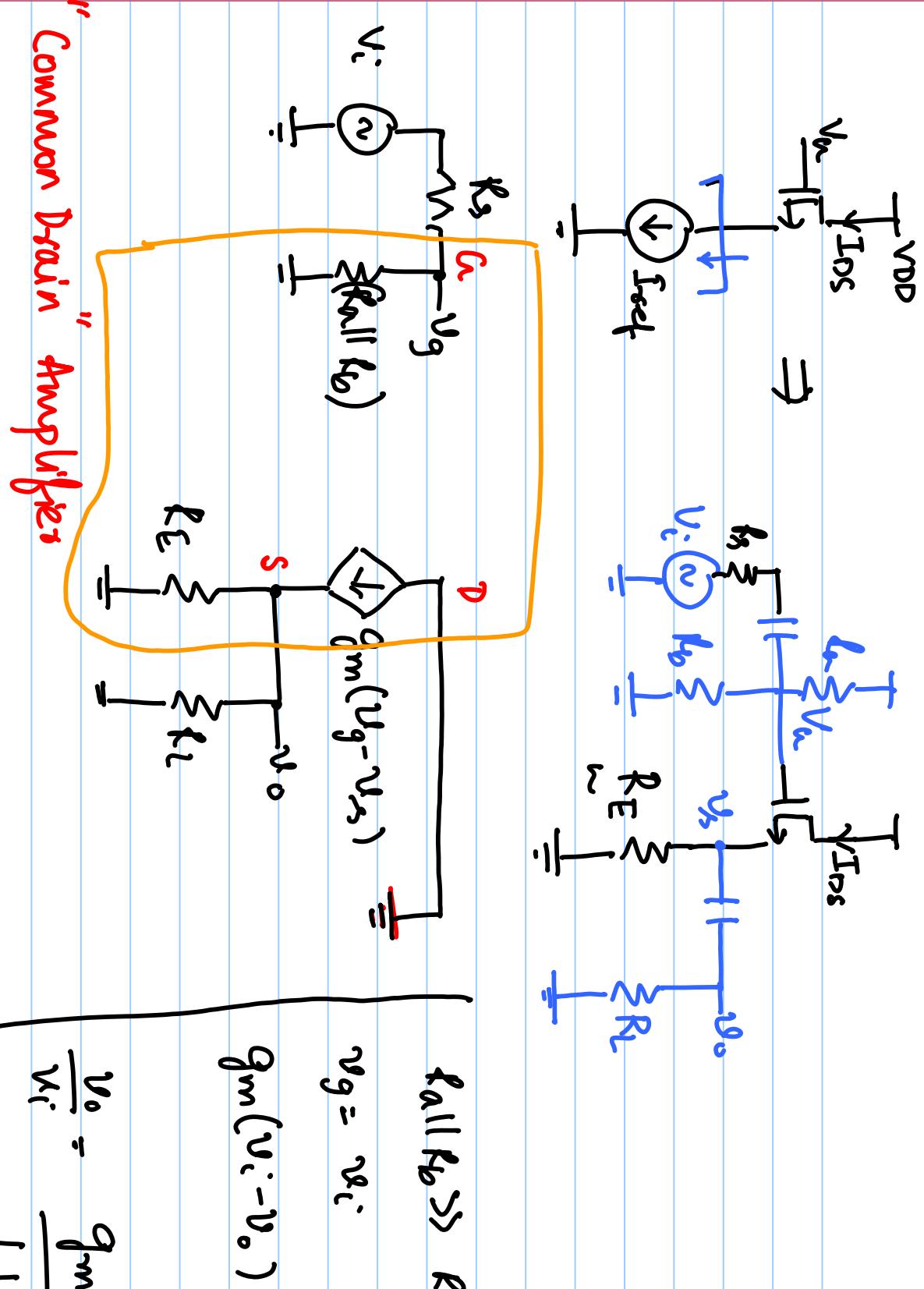


$$\frac{V_o}{V_i} = -g_m (R_L \parallel R_L)$$

$$g_m = \sqrt{\frac{2I_{ref}}{2L \mu_n W L}}$$

"Common Source Amplifier" (CSA)

-Gain depends on R_L .



$R_{\text{all}} \gg R_{\text{in}}$

$$w_9 = w_i$$

$$q_m(v_i - v_0) = \frac{v_0}{v_i}$$

$$\frac{V_0}{q_m} = R_E || R_L$$

$$V_i = \frac{1 + g_m(R_E || R_C)}{2}$$

$$\overline{v_0} = \left(P_E / |F| \right)$$

$$\frac{V_o}{V_i} = \frac{1}{1 + (R_E || R_C)}$$

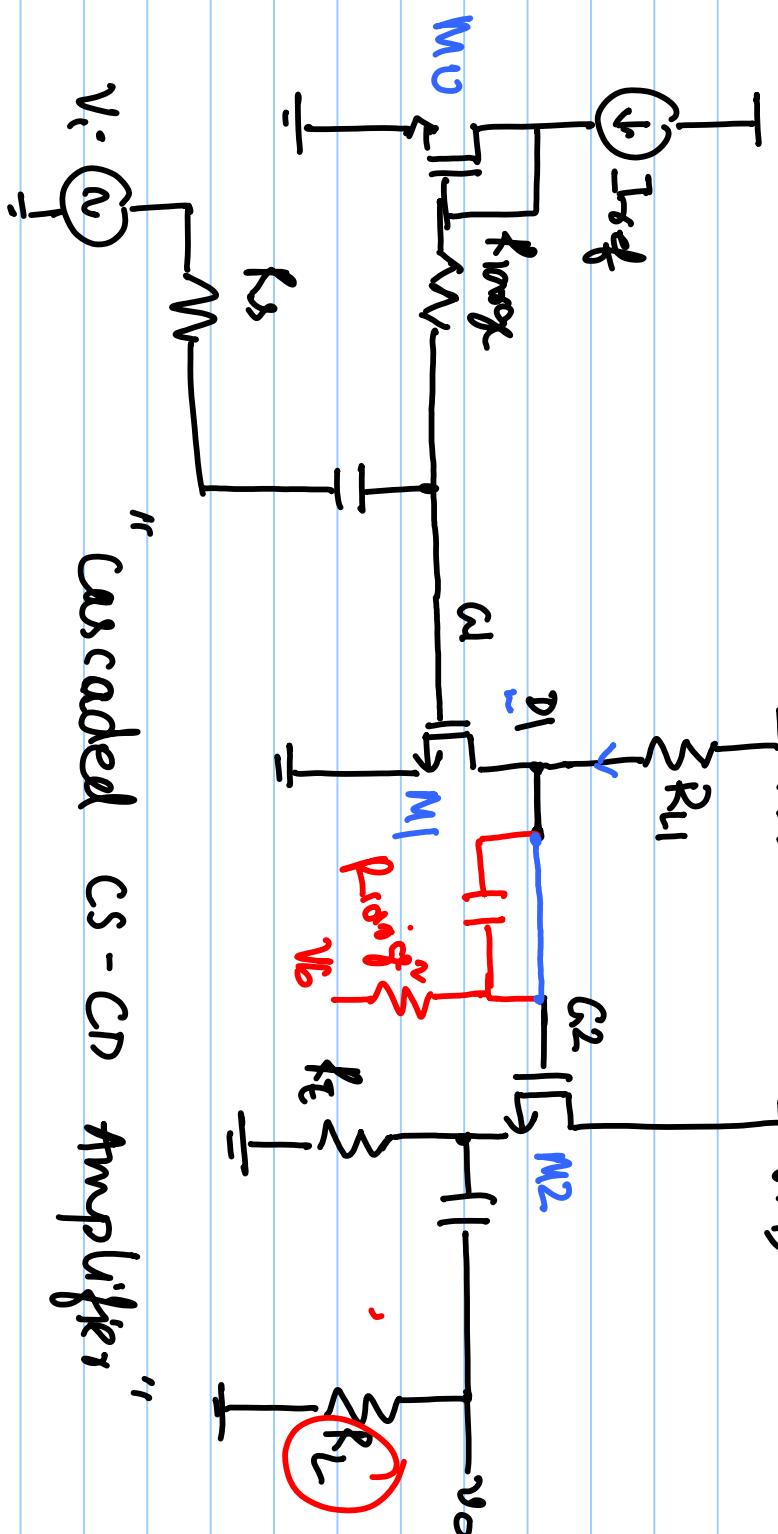
CS : Large gain, Gain depends on R_L ($A = -g_m(R_s \parallel R_L)$)

CD : Unity gain (≈ 1), Gain doesn't depend on R_L ($\frac{1}{g_m} \ll (R_s \parallel R_L)$)

Ideal Amplifiers

$$\text{For } \frac{V_o}{V_i} = \text{large and independent of } R_L$$

first-stage **second-stage**



$$\frac{v_0}{v_i} = - g_{m1} R_L + \frac{g_{m2} (R_E || R_L)}{g_{m2} (R_E || R_L) H}$$

$$\frac{v_0}{v_i} \approx - g_{m1} R_L$$