In a second

Stepper signals - MOSFETs on

Comparator current - Charging the load

Dynamic power - Subthreshold conduction

Diode - Leakage

C MOS Logic
\[ V_{\text{out}} = (1 - 2e^{-t/\tau_{\text{RC}}}) \cdot V_{\text{IR}} \]

\[ 1 - 2e^{-t/\tau_{\text{RC}}} = 0.9 \]

\[ e^{-t/\tau_{\text{RC}}} = 0.05 \]

Noise margin is reduced \[ 2 \] logic levels closer.

Static power - same at LF & HF.

More elaborate circuitry.