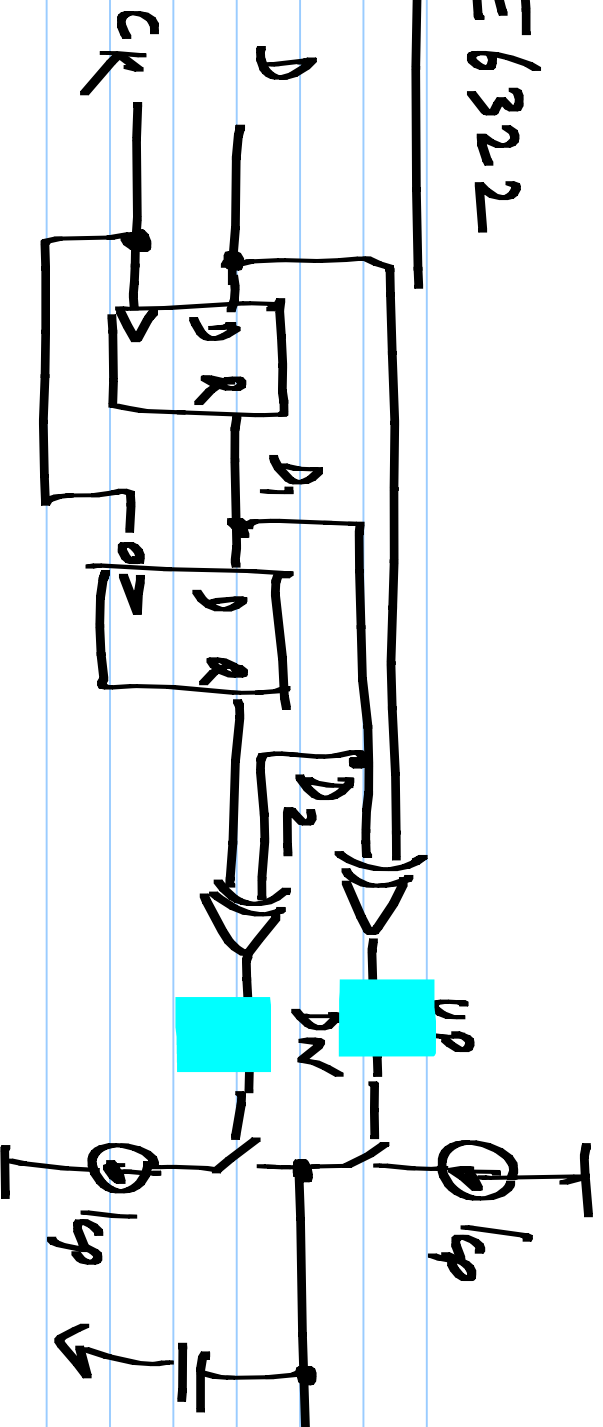


EE 6322

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ck - delay of the FF:

Up wider than it should be
DN same as "

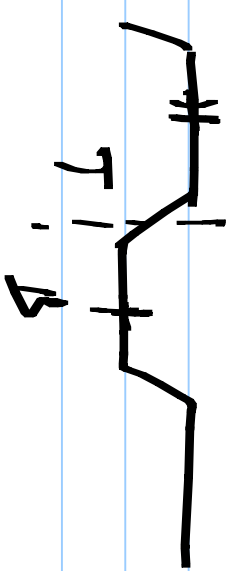
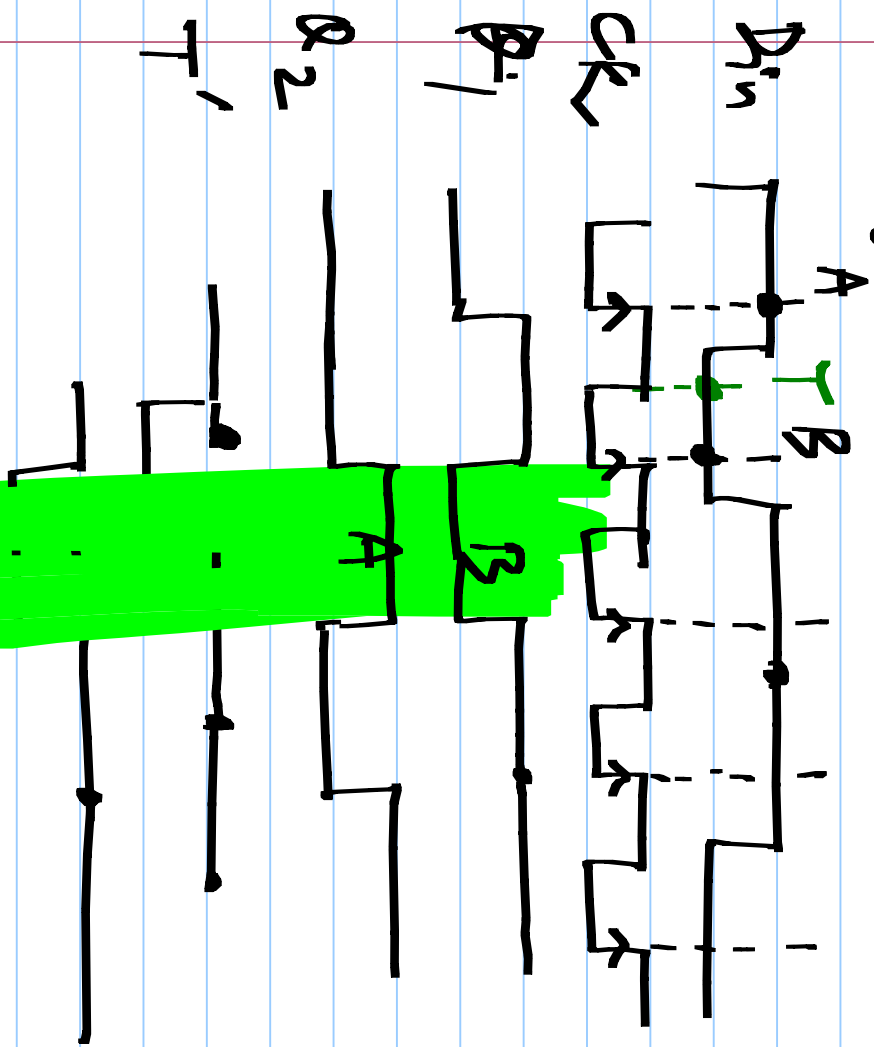
CDR settles to a skewed position

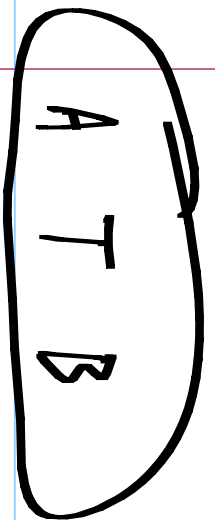
Bang-Bang phase detector

3 consecutive

samples $T/2$

apart





DN: 001, 110

$\bar{A}\bar{T}B + A\bar{T}\bar{B}$

0 0 0 No transition

0 0 1 Clock too early DN = $T \oplus B$

0 1 0 Shouldn't occur

0 1 1 Clock too late UP = $T \oplus A$

1 0 0 Clock too late UP

1 0 1 shouldn't occur

1 1 0 Clock too early DN

1 1 1 No transition

