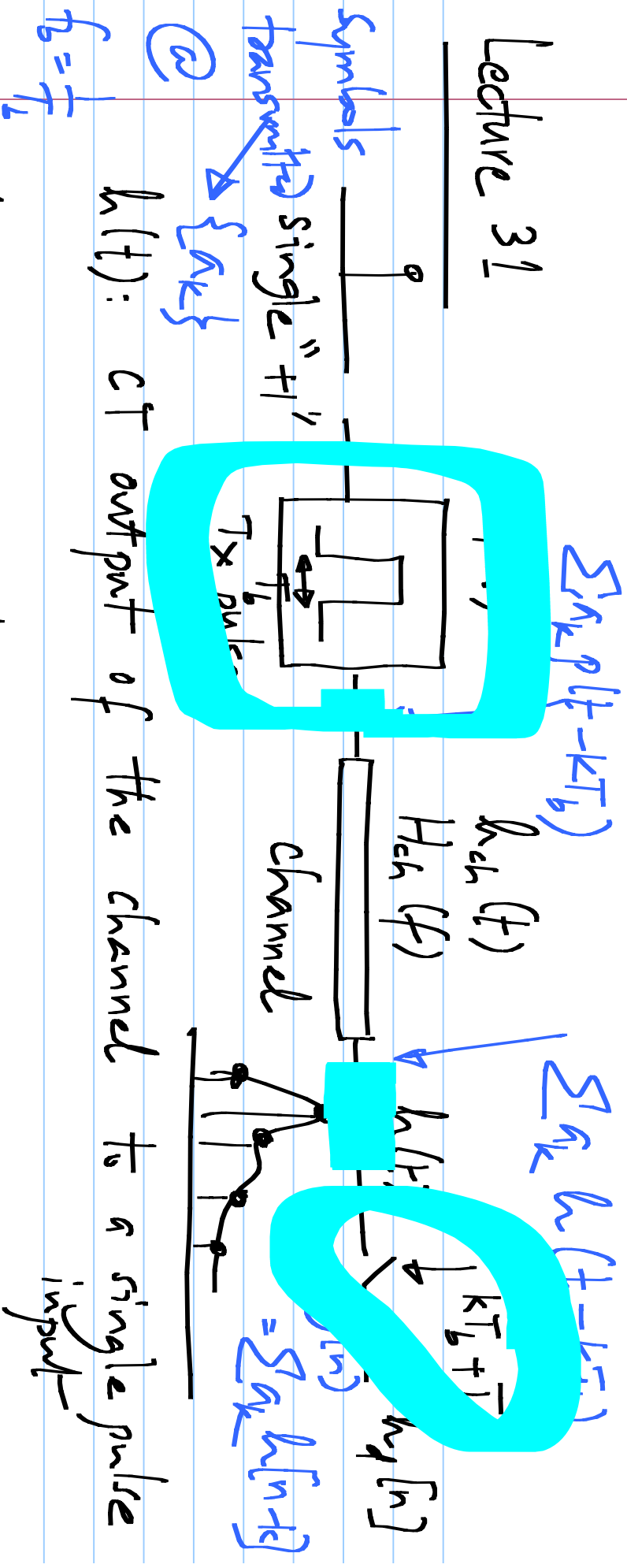
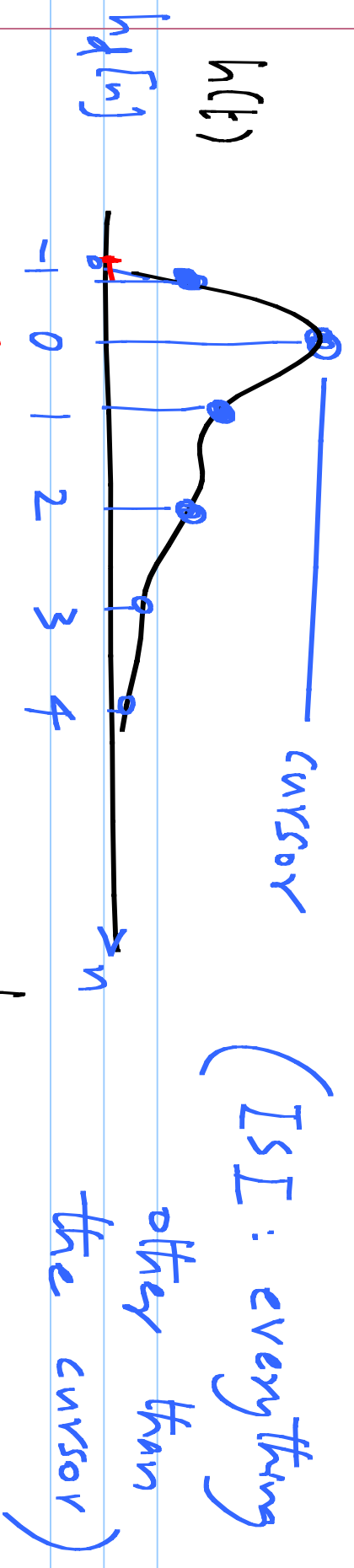


Lecture 31



$h_d[n]$: DT output

$h(t)$: spans more than $T_b \Rightarrow$ Inter symbol "memory"
 $h_d[n]$: more than 1 non-zero element interference (ISI)



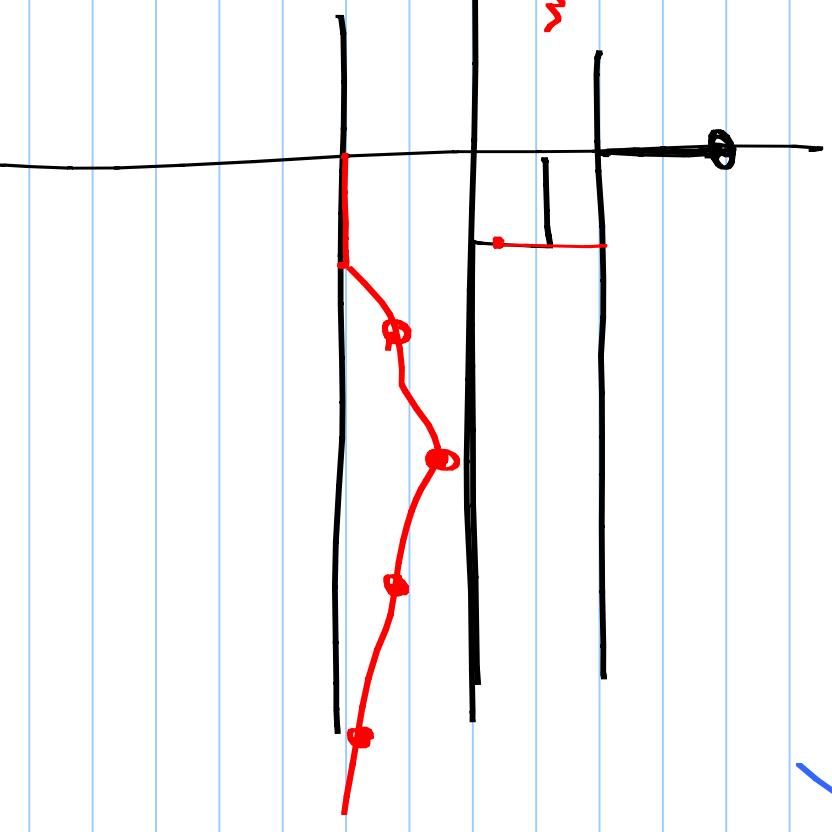
Pre-cursor

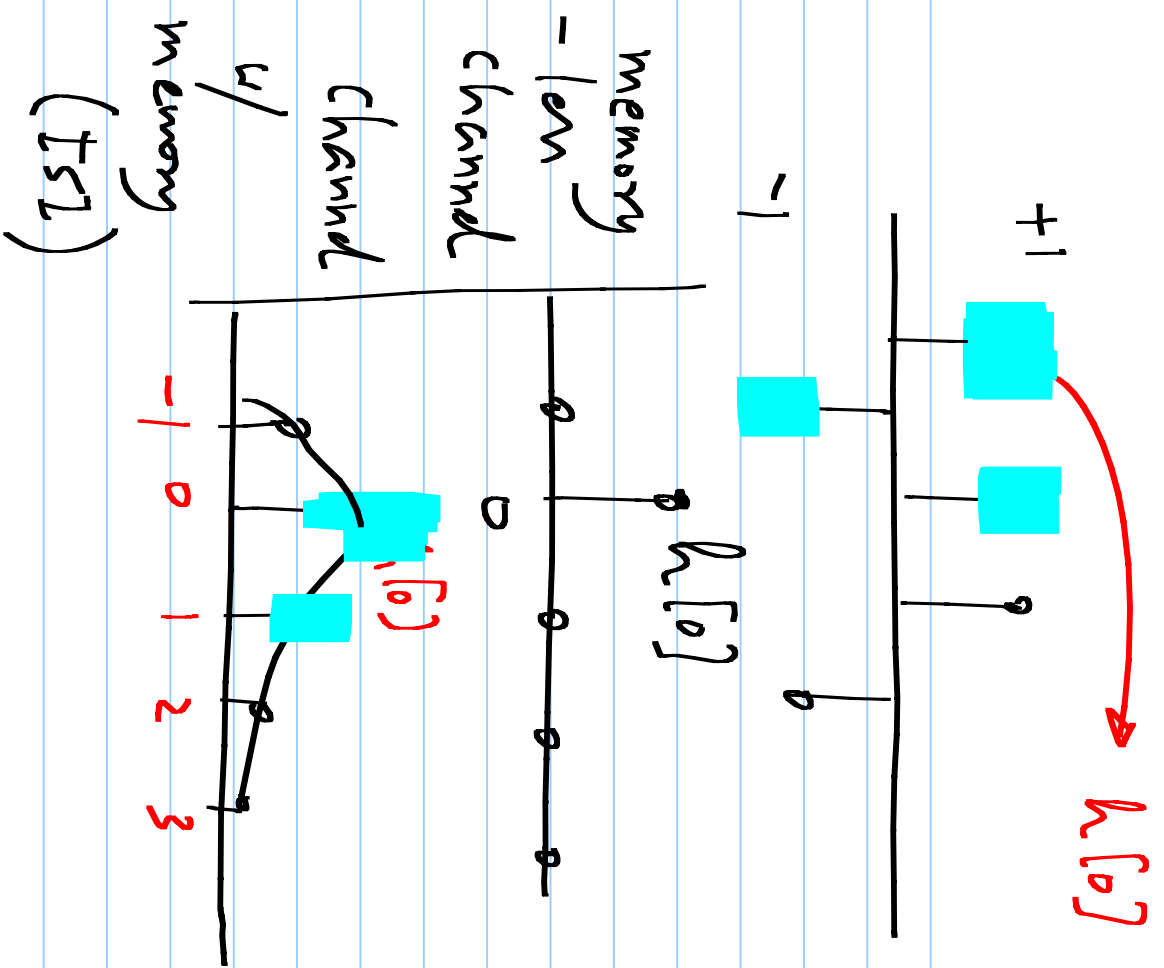
post-cursor ISI

Interference from future bits

Interference from past bits

Present bit





Eliminating ISI

$h_{eq}[n]$ - Equalizer

* Use a discrete-time filter h_{eq} either @ Tx

or @ Rx (assume that CDR is working correctly)

Linear $\underbrace{h_{eq}[n]}_{\text{highpass}} * \underbrace{h_d[n]}_{\text{lowpass}} \sim \delta[n]$

DT equalization

$$\frac{H_{eq}(\Omega) \cdot H_d(\Omega)}{H_{eq}(\Omega)} \sim 1 \quad (0 \leq \Omega \leq \pi)$$

* Use a CT highpass filter @ Tx ($\sim$ changing) or @ Rx $H_{eq}(f) \cdot H_{ch}(f) \sim 1$

Input bits $\{a_k\}$

Memoryless channel:

Channel w/ ISI:

$$h_d[n]$$

$$\sum_k a_k h[n-k]$$

$$a_0 = 1$$

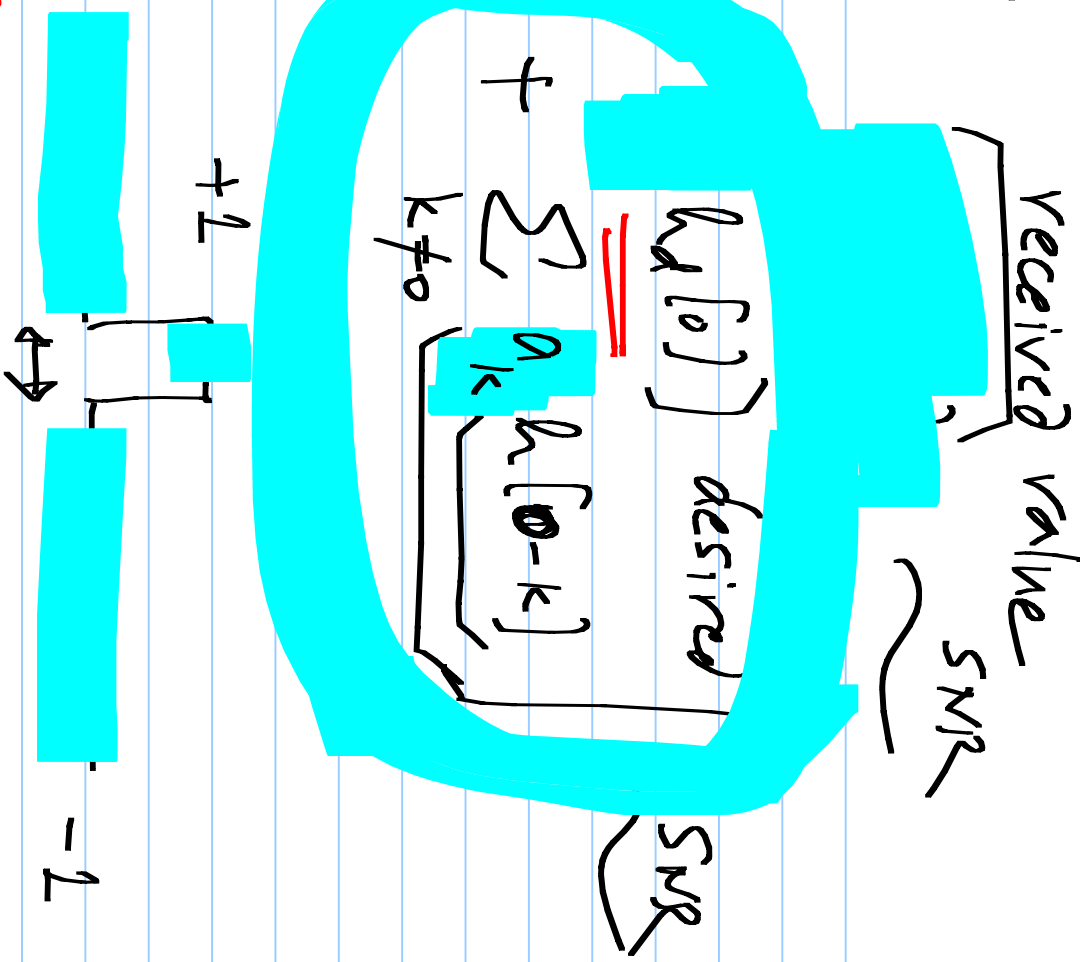
~~total~~

$$ISI = \sum_{k \neq 0} |h[-k]|$$

$$N/c$$

$$\sum_{k \neq 0}$$

$$\geq h[0]$$



$$h_x[n] \quad \begin{array}{c} 0.9 \\ 0.1 \end{array} \quad \text{erfc}\left(\frac{1}{\sigma_N}\right)$$

$$\begin{array}{c} 0 \\ 0 \end{array} \downarrow \quad -h[1] - h[0] \quad -0.9 - 0.1$$

$$\begin{array}{c} 0 \\ 1 \end{array} \quad -h[1] + h[0] \quad (+0.9 - 0.1)$$

$$\begin{array}{c} 1 \\ 0 \end{array} \quad +h[1] - h[0] \quad -0.9 + 0.1$$

$$\begin{array}{c} 1 \\ 1 \end{array} \quad h[1] + h[0] \quad (0.9 + 0.1)$$

$$w h_N \sigma_N$$

$$w$$

$$\frac{1}{2} \text{erfc}\left(\frac{\sigma_N}{\sigma_N}\right)$$

$$\begin{array}{c} 0 \\ +1 \\ -1 \end{array} \quad \begin{array}{c} +1 \\ +1 \\ -1 \end{array} \quad \begin{array}{c} +1 \\ +0.8 \\ -0.8 \end{array} \quad \left. \begin{array}{c} +1 \\ +0.8 \\ -0.8 \end{array} \right\} p = \frac{1}{4} + \frac{1}{2} \text{erfc}\left(\frac{\sigma_N}{\sigma_N}\right)$$