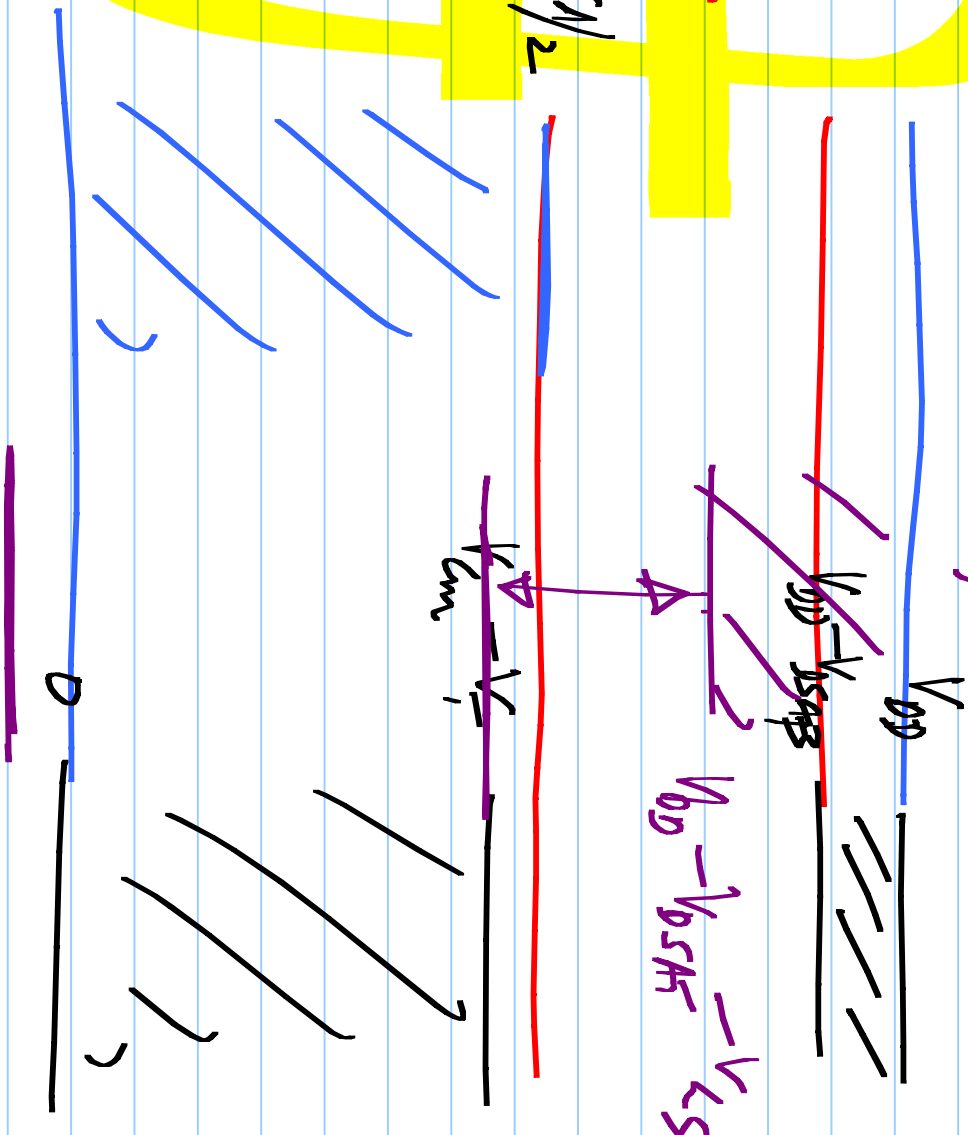
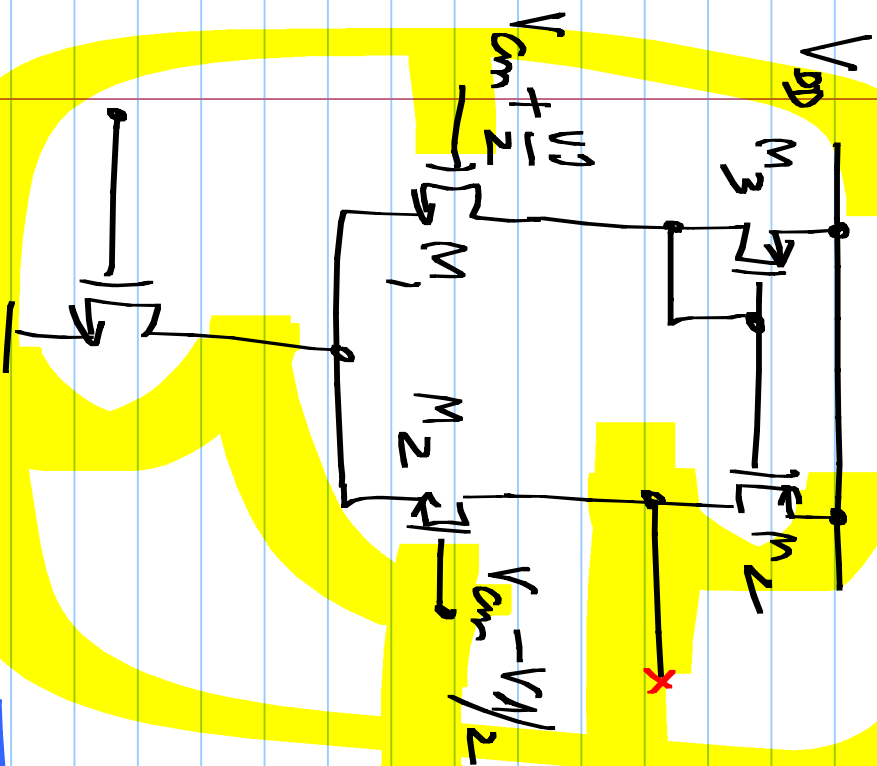
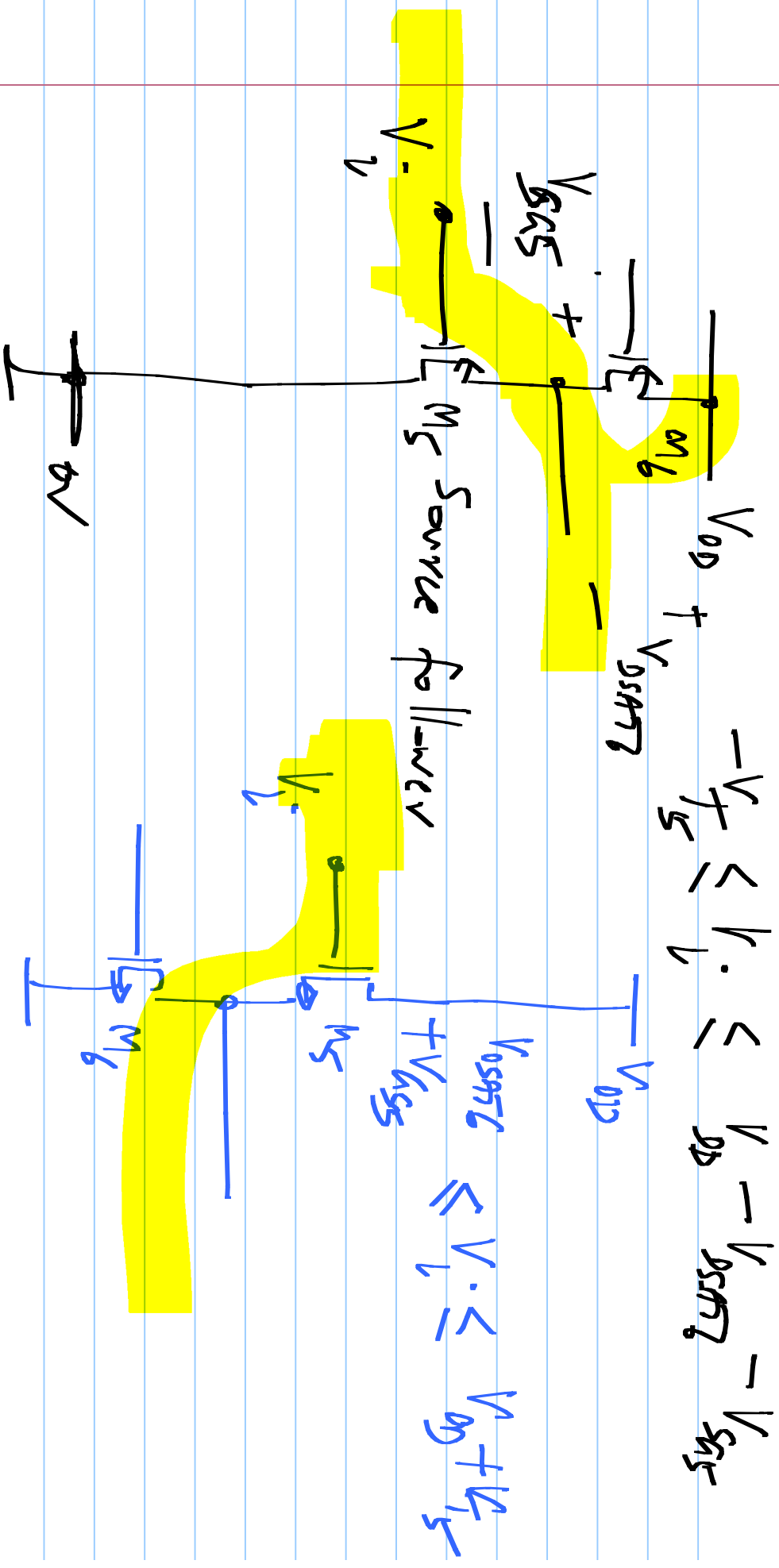


Lecture 23

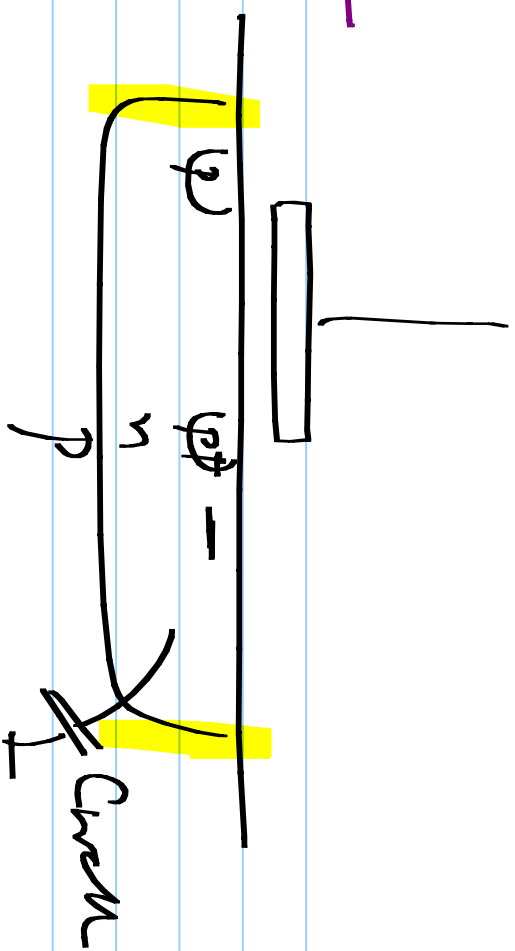
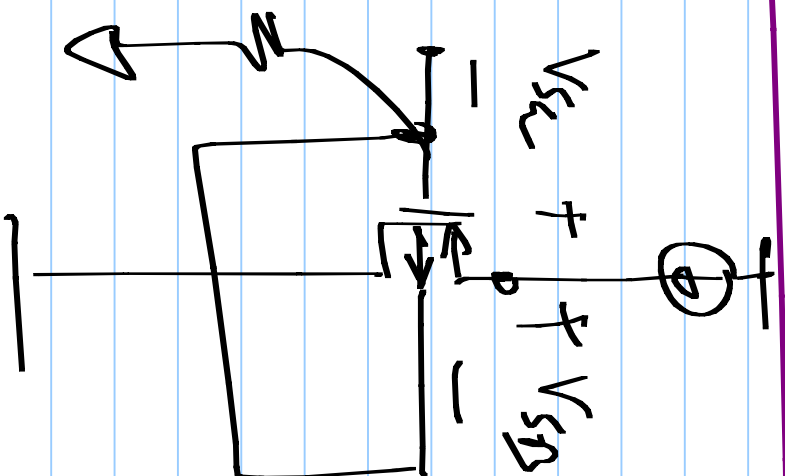
7/3/2016 $V_{D1} + V_{D2}$

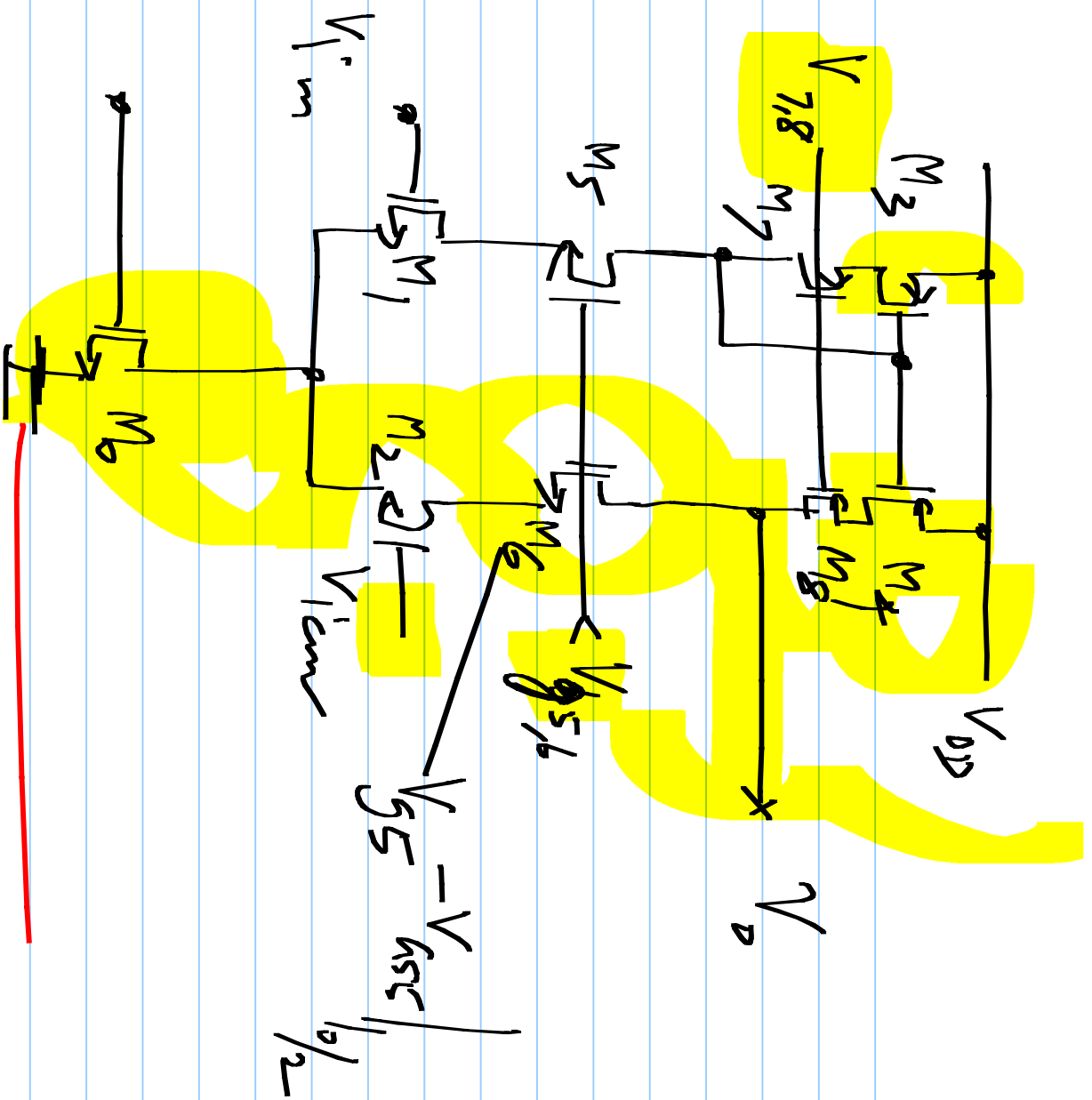
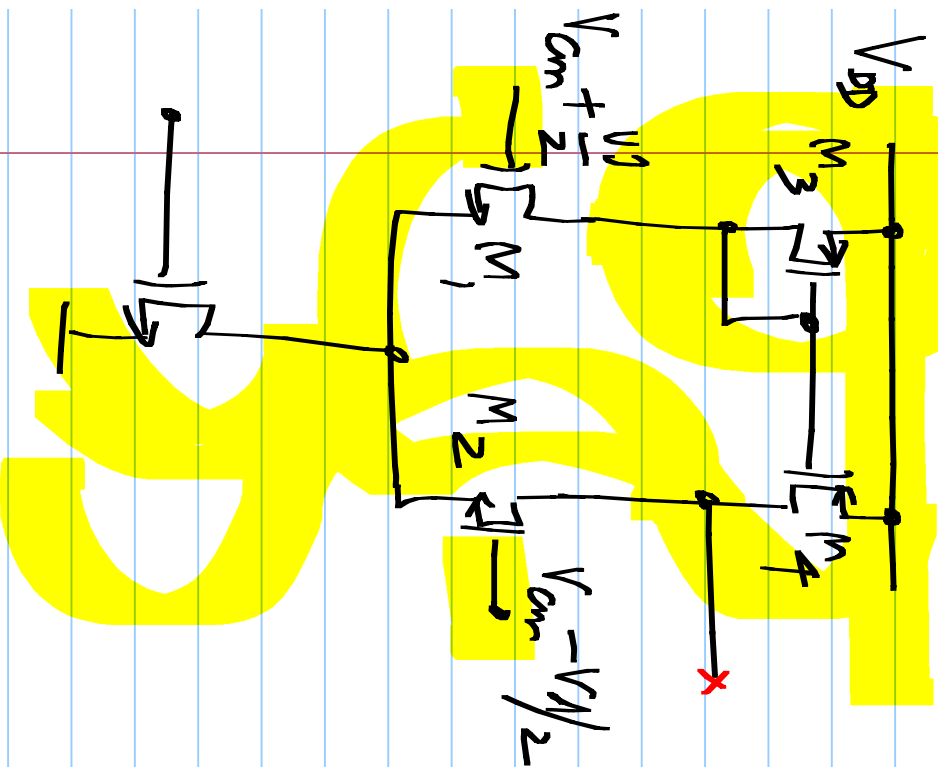
p_{mos}





PMOS source follower





Single stage opamp

$$V_{DSM1/0} \leq V_i < V_{DD} - V_{S13/0/2} + V_{T1}$$

$$V_{icm} \leq V_o \leq V_{DD} - V_{DS13/0/2} - V_{T1}$$

$$V_{DS11/0} + V_{T1} \leq V_i < V_{icm} \leq V_{GS} - V_{T1}$$

$$V_{GS} \leq V_o \leq V_{GS} + V_{T1}$$

$$V_{GS} \leq V_{DD} - V_{DS13/0/2} - V_{T1}$$

