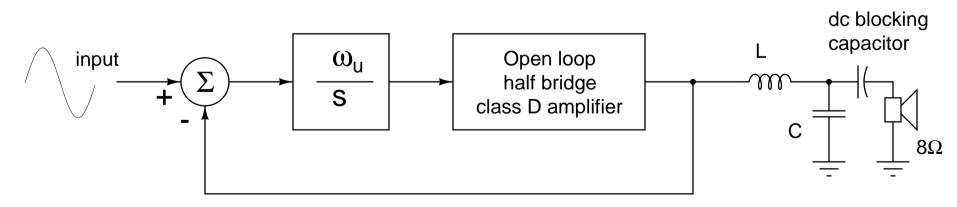
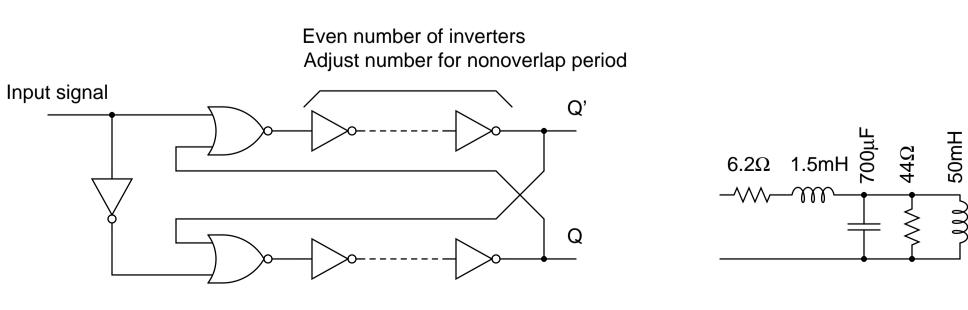


(dc blocking capacitor not required for bridged three level topology)

(a) Open loop half bridge class D amplifier



(b) Closed loop half bridge class D amplifier



(c) Non overlap high period generator(Use NAND for non overlap low period generation)

(d) Loudspeaker model