

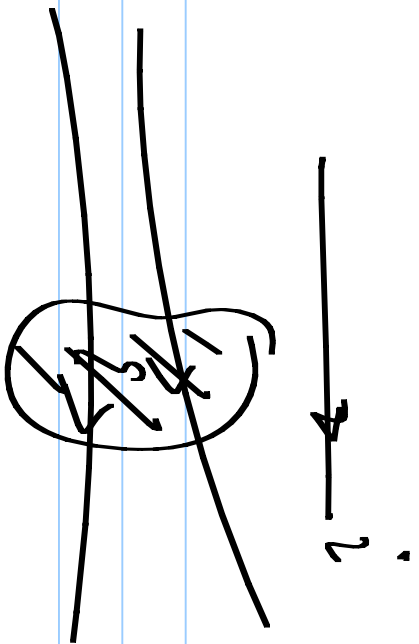
EE 2015

4/8/2017



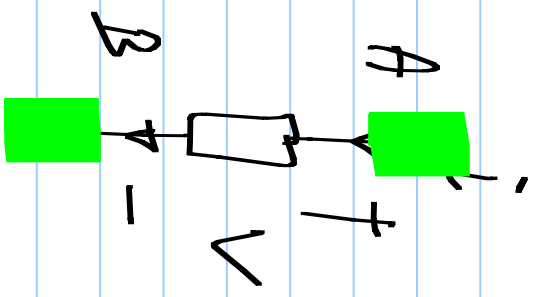
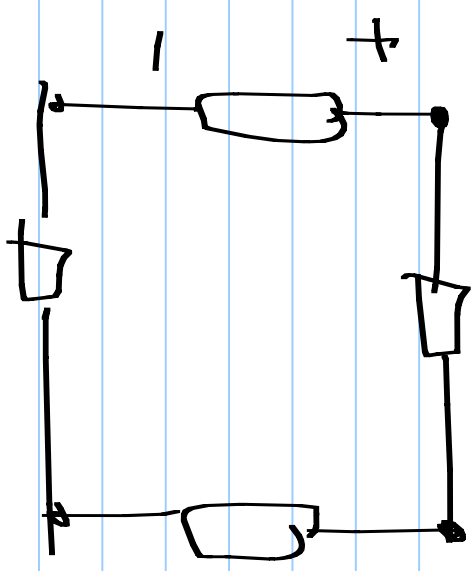
A charge q moving from A to B
loses potential energy = $q(V_A - V_B)$

Potential difference
(Voltage)



$$i \frac{dq}{dt}$$

Wires: connecting elements



Two-terminal element

A
I
+
Passive sign convention



B
-
charge Δq goes from A to B

Rate of p.e. lost:

$$\left(\frac{\Delta q \cdot V}{\Delta t} \right)$$

Power dissipated in the device

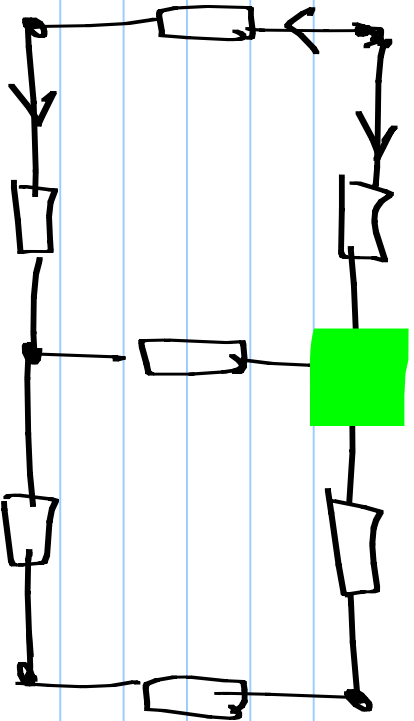
$I \cdot V$

Circuit analysis:

Determine

V across

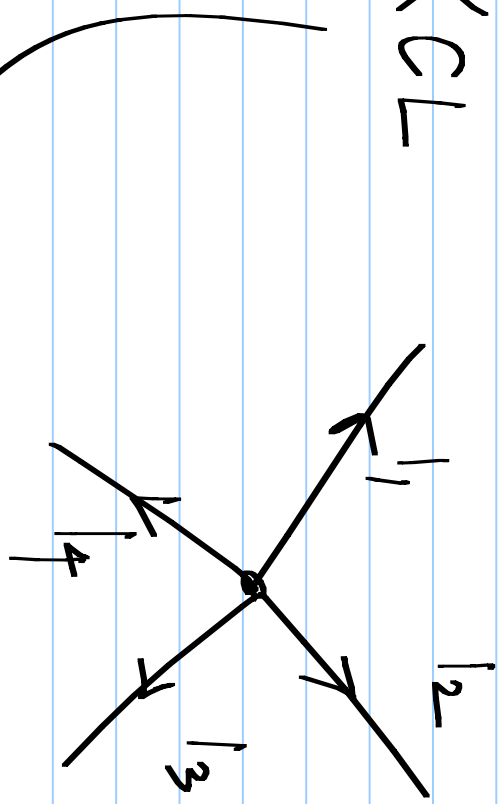
I through every
element



Kirchhoff's current law
Voltage law

Node: Point of connection of 2 or more elements

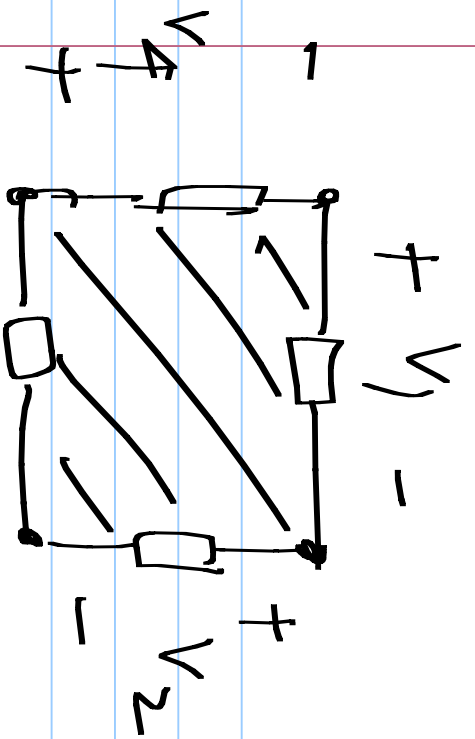
KCL



$$I_1 + I_2 - I_3 - I_4 = 0$$

Sum of currents leaving a node (closed surface) $= 0$

No Δ charge accumulation



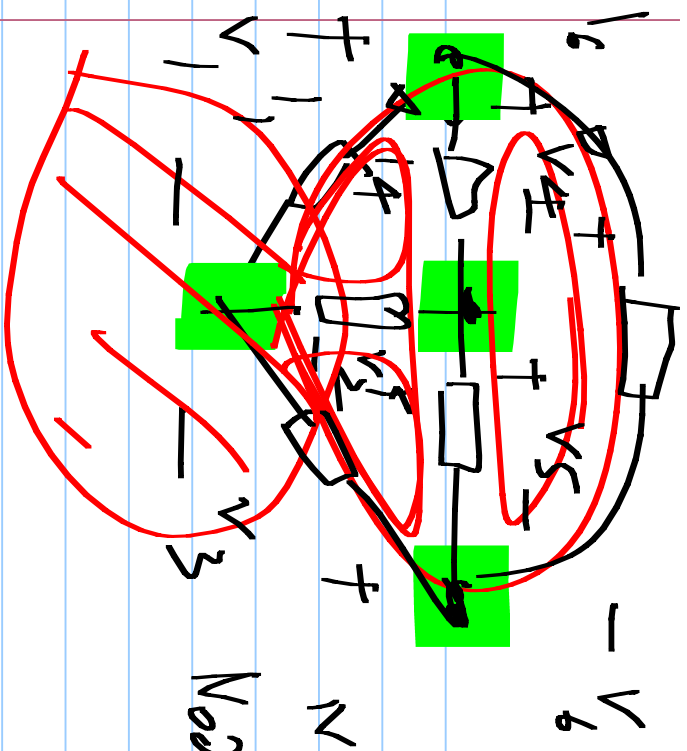
Kirchhoff's voltage law

$$V_1 + V_2 + V_3 + V_4 = 0$$

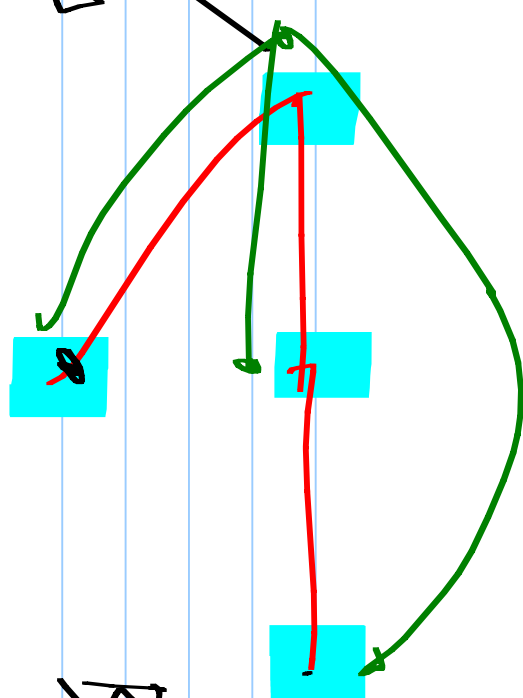
$$\nabla \times \vec{E} = - \frac{\partial \vec{B}}{\partial t}$$

Sum of voltage drops around a loop = 0

Resistor changing magnetic flux in the loop



Nodes



B branches

$$N \leq B \leq \frac{N(N-1)}{2}$$

0

0

$$|I| + |I_4| + |I_6| = 0$$

$$V_4 + V_2 - V_1 = 0$$

$$B = (N-1)$$

~~VL of~~

0

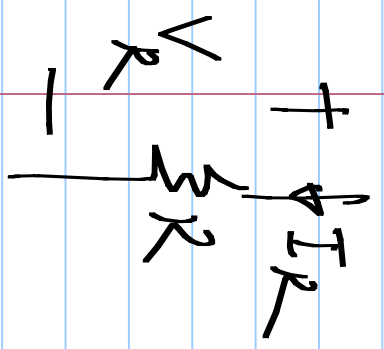
Circuit w/ B branches & N nodes

KCL : $N-1$ Need $2B$

KVL : $B - (N-1)$ equation

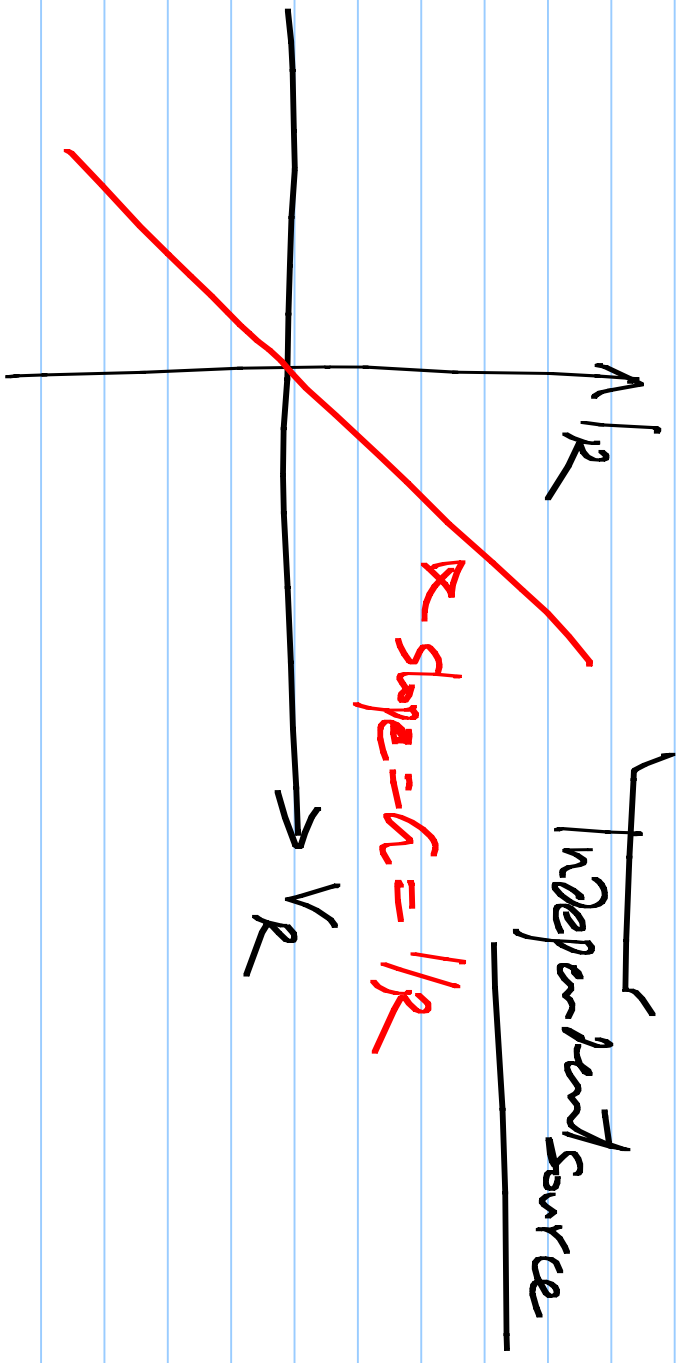
B

Two-terminal elements: $R, C, L, V_s, I_s,$



$$V_R = I_R \cdot R$$

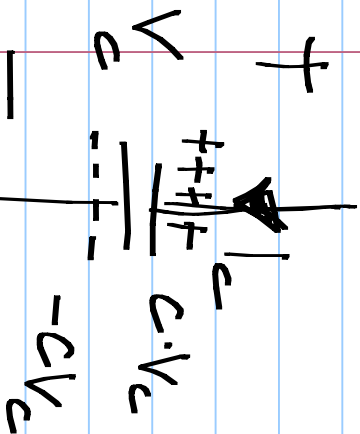
Resistance



$$I_R = G \cdot V_R$$

Conductance

C



$$I_c = C \frac{dV_c}{dt}$$

$$\frac{d}{dt}(C V_c)$$

$$C \frac{dV_c}{dt}$$

