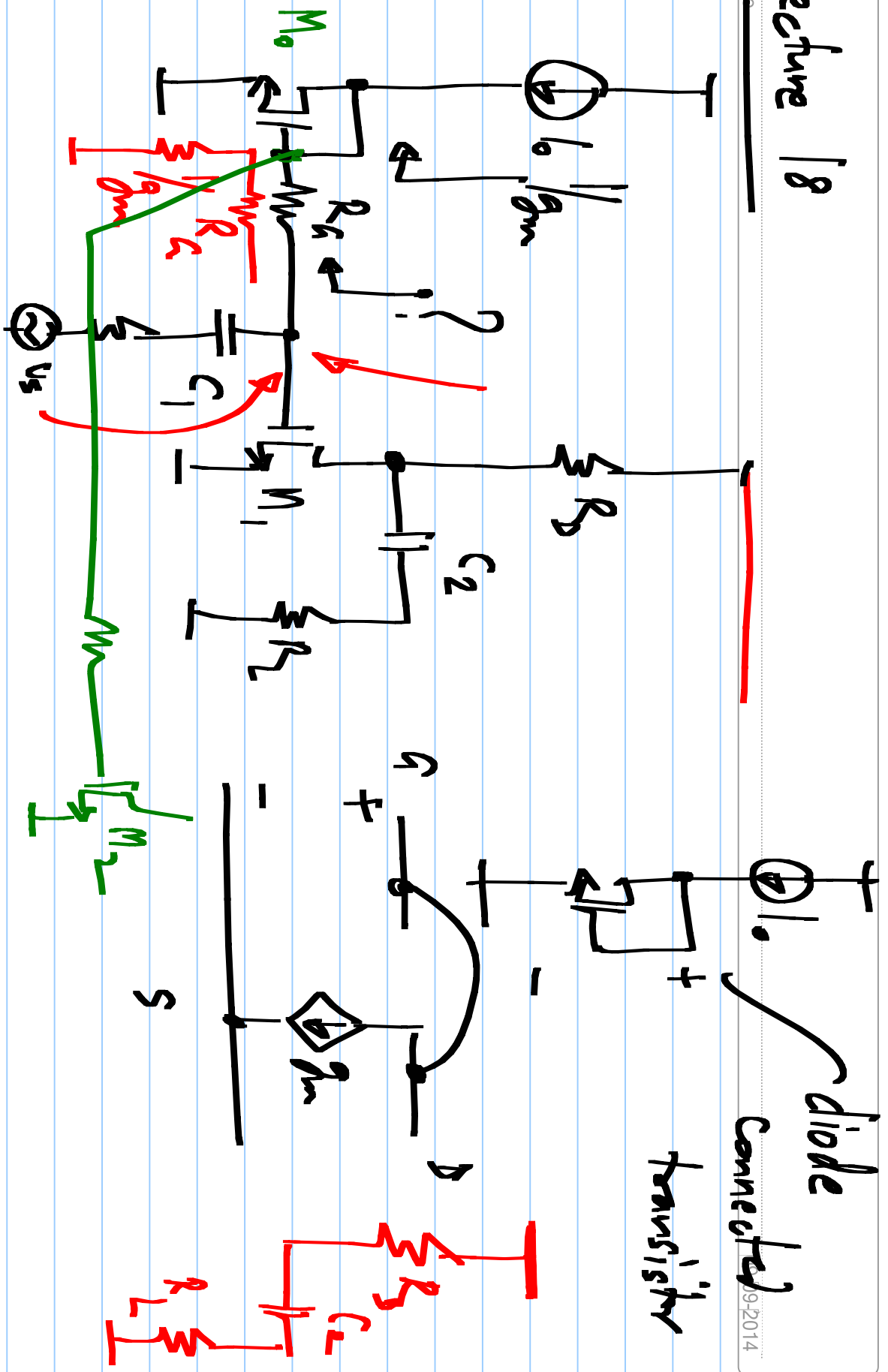
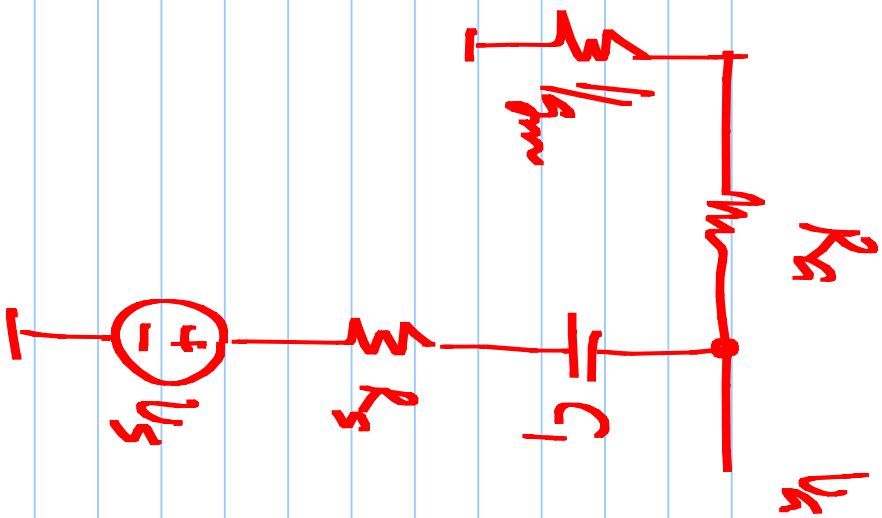


# Lecture 18

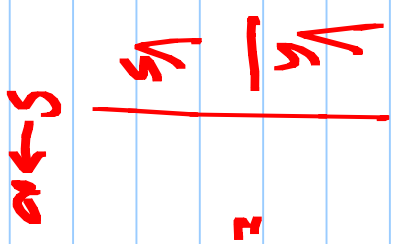
Notes Title 9/2/2014



diode connected transistor



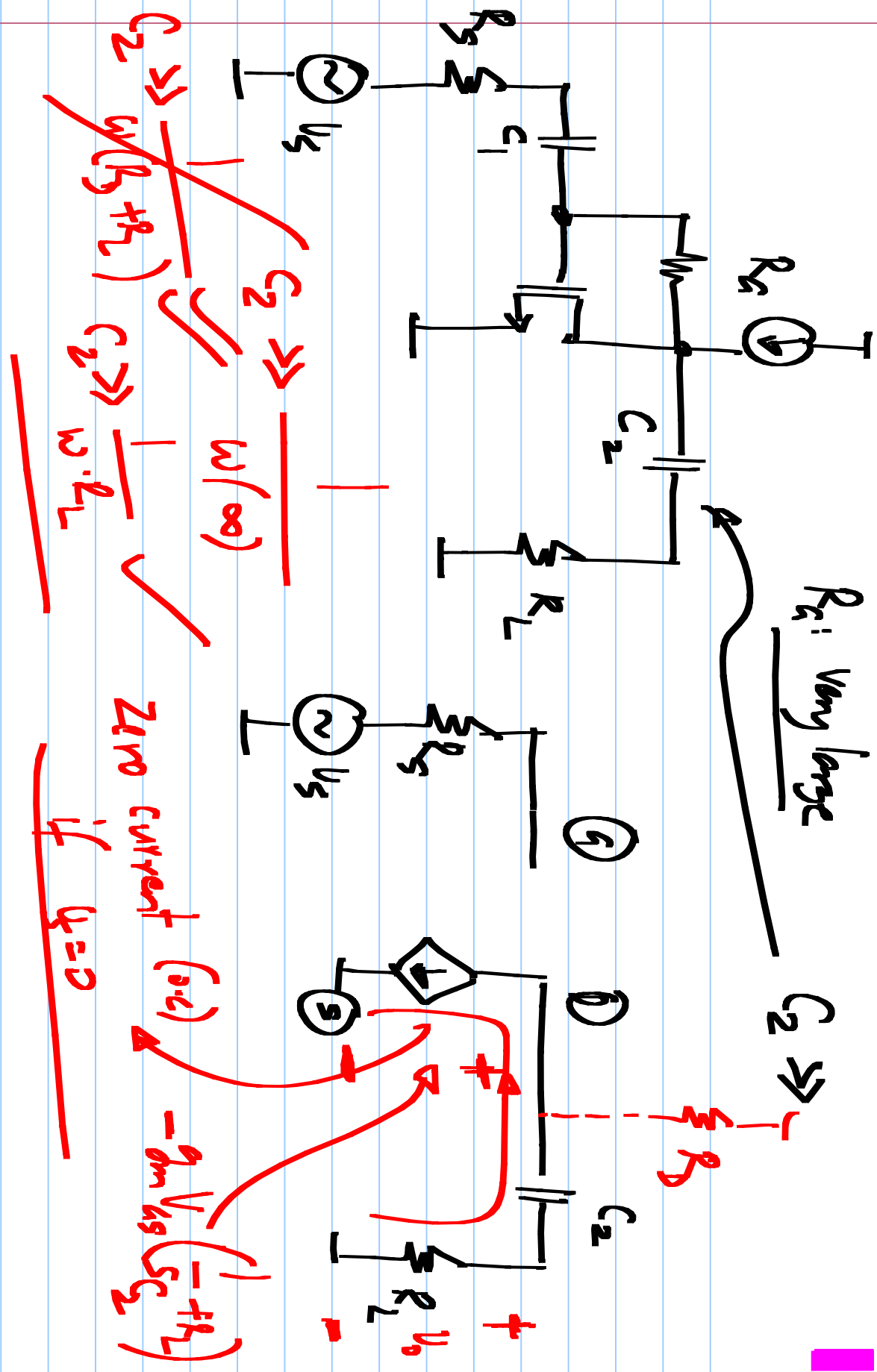
$$U_a = \frac{U_s}{1 + s C_1 (R_a + 1/g_m)}$$



$$R_a + 1/g_m$$

$$C_1 \gg \frac{1}{\omega (R_s + R_a + 1/g_m)}$$

$$R_L \gg R_s$$



$R_B$ : very large

$C_2 \gg$

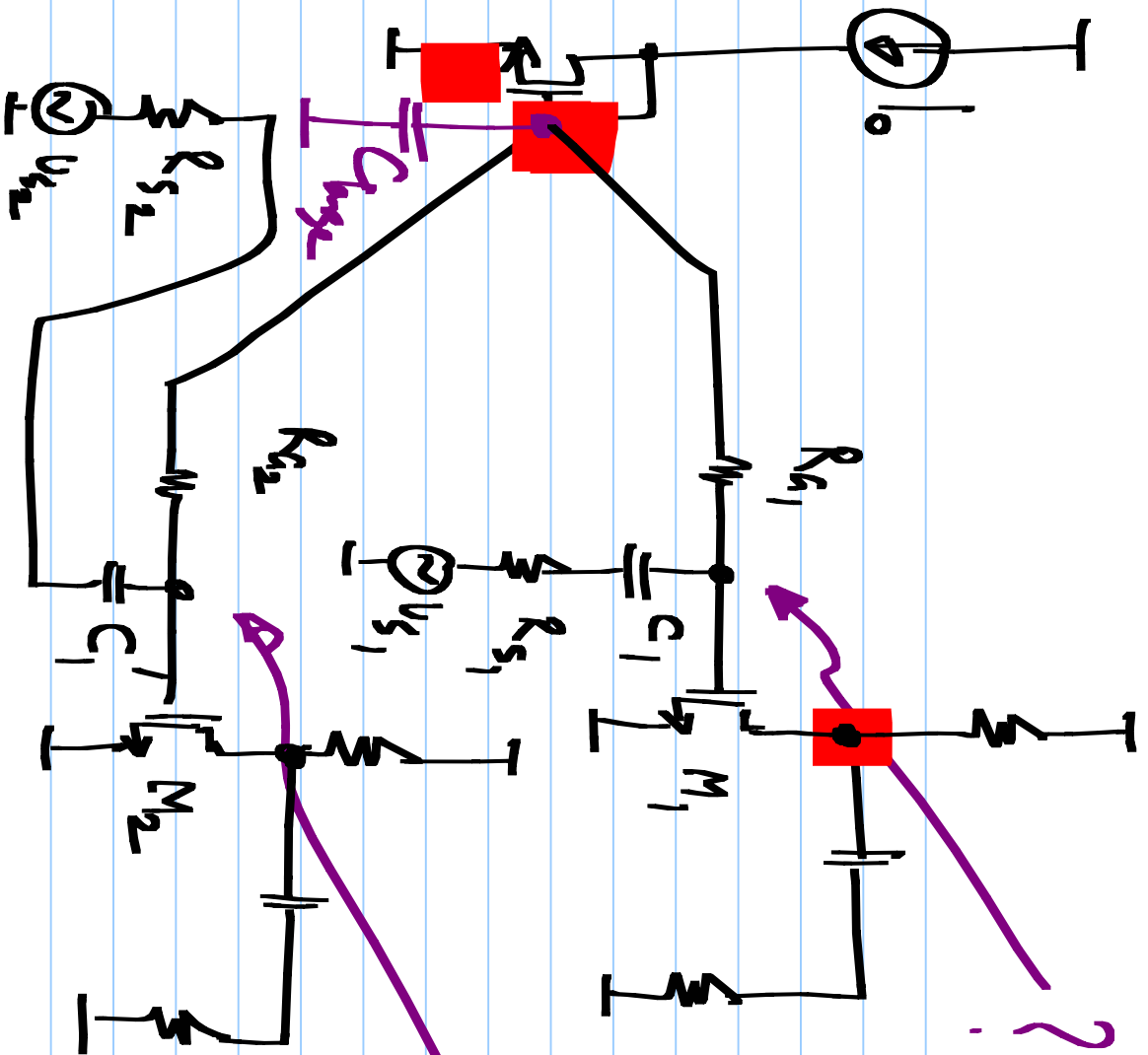
$\omega \gg \frac{1}{\omega \cdot R_L}$

~~$C_2 \gg \frac{1}{\omega(R_B + R_L)}$~~

Zero current (d.c.) if  $Q = D$

$-g_m V_{gs} \left( \frac{1}{sC_2} + R_L \right)$



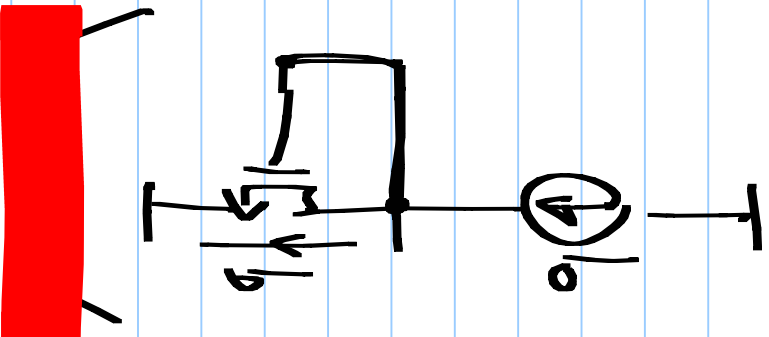


?  $V_{S1} + \alpha \cdot V_{S2}$   
 Swing limit

$V_{S2} + \beta V_{S1}$

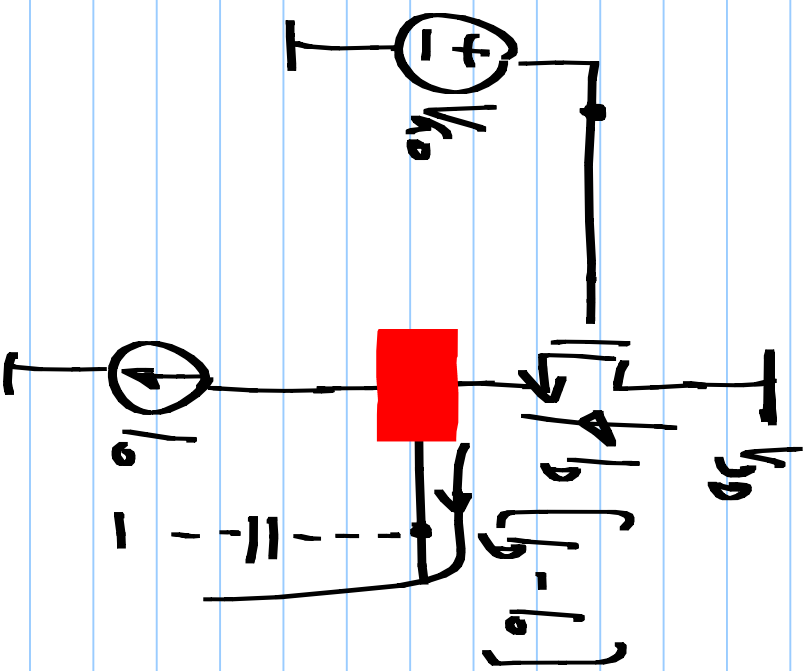
Sensing @ drain

$V_b$  to gate



Sensing @ source

$V_b$  to source



Control:

$|V_b| > |V_o|$

$V_s$  must

be increased

