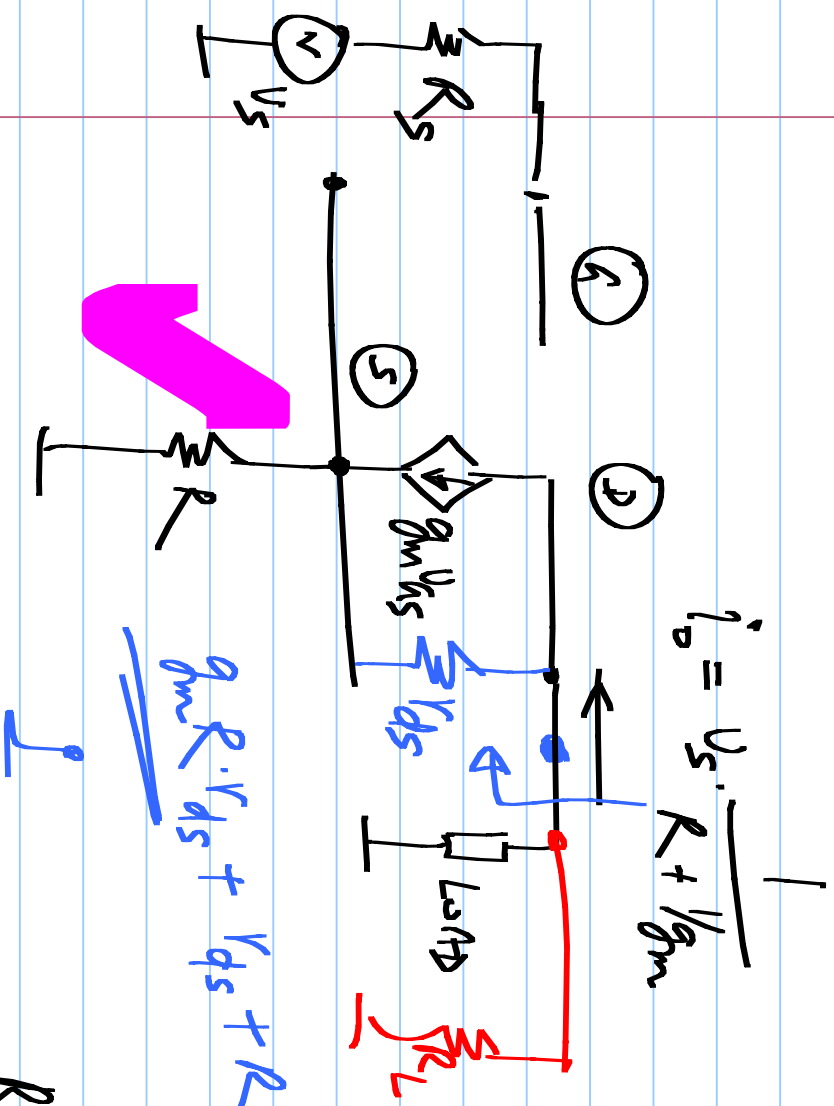
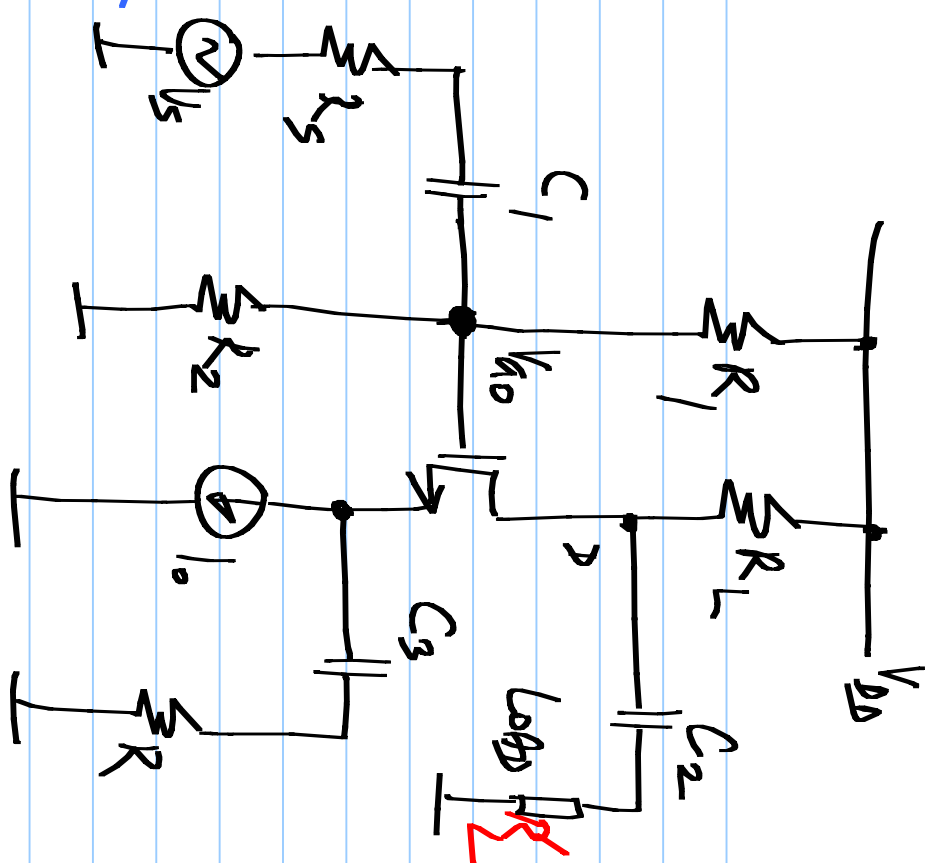


VCCS using a transistor.



$$i_o = V_S \cdot \frac{1}{R + 1/g_m}$$



$$R_1 \parallel R_2 \gg R_S$$

$$\frac{1}{\omega C_1} \ll R_s + R_1 \parallel R_2$$

$$\frac{1}{\omega C_2} \ll R_L + R_L'$$

$$\frac{1}{\omega C_3} \ll R + \frac{1}{g_m} \approx R$$

Quiescent

increment

$$V_c: V_{DD} \cdot \frac{R_2}{R_1 + R_2}$$

$$V_S \cdot \frac{R_1 \parallel R_2}{R_S + R_1 \parallel R_2} \approx V_S$$

$$V_D: V_{DD} - I_0 R_L = \frac{g_m}{1 + g_m R} V_S \cdot (R_L \parallel R_L') \approx -\frac{V_S}{R} (R_L \parallel R_L')$$

$$I_D: I_0 = \frac{V_S}{R + 1/g_m} \approx \frac{V_S}{R}$$

$$V_{DD} - I_0 R_L = \frac{V_S}{R} (R_L \parallel R_L') > V_{DD} \cdot \frac{R_2}{R_1 + R_2} + V_S - V_i$$

$$-1_o R < V_S <$$

$$-1_o \left(R + \frac{1}{g_m} \right)$$

(cutoff)

$$V_{DD} - \frac{R_D}{R_1 + R_2} - 1_o R_L + V_T$$

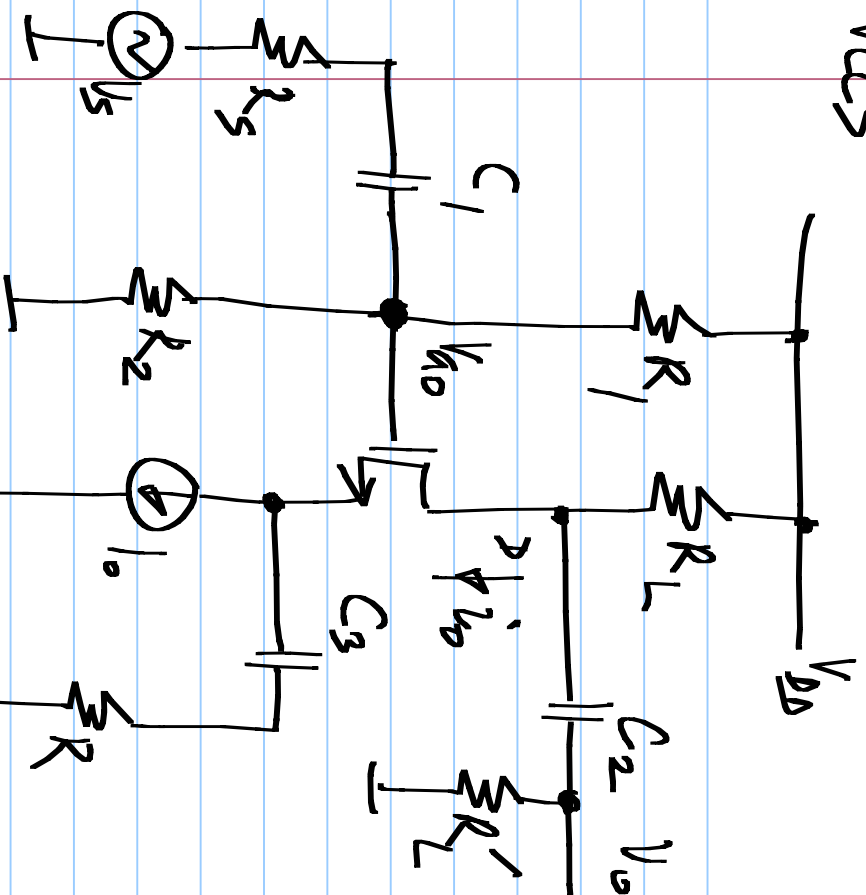
$$1 + \frac{R_L \parallel R'_L}{R}$$

(Triode)

$$g_m R_L \parallel R'_L$$

$$\frac{1}{R + 1/g_m}$$

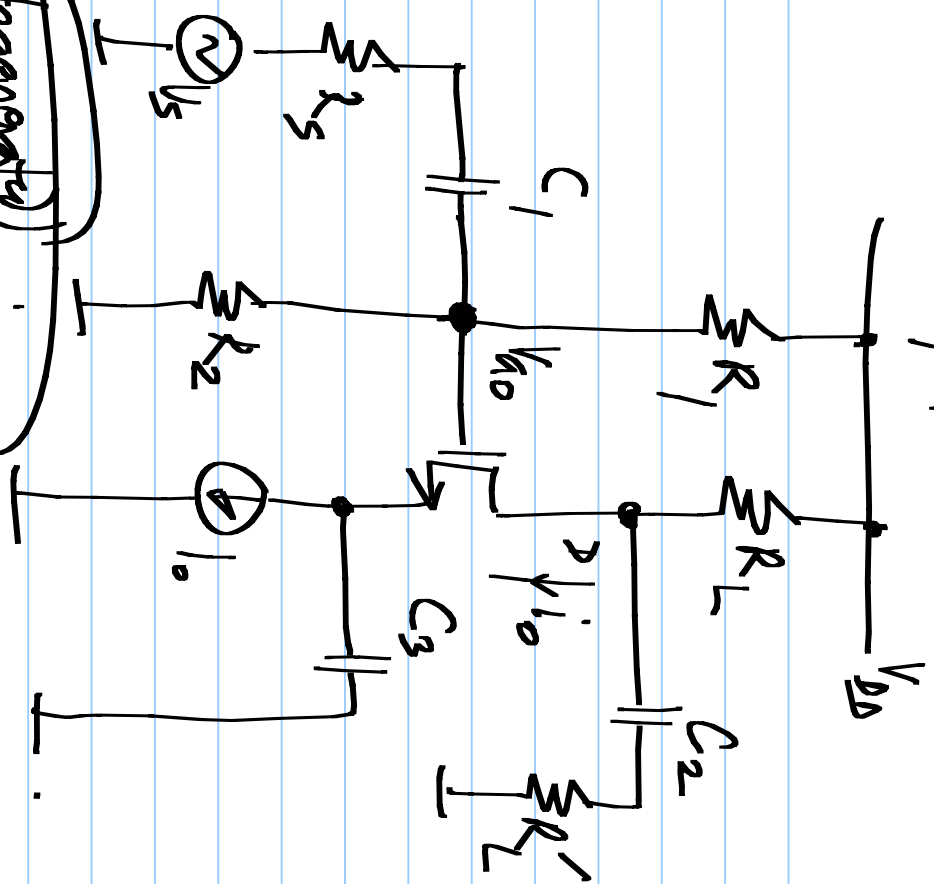
VCCS



$$\frac{i_o}{v_s} =$$

$$\frac{1}{R + 1/g_m} \approx \frac{1}{R}$$

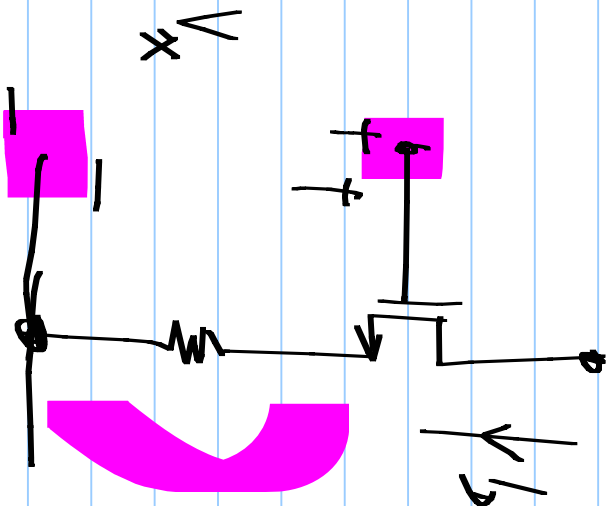
CS amplifier



$$\frac{i_o}{v_s} = g_m$$

degenerates

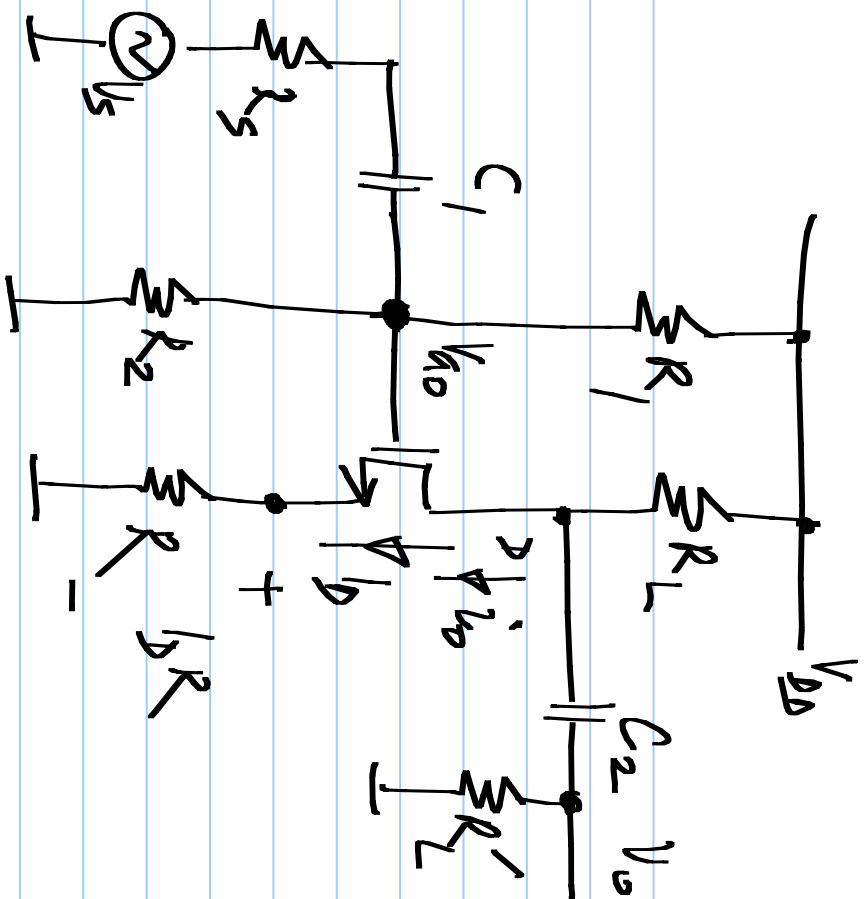
$$\frac{\partial I_D}{\partial V_x} = \frac{g_m}{1 + g_m R}$$



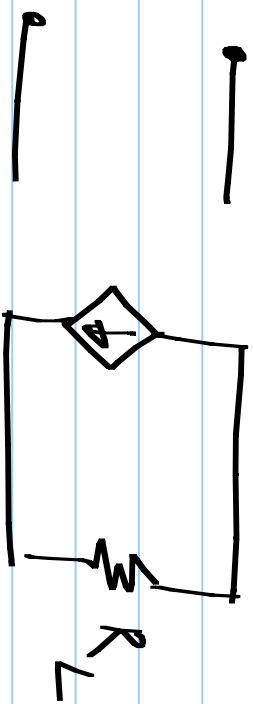
Degenerated transistor

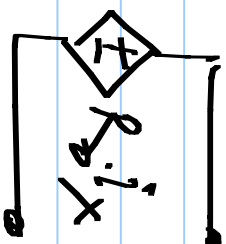
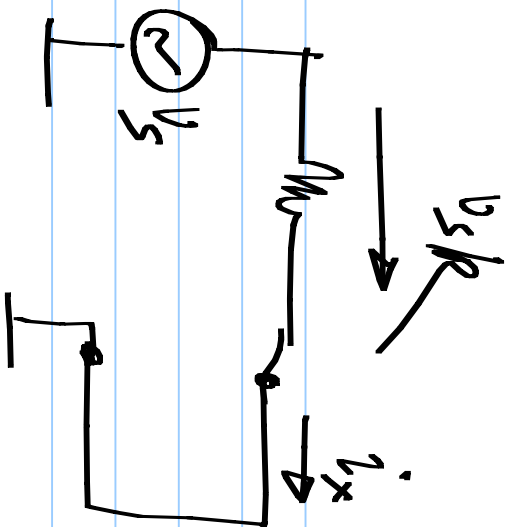
$$\frac{V_o}{V_s} = 1 - \frac{g_m}{1 + g_m R} \quad R_L \parallel R_L' \approx 1 - \frac{R_L \parallel R_L'}{R}$$

$$= -\frac{R_L}{R} \quad \Bigg|_{R_L' = \infty}$$

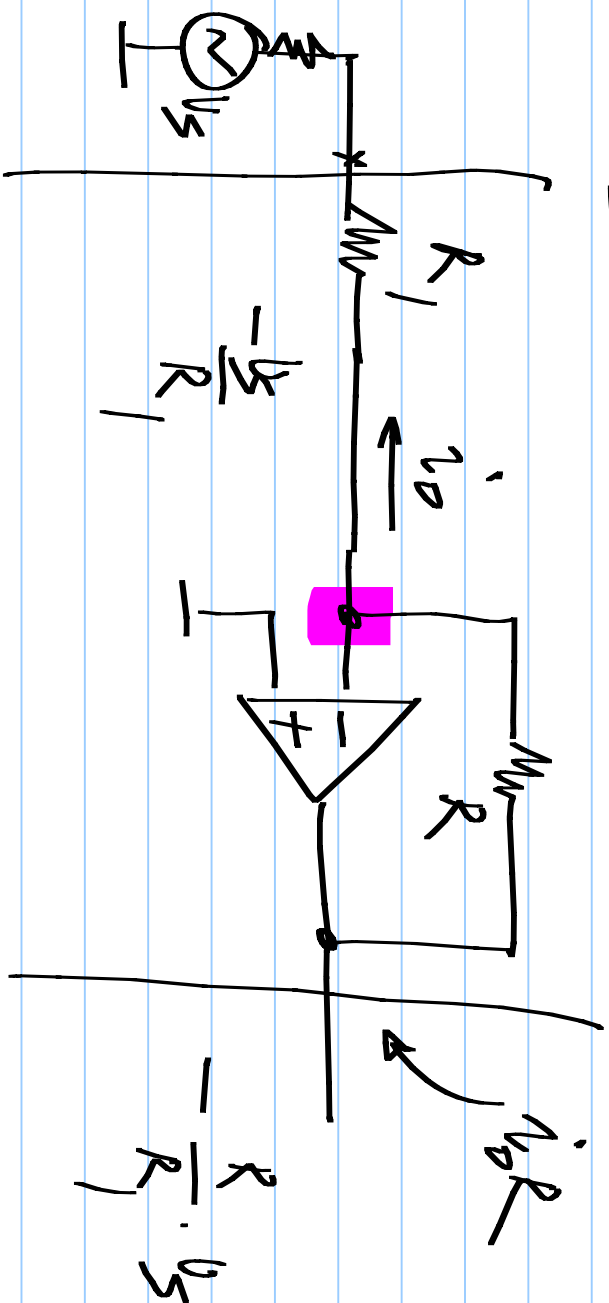


$$I_D = \frac{\mu_{ox}}{2} \frac{W}{L} (V_{gs} - V_T)^2$$





$$\frac{R}{R} \cdot v_s$$



Inverting amplifier

CCCS:

$$i_{out} = i_s$$

