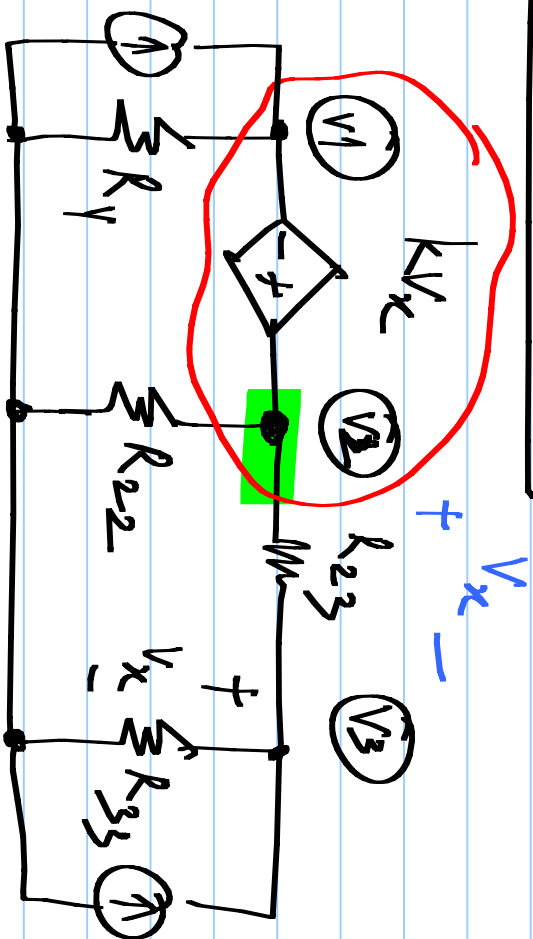


# EC 1010: Lecture 7



- ✓ Resistors & ind. I sources
- ✓ ind. V sources
- ✓ VCS

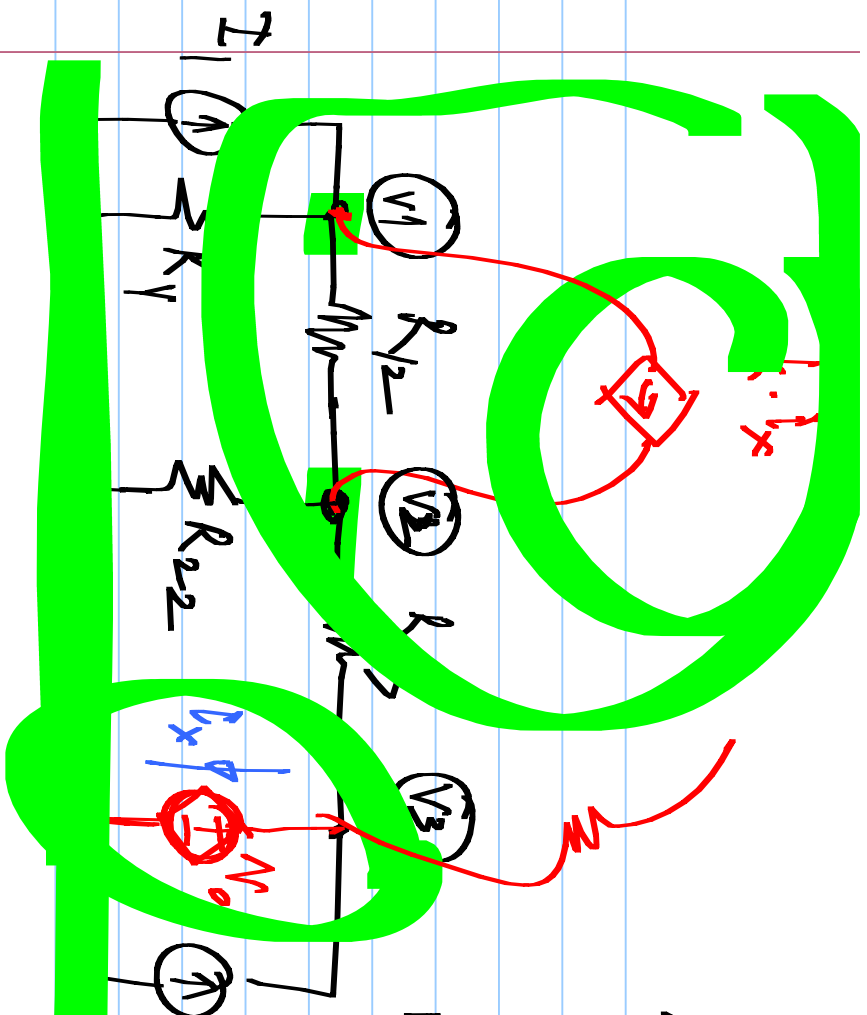
VCS

\* Combine nodes to which V-source is connected into a supernode

Eq. for the VCS

$$V_2 - V_1 - kV_3 = 0$$

$$V_2 - V_1 - kV_2 + kV_3 = 0$$



$$n1: V_1 \cdot (G_{11} + G_{12}) + k \cdot i_x - V_2 \cdot G_{12} = I_1$$

$$n2: \dots - k i_x \dots$$

$$\left( \frac{V_3}{R_{33}} \right)$$

$$V_2 - V_1 - R_{x1} i_x = 0$$

$$V_2 - V_1 - R_x \cdot V_3 / R_{33} = 0$$

## Nodal analysis:

- \* Node voltages (wrt ref. node): variable vector
- \* KCL eq. at every node (excl. ref.).

$$[G] V = I$$

Conductance matrix  $\swarrow$  Node voltage vector

$\searrow$  Source vector

- \* Circuits w/ resistors & ind. current sources

\* Circuits w/ resistors & ind. current sources

Symmetric  $[G]$  matrix

— VCCS :  $[G]$  : asymmetric

— ind. voltage sources  $[G] \quad V = I$

— CCVS / CCCS : similar form if the

controlling current is through a resistor  
— Auxiliary variables if the cont. current is

through voltage source

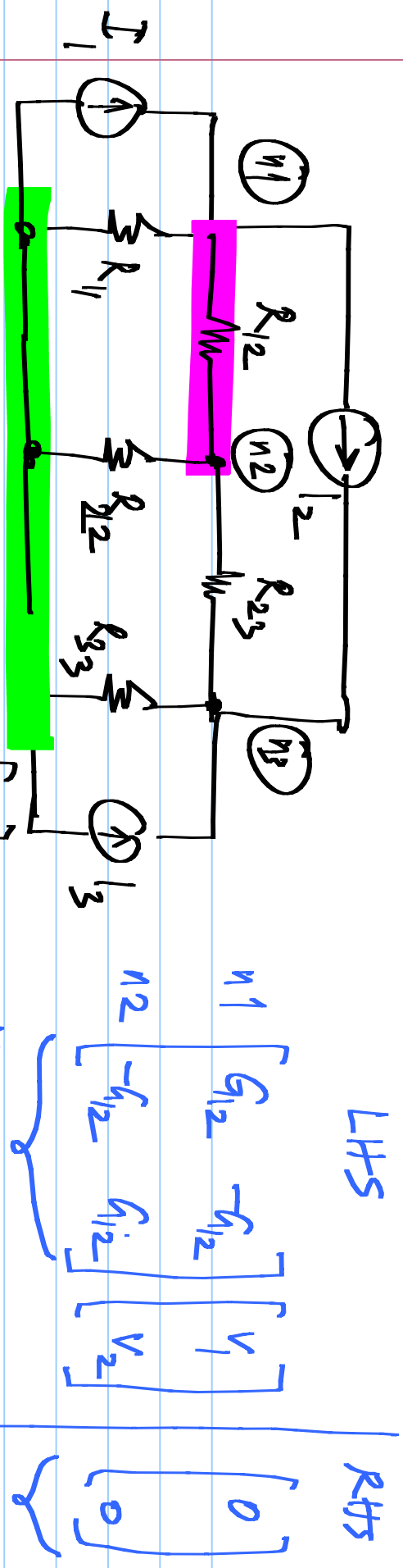
# Modified nodal analysis (MNA)

variable vector:

$$[G] \underline{v} = \underline{j}$$

currents through voltage sources,  
controlling currents

$$\left[ \begin{array}{c} \text{vector of} \\ \text{node} \\ \text{voltages} \\ \hline \text{vector } \delta \\ \text{auxiliary} \\ \text{branch currents} \end{array} \right] \underline{i_b} = \underline{v}$$

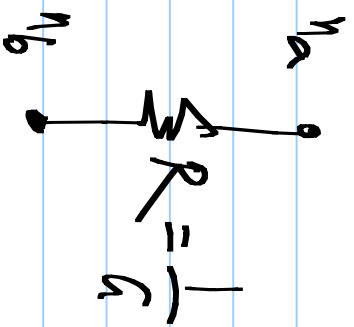


$$\begin{array}{l}
 \text{node } n1 \\
 \text{node } n2 \\
 \text{node } n3
 \end{array}
 \begin{bmatrix}
 G_{11} + G_{12} & -G_{12} & 0 \\
 -G_{12} & G_{12} + G_{22} + G_{23} & -G_{23} \\
 0 & -G_{23} & +G_{33} + G_{23}
 \end{bmatrix}
 \begin{bmatrix}
 v_1 \\
 v_2 \\
 v_3
 \end{bmatrix}
 =
 \begin{bmatrix}
 I_1 - I_2 \\
 0 \\
 I_2 + I_3
 \end{bmatrix}$$

MNA stamp

$$\begin{array}{l}
 \text{LHS} \\
 \text{RHS}
 \end{array}
 \begin{bmatrix}
 G_{12} & -G_{12} \\
 -G_{12} & G_{12}
 \end{bmatrix}
 \begin{bmatrix}
 v_1 \\
 v_2
 \end{bmatrix}
 =
 \begin{bmatrix}
 0 \\
 0
 \end{bmatrix}$$

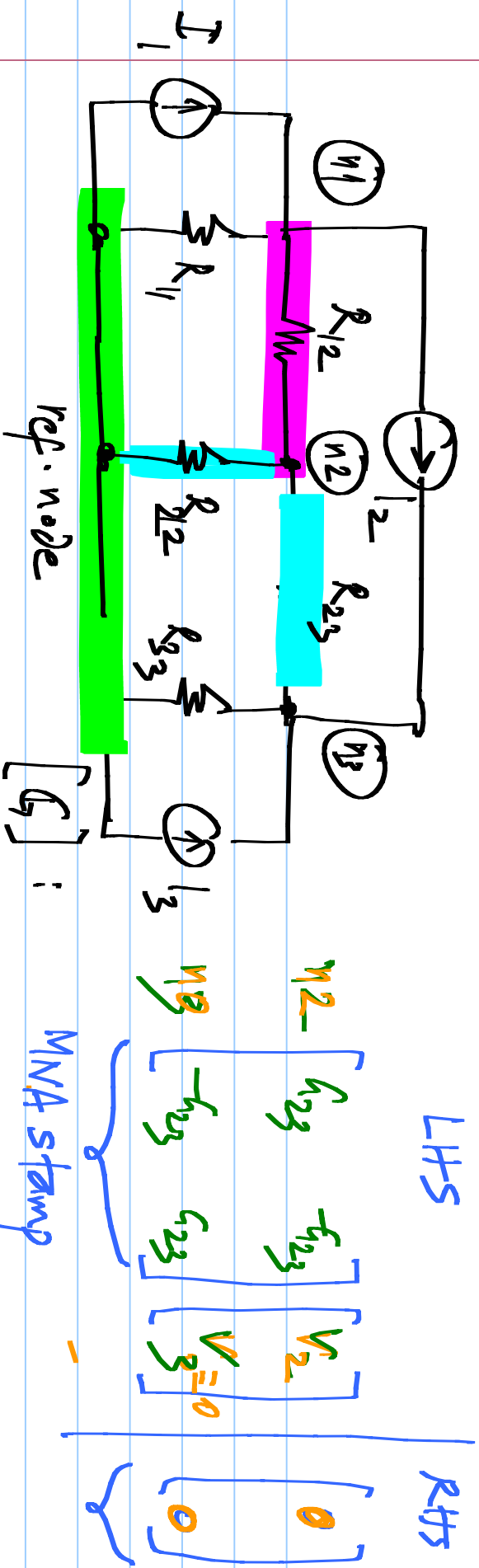
MNA stamp for a resistor:



$$\begin{matrix} n_a & n_b \\ \begin{bmatrix} g & -g \\ -g & g \end{bmatrix} \end{matrix} \begin{bmatrix} v_a \\ v_b \end{bmatrix}$$

$R_{th}$

$$\begin{bmatrix} 0 \\ 0 \end{bmatrix}$$



$$\begin{bmatrix}
 \text{node } n1 \\
 \text{node } n2 \\
 \text{node } n3
 \end{bmatrix}
 \begin{bmatrix}
 G_{11} + G_{12} & -G_{12} & 0 \\
 -G_{12} & G_{12} + G_{23} + G_{22} & -G_{23} \\
 0 & -G_{23} & +G_{33} + G_{23}
 \end{bmatrix}
 \begin{bmatrix}
 V_1 \\
 V_2 \\
 V_3
 \end{bmatrix}
 =
 \begin{bmatrix}
 I_1 - I_2 \\
 0 \\
 I_2 + I_3
 \end{bmatrix}$$

MNA stamp

LHS

RHS

$$\begin{bmatrix}
 G_{12} & -G_{23} \\
 -G_{23} & G_{23}
 \end{bmatrix}
 \begin{bmatrix}
 V_2 \\
 V_3
 \end{bmatrix}$$

$$\begin{bmatrix}
 -G_{23} & G_{23} \\
 G_{23} & -G_{23}
 \end{bmatrix}
 \begin{bmatrix}
 V_3 \\
 V_2
 \end{bmatrix}$$

MNA stamp



$$\begin{matrix} n_1 \\ n_2 \\ n_3 \end{matrix} \begin{bmatrix} g_{12} & -g_{12} & \\ -g_{12} & g_{12} + h_{12} + h_{23} & -g_{23} \\ -g_{23} & +g_{23} & \end{bmatrix} \begin{matrix} v_1 \\ v_2 \\ v_3 \end{matrix}$$