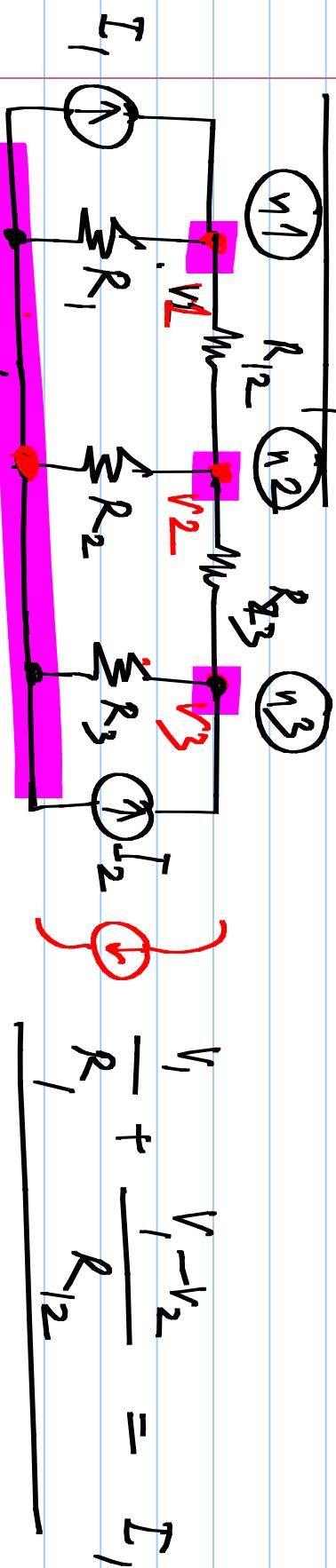


Node analysis: KCL at N-1 nodes



reference node

$$\frac{V_2 - V_1}{R_{12}} + \frac{V_2 - V_3}{R_2} + \frac{V_2 - V_3}{R_{23}} = I_1$$

$$\frac{V_3 - V_2}{R_2} + \frac{V_3 - V_1}{R_3} = I_2$$

$$\begin{bmatrix} 1 & -\frac{1}{R_{12}} & -\frac{1}{R_{23}} \\ -\frac{1}{R_{12}} & 1 + \frac{1}{R_2} + \frac{1}{R_{23}} & -\frac{1}{R_2} \\ 0 & -\frac{1}{R_{23}} & 1 + \frac{1}{R_2} + \frac{1}{R_3} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \end{bmatrix} = \begin{bmatrix} I_1 \\ 0 \\ I_2 \end{bmatrix}$$

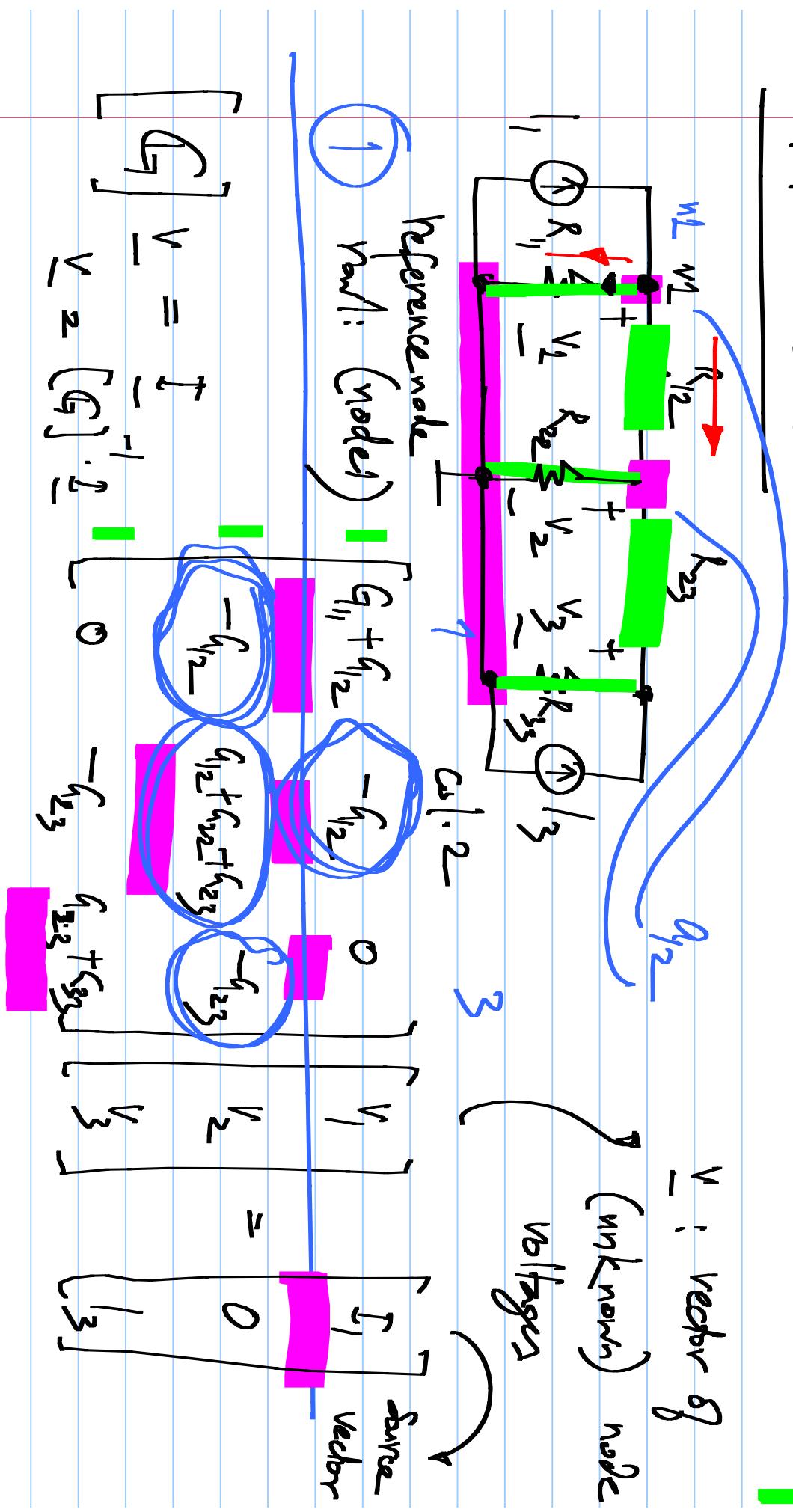
$V_1$

$V_2$

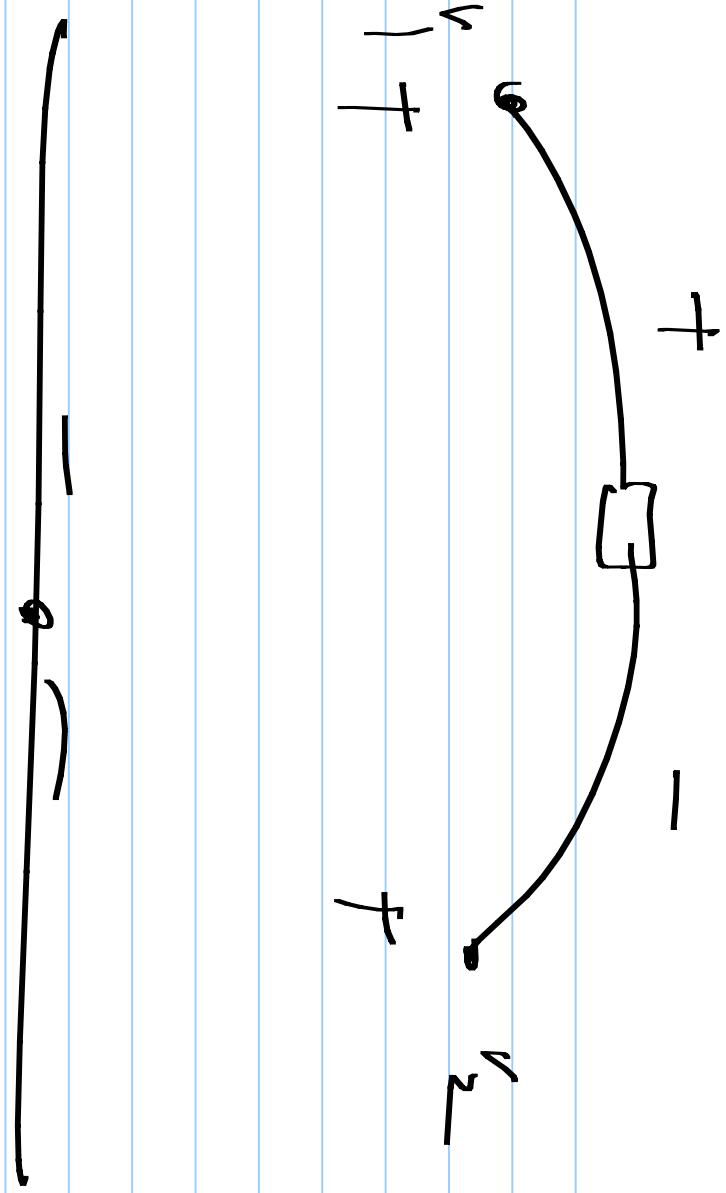
$V_3$

## EE1010: Lecture 6

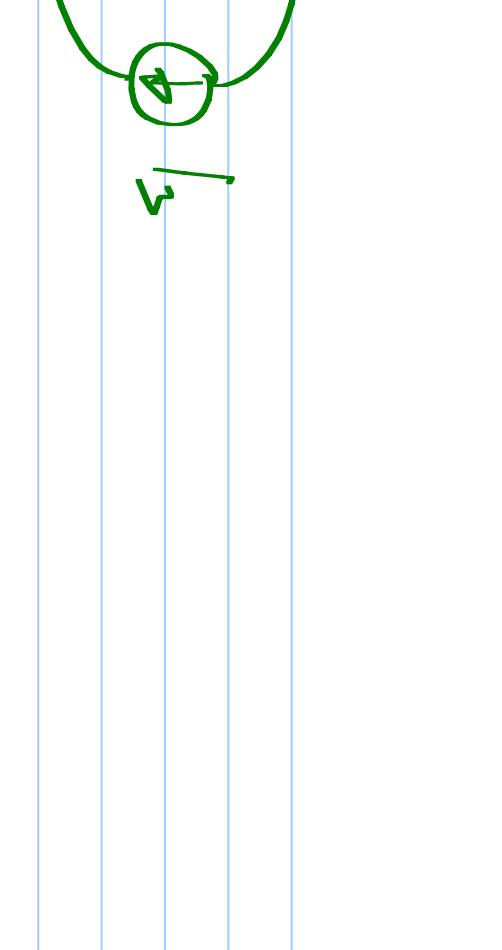
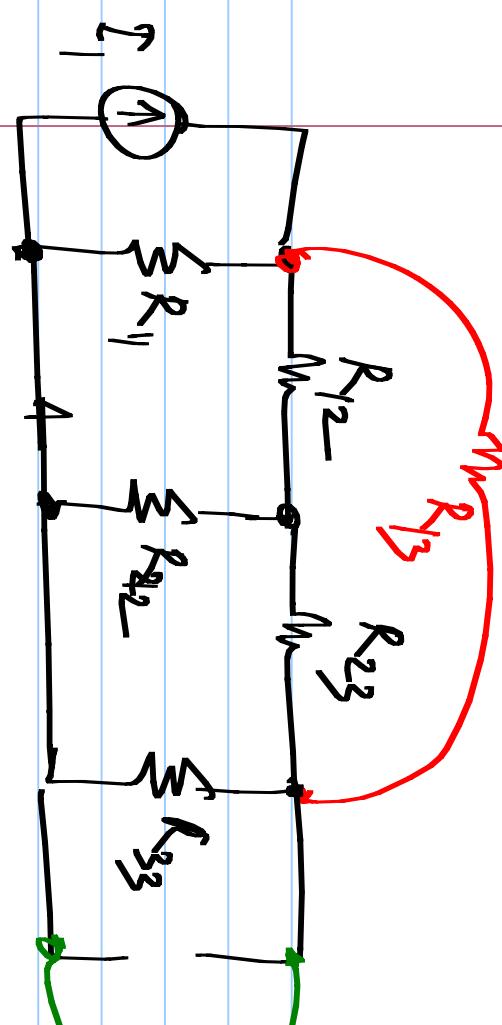
Resistances & ind. current sources



*pari. tay*

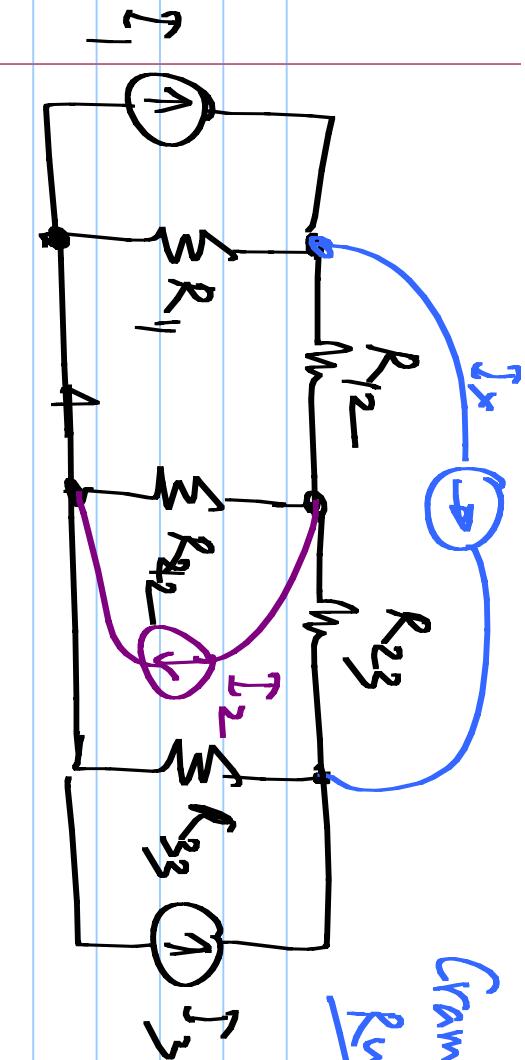


$$\begin{aligned}
 & \left. \begin{aligned}
 & -\epsilon_{13} \\
 & -\epsilon_{12} \\
 & -\epsilon_{23} \\
 & \epsilon_{13} + \epsilon_{23} + \epsilon_{33}
 \end{aligned} \right\} = 0 \\
 & \left. \begin{aligned}
 & \epsilon_{11} + \epsilon_{12} + \epsilon_{13} - \epsilon_{12} \\
 & \epsilon_{12} + \epsilon_{22} + \epsilon_{23} - \epsilon_{23} \\
 & -\epsilon_{13} + \epsilon_{23} + \epsilon_{33}
 \end{aligned} \right\} = 0
 \end{aligned}$$



$$\begin{bmatrix} I_1 + I_{12} & -I_{12} \\ -I_{12} & I_{12} + I_{22} + I_{23} \end{bmatrix} = \begin{bmatrix} I_1 \\ I_2 \end{bmatrix}$$

$$= \begin{bmatrix} I_1 \\ I_2 \\ I_3 + I_X \end{bmatrix}$$



Cramer's Rule

$$I_1 = \frac{\begin{vmatrix} I_1 & -I_{12} & 0 \\ 0 & I_{12} + I_{22} + I_{23} & -I_{23} \\ I_3 & -I_{23} & I_{23} + I_{33} \end{vmatrix}}{\begin{vmatrix} I_1 & I_2 & I_3 \\ I_2 & I_3 & I_1 \\ I_3 & I_1 & I_2 \end{vmatrix}}$$

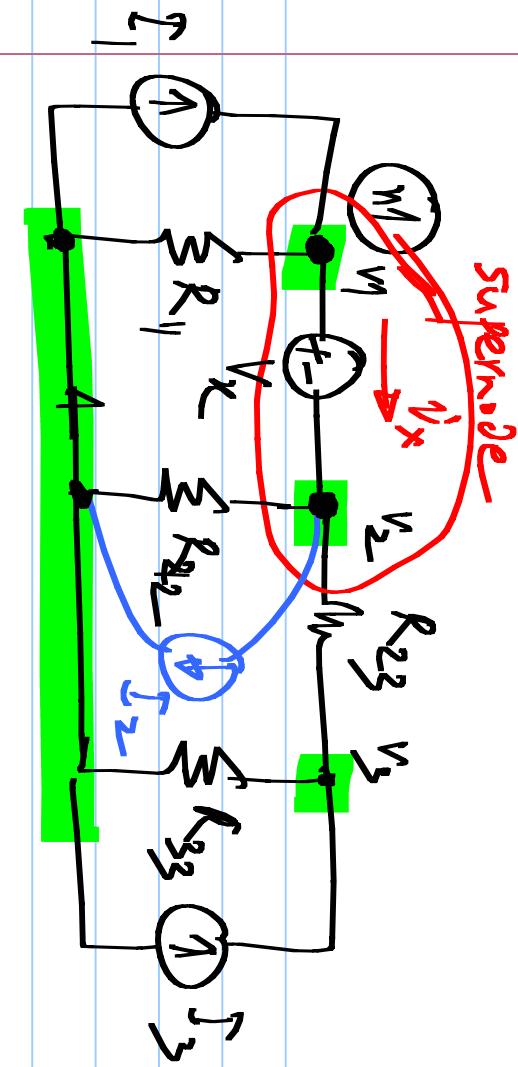
■

Superwirk  
(1,2)

$$V_1 \cdot g_{11} + V_2 (g_{22} + g_{23}) - V_3 \cdot g_{23} = -I_2$$

$$\text{Node 1} \quad V_1 \cdot g_{11} + i_x = I_1$$

$$\text{Node 2} \quad V_2 (g_{22} + g_{23}) - V_3 \cdot g_{23} - i_x = 0$$

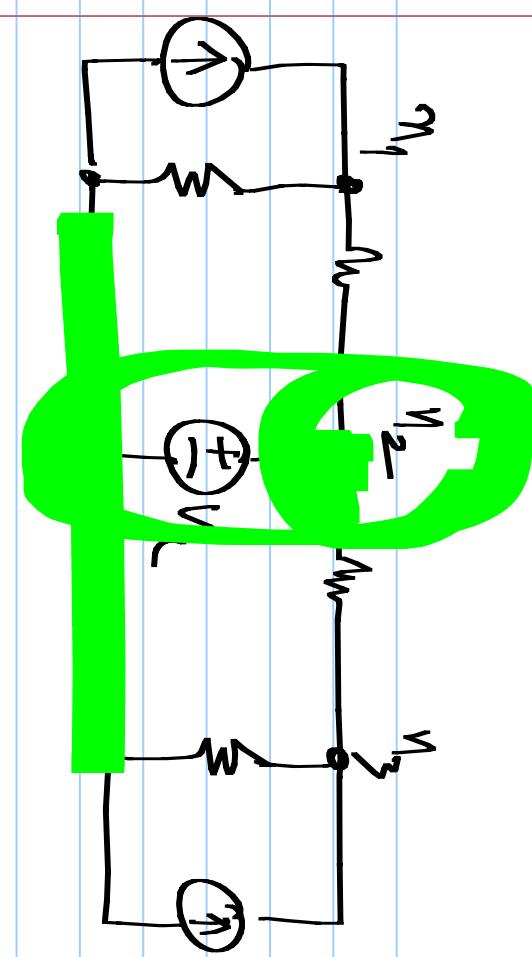


$$\frac{V_1 - V_2}{R_{21}} = i_x$$

■

## Nodal analysis:

- \* Circuits w/ conductances & ind. current sources
- \* Symmetric  $[G]$  matrix.
- \* Circuits w/ conductances, ind. current & ind. voltage sources
  - Combine nodes connected to voltage source into a supernode - single KCL eq. for it
  - Voltage source constraint



$$V_2 = V_1$$

$$\begin{bmatrix}
 0 \\
 -\tilde{g}_{12} \\
 -\tilde{g}_{23} \\
 \tilde{g}_{23} + \tilde{g}_{33} \\
 \tilde{g}_{12} + \tilde{g}_{22} + \tilde{g}_{23} \\
 -\tilde{g}_{12} + \tilde{g}_{33} \\
 \tilde{g}_{12} + \tilde{g}_{12} \\
 -\tilde{g}_{12} + \tilde{g}_{33}
 \end{bmatrix}
 = 
 \begin{bmatrix}
 \Sigma_1 \\
 \Sigma_2 \\
 \Sigma_3
 \end{bmatrix}$$

