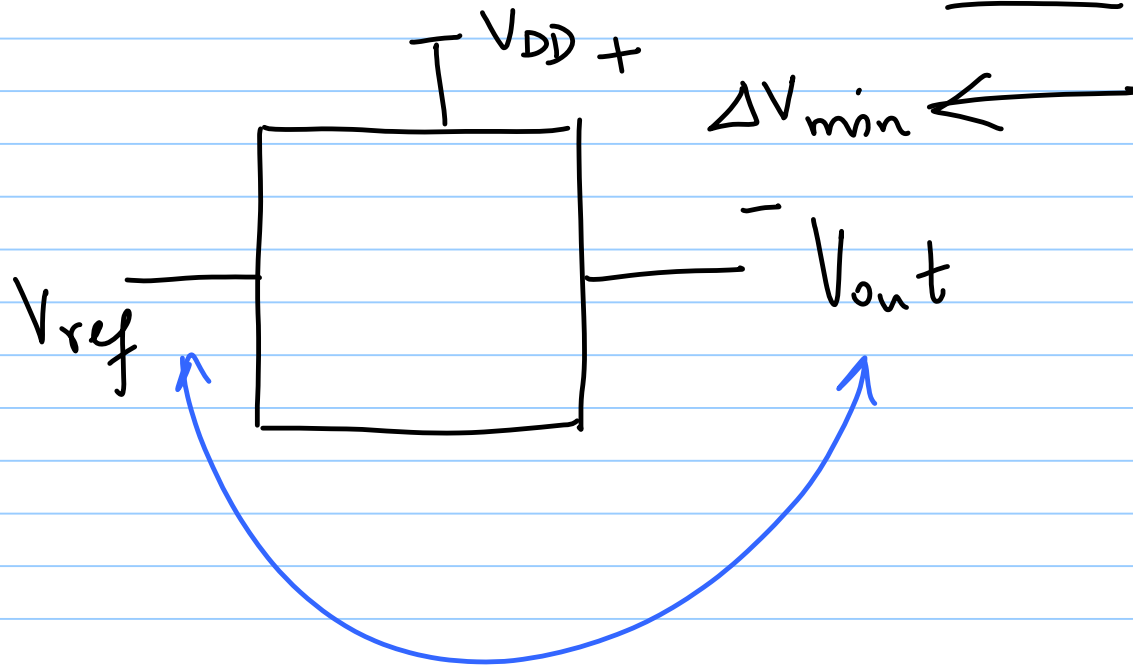
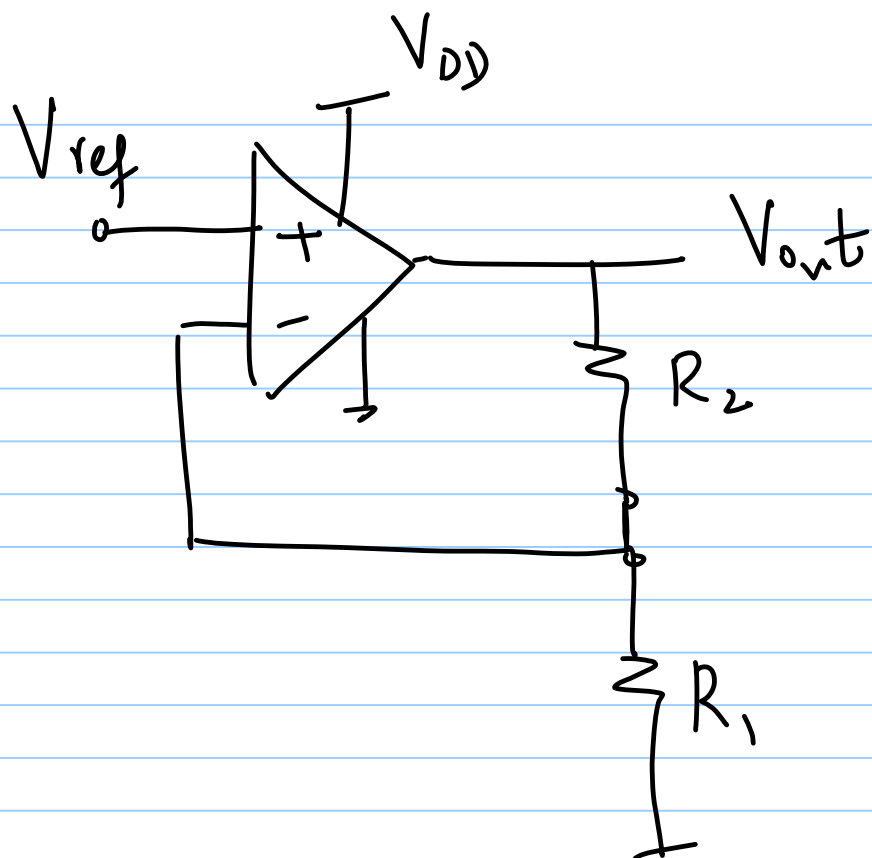


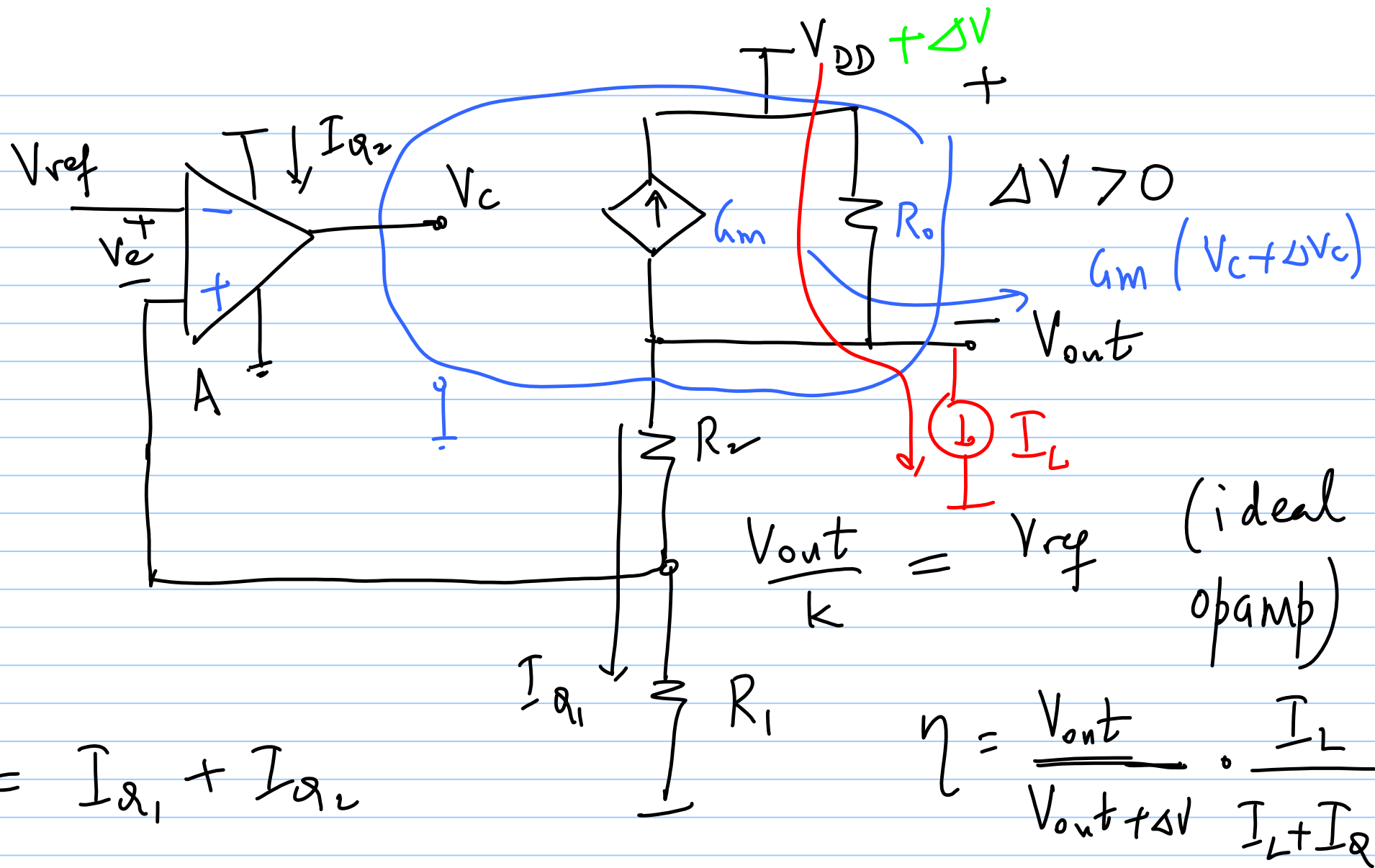
13/03/2019

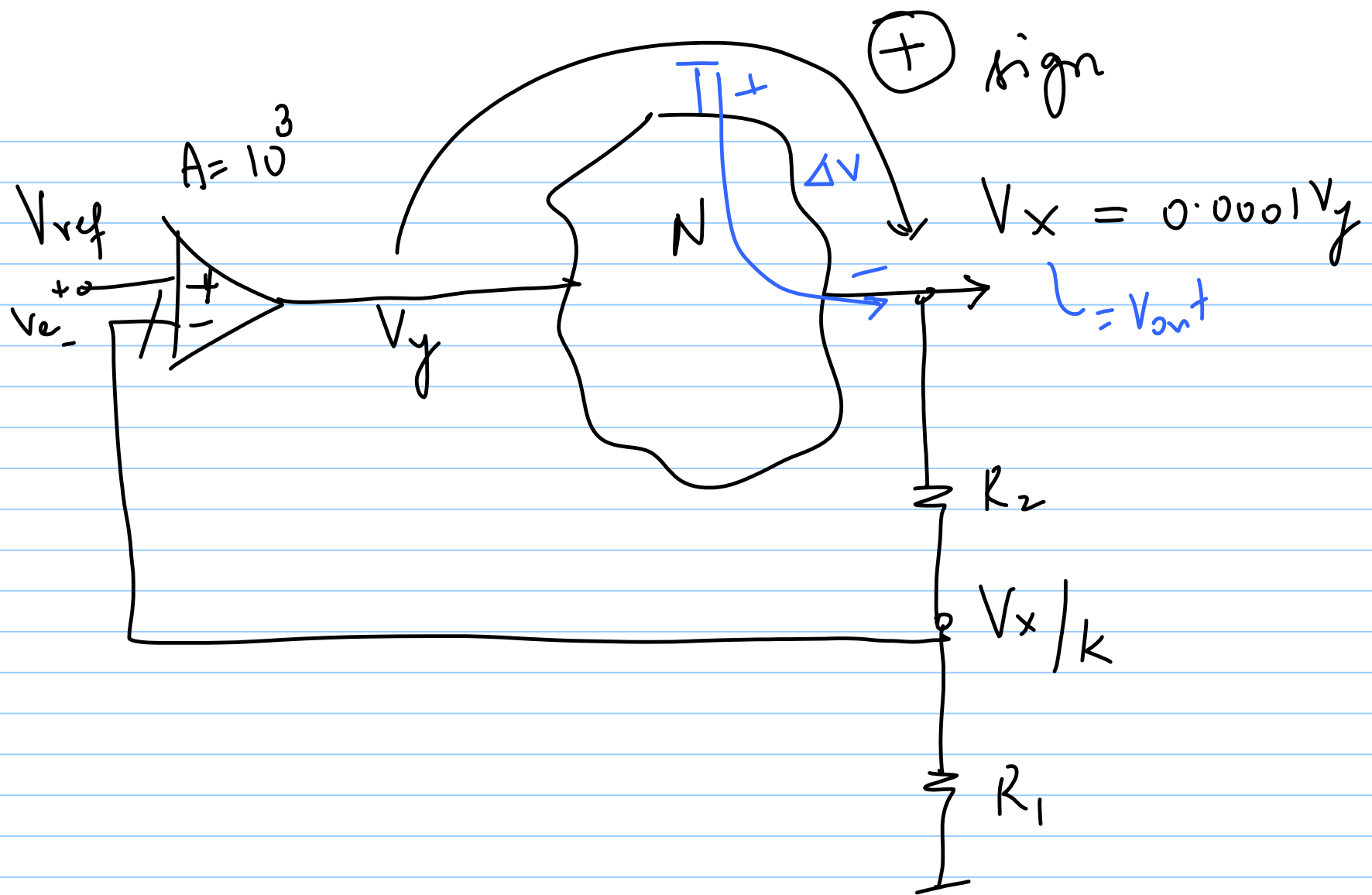
Lecture 26

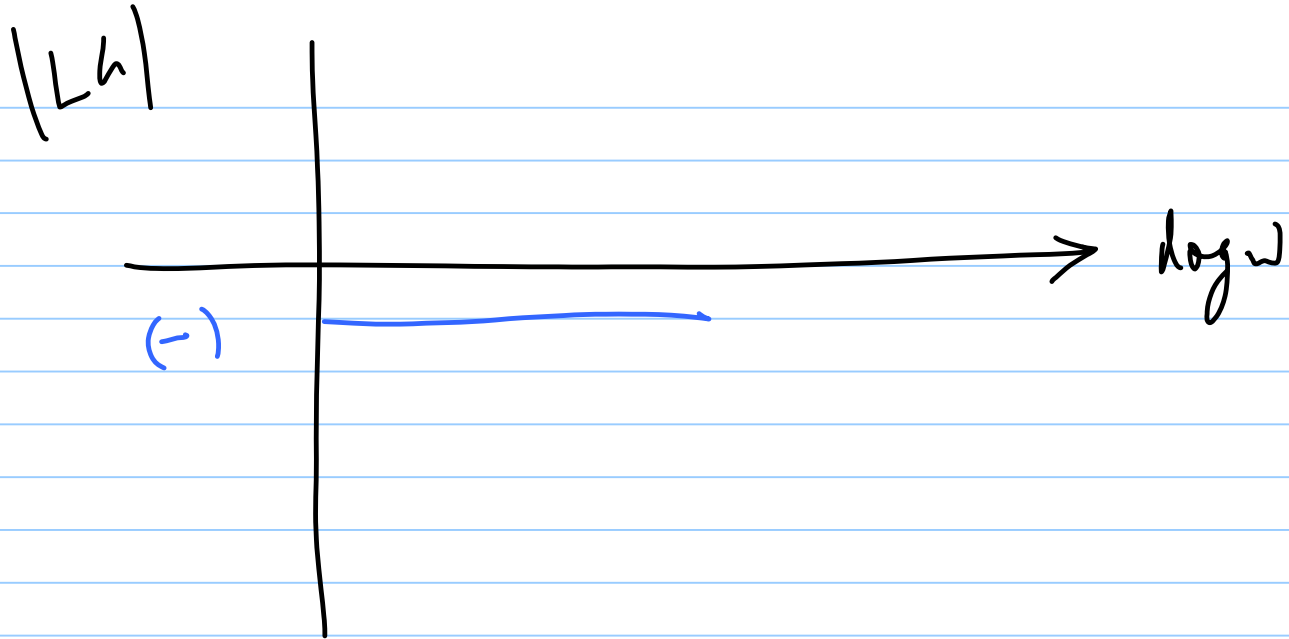
LDO



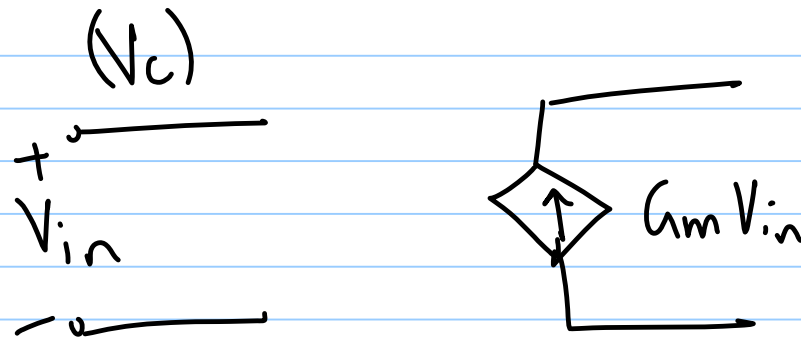








$$CL_h = k \cdot \frac{L_h(s)}{1 + L_h(s)}$$



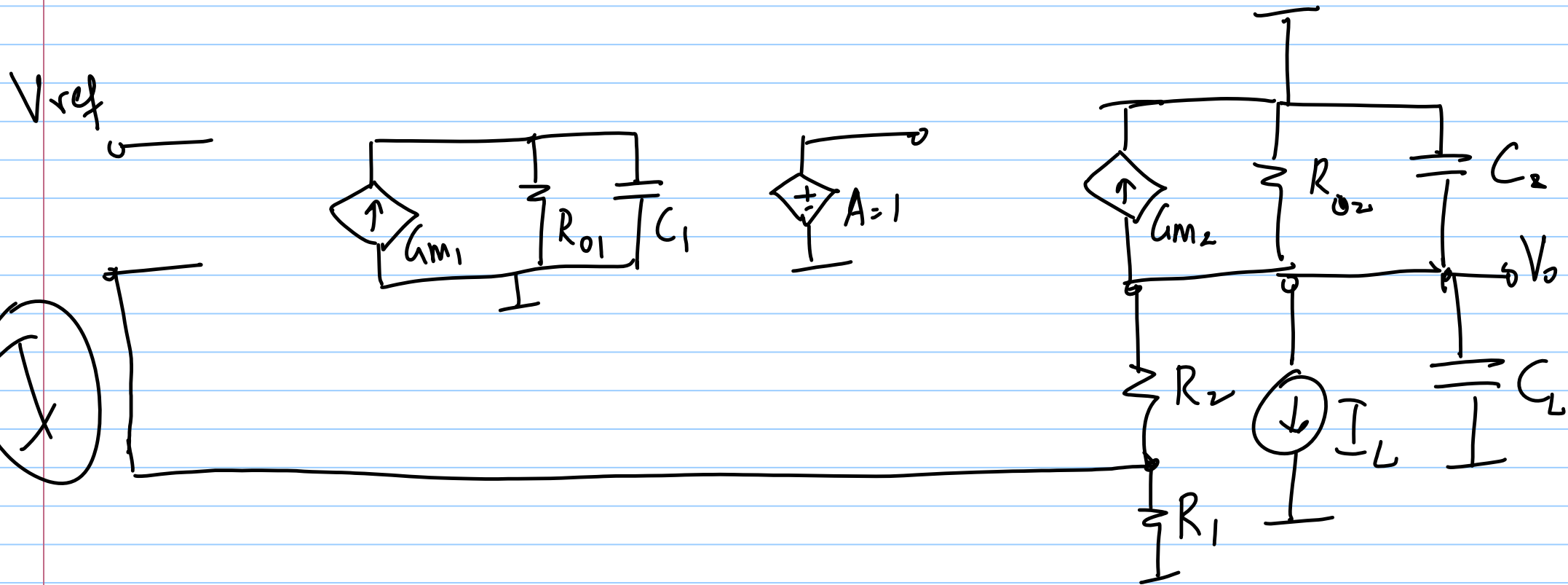
R_{out} of LDO $\rightarrow$ HW

$$R_{out} = \frac{\Delta V_{out}}{\Delta I_L} = \text{Load Regulation of the LDO}$$

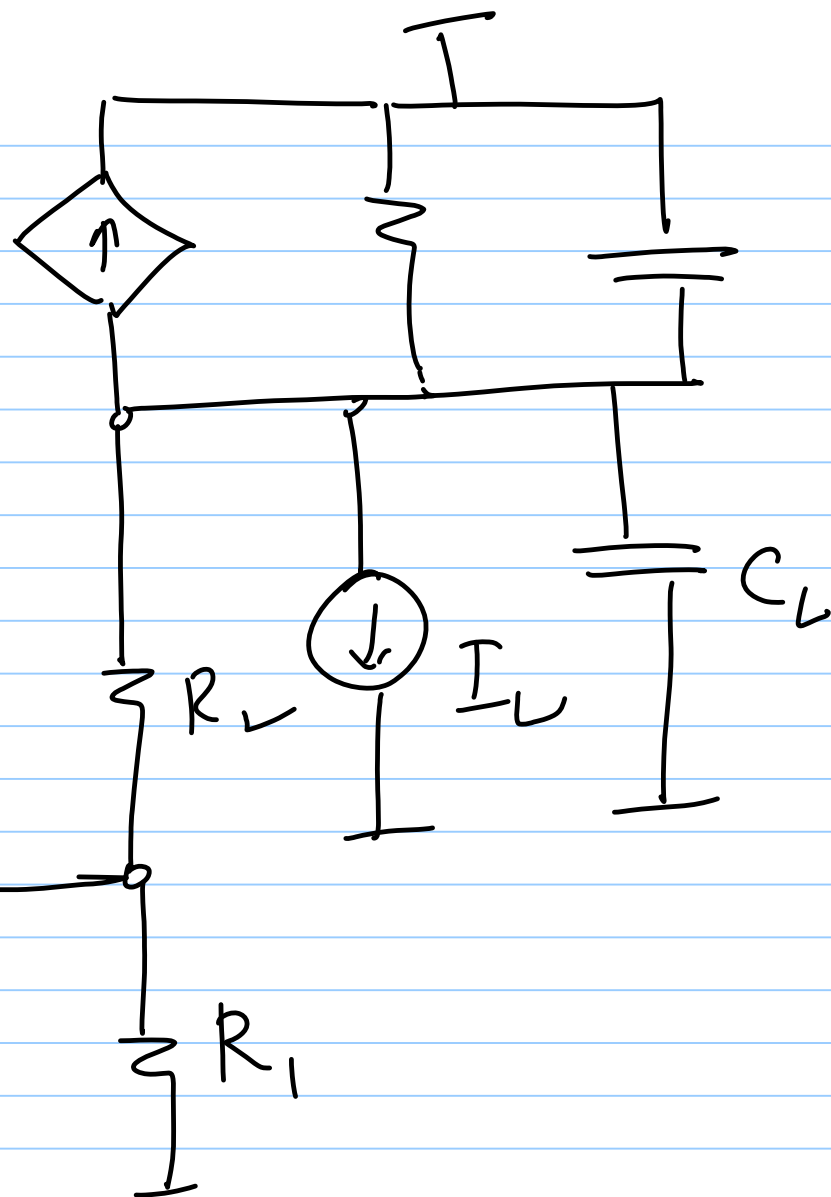
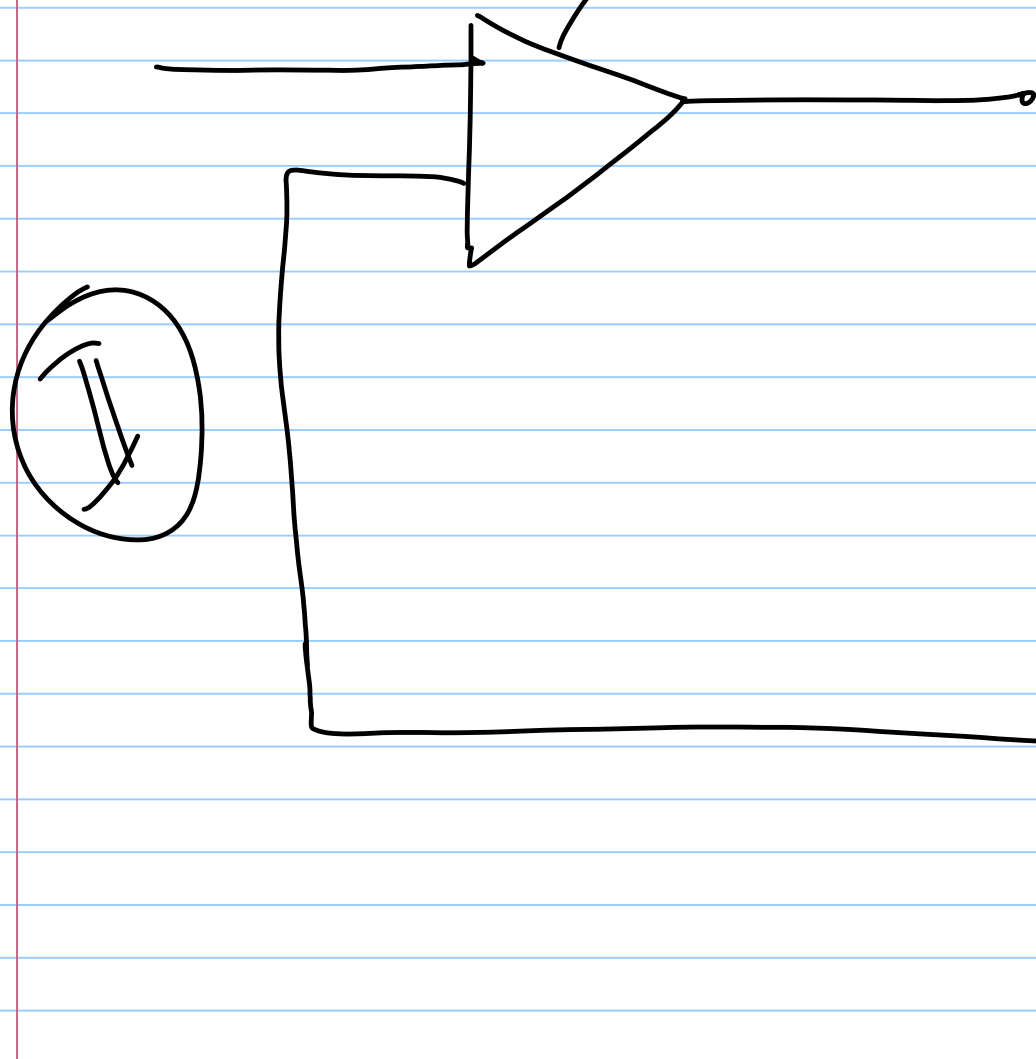
$$\frac{\Delta V_{out}}{\Delta V_{DD}} = \text{Line Regulation of LDO}$$

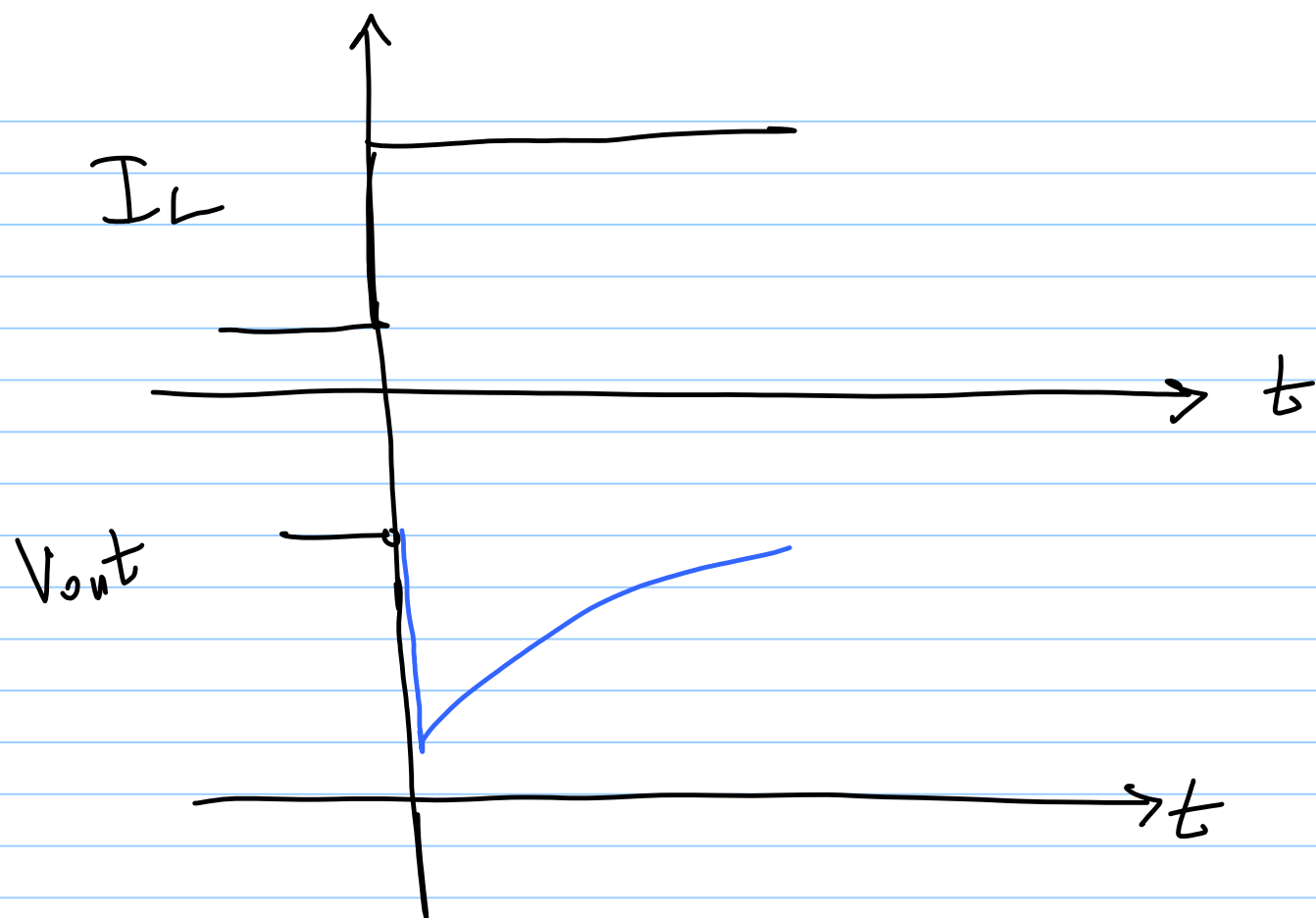
$$PSRR = \frac{\Delta V_{out} / s}{\Delta V_{DD} (s)}$$

Power supply rejection
Ratio



2-stage opamp





line transient