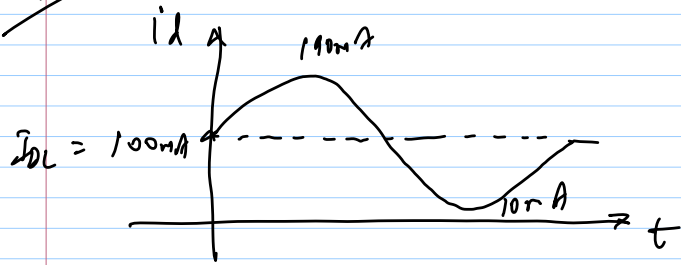
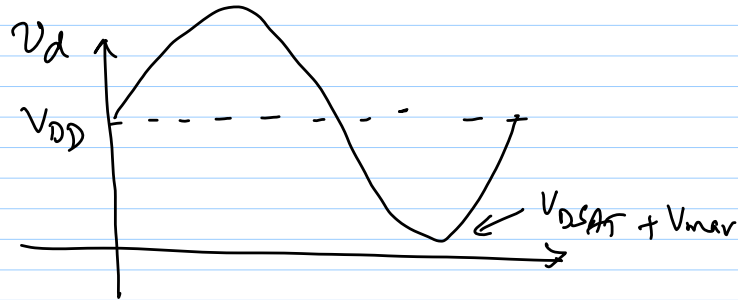


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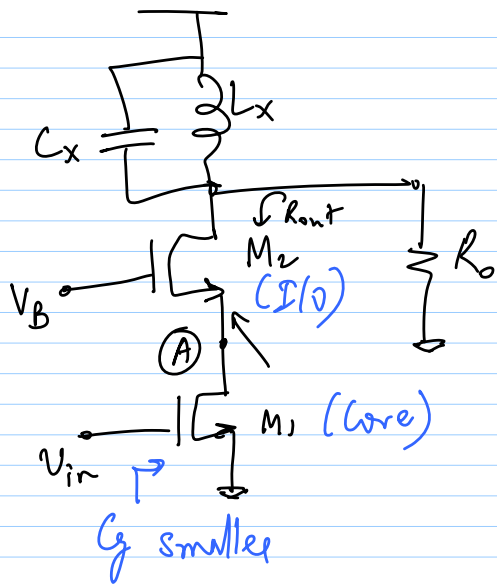


→ g_m is non-linear



- 1) gain ←
- 2) linearity
- 3) η
- 4) noise (OGB)
- 5) Rout

- * Device W 's are in several μm
- * $L > L_{min}$ may give better linearity but larger C_g & C_d
- * r_{ds} of device is low due to large $W \Rightarrow$ Cascode possibility



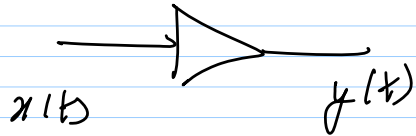
- Swing is limited
- C_{par} @ (A)
- + V_A swing is small \Rightarrow v_{d1} is small (better linearity)
- + Stability
- + R_{out} is larger
- + Breakdown Voltage stresses are lower

Core devices — 90nm, 1.2V

→ I/O — 250nm, 2.5V

- * V_{DSAT2} — choose equal to V_{DSAT1}
- * $L = L_{min2}$, $W = W_{DSAT2}$, I_{DC}
- * V_B — choose such that
 - a) V_A is large enough for linearity
 - b) M_2 does not go into triode

Linearity



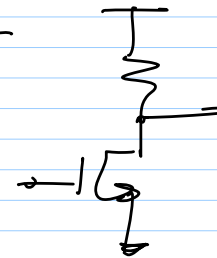
$$x(t) = a(t) \cos(\omega_0 t + \phi(t))$$

$$y(t) = A(t) \cos(\omega_0 t + \phi(t) + \theta(t))$$

If PA BW \gg signal BW:

$$y(t) \approx \underbrace{A(a(t))}_{\text{AM-AM char.}} \cos\left(\omega_0 t + \phi(t) + \underbrace{\theta(a(t))}_{\text{AM-PM char.}}\right)$$

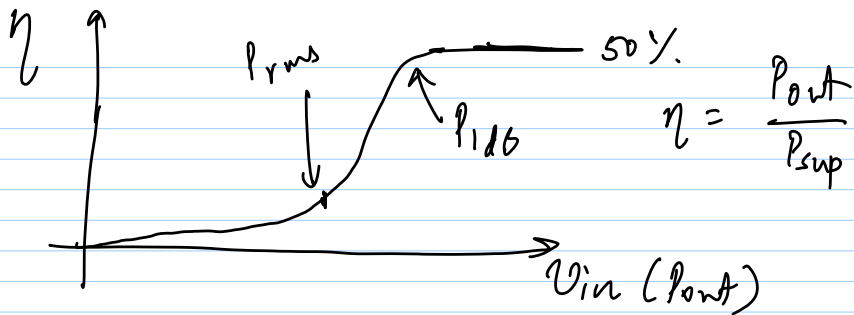
$$\frac{\eta}{\text{class A}}$$



$$\eta_{\max} = 25\%$$



$$\eta_{\max} = 50\%$$



PAE - power added efficiency
when gain is not large

$$\eta_{\text{PAE}} = \frac{P_{\text{out}}}{P_{\text{in}} + P_{\text{sup}}}$$