



SYSTEMS LAB WORKSHOP

In collaboration with EE & CSE departments of IIT-Madras

India Systems Development Lab (ISDL) of IBM is pleased to invite faculty and students of IIT-Madras for a workshop on latest trends in high performance IBM Microprocessors and exciting current research focus areas, challenges in EDA/VLSI Digital Design & Verification, and Analog IC design. Advantages of OpenPOWER platform for academic research would also be illustrated. Overviews of research work in IIT-M departments (EE & CSE) by distinguished professors.

Agenda

Technical talks - Focus on challenges and problems in

- Electronic Design Automation (EDA) for IBM Microprocessor Designs
- Distributed Graph based **Algorithms** and **Analytics** in EDA
- Next generation **Formal Verification**
- CAPI - Reconfigurable **HW Acceleration** & Applications for Machine Learning
- **Analog IC design** in high-end Microprocessor Designs
- **OpenPower** Platform
- **IBM Academic Initiatives**
- **IIT-M Research Overview**

We encourage you to take this opportunity to interact face-to-face with senior technical experts in these domains and explore collaborative research areas and internships

DATE: 3rd OCT, 2016
(MON)

TIMINGS:
9 AM TO 4.30 PM

VENUE: ESB 244

WHO CAN ATTEND:
OPEN TO ALL
EE & CSE DEPT

VISIT:
ONTHEHUB.COM/IBM/

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