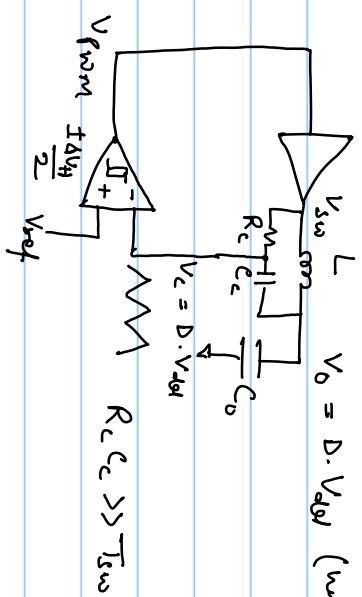


Current Mode Hysteresis converter

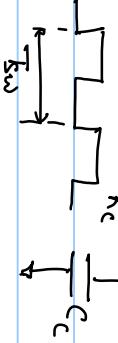
$$V_o = D \cdot V_{dc} \quad (\text{under no load condition})$$

or
R_{dc} \rightarrow 0



$$I_R = \frac{V_{dc}}{R_c} \quad V_c = D \cdot V_{dc}$$

$$\frac{V_{dc}}{R_c} = \frac{1}{C_c} \int I_R dt$$



Choosing current ($R_c C_c > T_{sw}$) during T_{on} .

$$I_R = \frac{V_{dc} - V_c}{R_c}$$

$$I_R = C_c \frac{dV}{dt}$$

$$dV = \Delta V_H$$

$$dt = D \cdot T_{SW}$$

$$\Rightarrow \frac{V_{dd} - D \cdot V_{dd}}{R_c} = C_c \frac{\Delta V_H}{D \cdot T_{SW}}$$

$$\left[\frac{1}{T_{SW}} = \frac{V_{dd}(1-D) \cdot D}{R_c C_c \Delta V_H} = F_{SW} \right]$$

$$\Delta I_L = \frac{V_{dd} (1-D) D}{L} T_{SW}$$

$$F_{SW} = \frac{V_{dd} (1-D) \cdot D}{L \Delta I_L}$$

$$\Delta I_L \rightarrow \Delta V_H$$

$$L \rightarrow R_c C_c$$

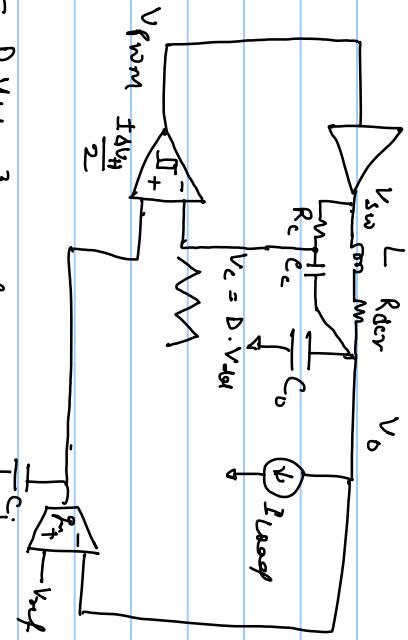
In voltage mode hypothesis

$$F_{SW} = \frac{V_{dd} (1-D) D}{L \Delta V_H} \times R_{ext}$$

Voltage mode hysteresis

Current mode hysteresis

- ① Larger R_{SEN} \rightarrow larger output ripple
- ② Frequency is depending on C & R_{SEN}
- ③ ΔV_H is small $\rightarrow V_{FROM}$ is sensitive to noise
 - ④ ΔV_H could be large so less sensitive to noise



$$V_o = D \cdot V_{dd} - I_{LOAD} \cdot R_{SEN}$$

$$V_L = D \cdot V_{dd}$$

Since feedback is from V_1 not V_2

so there will be an error in V_0

Verner = Head · Rcdy

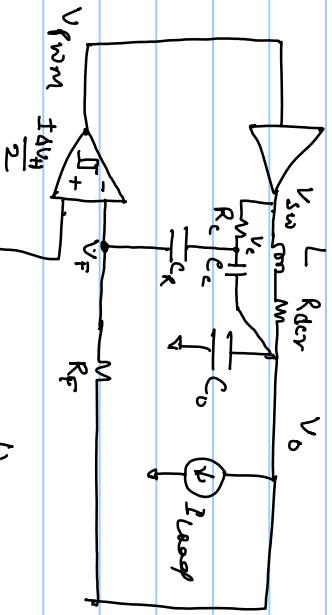
In order to correct this error, Verner should be asked to limit!

zurück + ferner → then

$$\text{Verror} = V_e - V_0$$



Present is shown because no direct feedback from V_c to the computer.



$V_{pwm} + \frac{\Delta V_p}{2}$

Primary Voltage

D. Vdd - Ilead · Rddr
Current Mode

V_c
~~ΔΔΔΔ~~ - D. Vdd

V_F
~~ΔΔΔΔ~~ - V_0

controlling switching Frequency

current mode

$$F_{sw} = \frac{V_{dd}(1-D) \cdot D}{R_{sense} \Delta V_H}$$

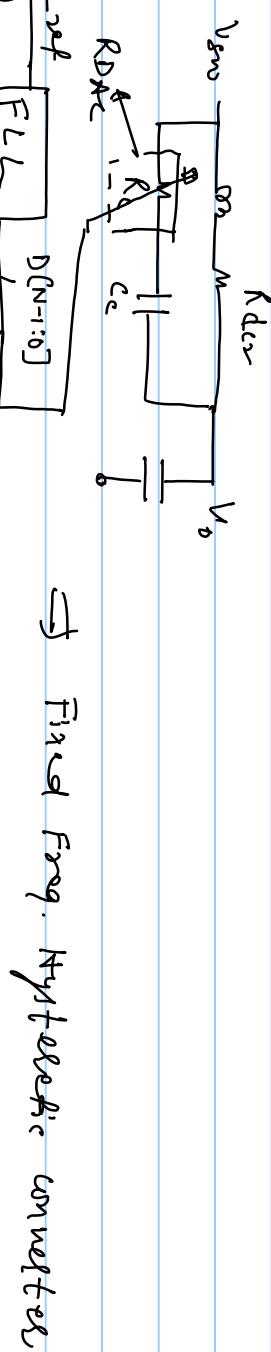
Fix ΔV_H

and F_{sw} can be easily controlled
by R_e or C_c
↓
Preferred.

very difficult to control F_{sw} due to small ΔV_H

voltage mode

$$F_{sw} = \frac{V_{dd}(1-D)D}{L \Delta V_H} \times R_{sense}$$



⇒ Fixed Freq. Hysteresis converter

V_{fwd}
(F_{sw})

freq. locking loop → forward $F_{sw} = F_{ref}$