

Switching Regulators

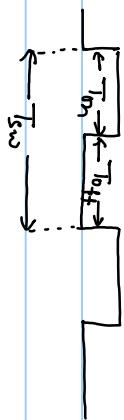
- works on the principle of Pulse Width Modulation (PWM)
- offers higher efficiency across wide V_{out}/V_{in} range
- can buck (step down), boost (step-up) or invert input power supply



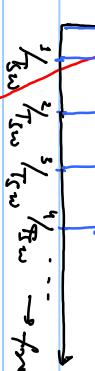
- $V_{out} < V_{in}$
- $V_{out} > V_{in}$

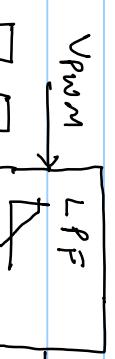
→ $V_{out} < 0$ (negative or inverting)

any node.



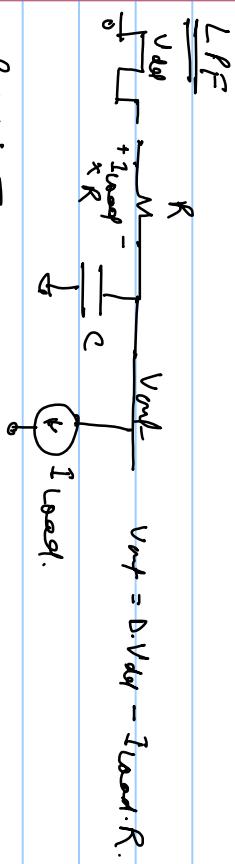
If 3rd cutoff of LPF , $\omega_c \ll \frac{1}{T_{sw}}$
then all harmonics are filtered out and we get only dc component at V_{out} .





de weise weg
small ripples.

$$V_{out} = \frac{T_m}{T_{ew}} \times V_{dcl} = D \cdot V_{dcl}$$



$$R_C >> T_{ew}$$

$$T_{ew} = 1 \text{ ms}$$

R should be much smaller to supply higher load current

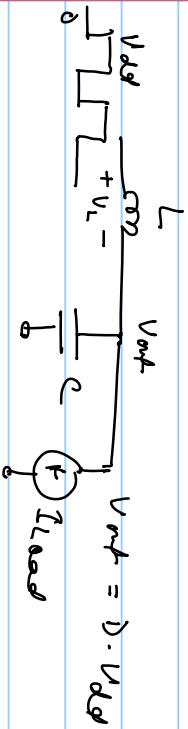
$$R = 1 \text{ m}\Omega \text{ or } 1\Omega$$

$$I_{load} = 1 \text{ A}$$

$$Rc = 10 \times T_{sw}$$

$$C = \frac{10 \times 1\mu}{0.1} = 100\mu F$$

RC filter is not practical due to large C
so we use LC filter



ideally C is shorted

$$V_L = L \frac{dI_L}{dt}$$

$$dI_L = \frac{1}{L} V_L dt$$

$$\Delta I_L = \frac{1}{L} V_L dt$$

during T_{on}

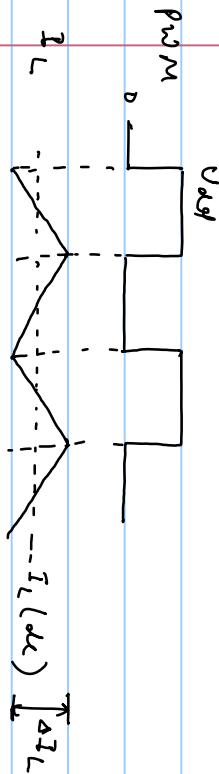
$$\Delta I_L = \frac{V_{dd} - V_{out}}{L} \times T_{on} \quad (\text{slope} = \frac{V_{dd} - V_{out}}{L})$$

$$\Delta I_L = \frac{V_{dd} (1-D) D}{L} \times T_{dw}$$

during off time

$$\Delta I_L = -\frac{V_{out}}{L} T_{off}$$

$$= -\frac{V_{out}}{L} (1-D) T_{dw}$$

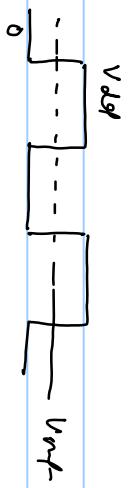


$$I_L = I_L(\text{dc}) + \Delta I_L$$

$$I_L(\text{dc}) = I_{L\text{load}}$$

Volt-second balance

In steady state average voltage drop across
the inductor = 0



Voltage drop across the inductor during ON-time.

$$V_{L(ON)} = V_{dd} - V_{out} \quad \text{--- (1)}$$

Voltage drop across the inductor during OFF-time

$$V_{L(OFF)} = -V_{out}$$

Average Voltage across the inductor.

$$= \frac{\text{Area of one period}}{T_{sw}} = \frac{(V_{dd} - V_{out})T_{on} + (-V_{out})T_{off}}{T_{sw}} = 0$$

$$(V_{dcl} - V_{out}) \cdot T_{on} = V_{out} \cdot T_{off}$$

Volt-second balance means
product of Voltage and time during T_{on}
should be same as product of Voltage and time
during T_{off} .

$$V_{out} T_{on} = V_{out} (T_{on} + T_{off})$$

$$V_{out} = \frac{T_{on}}{T_{sw}} \cdot V_{dcl} = D \cdot V_{dcl}$$

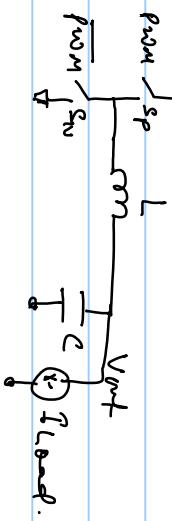
Buck or step-down converter

because $D \leq 1$

Buck converter

Buck power stage

→ switches + LC filter.



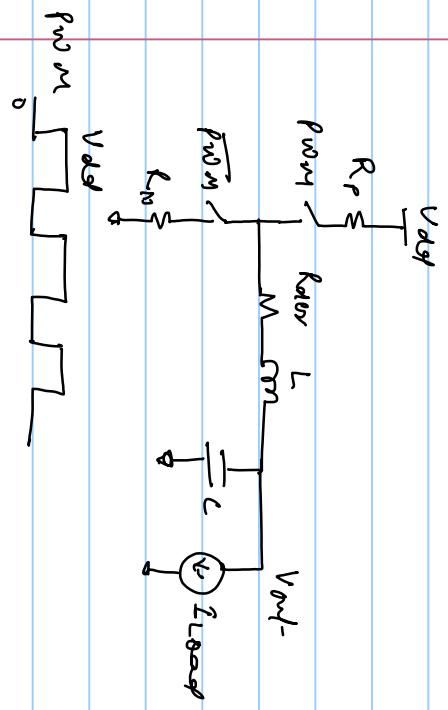
Ideally switch s_p & s_n and L also lossless.

But in reality there is some finite resistance.

$$s_p \rightarrow \frac{i_{R_p}}{i_m} +$$

$$s_n \rightarrow \frac{i_{R_n}}{i_m} -$$

$$L \rightarrow i_{R_L} L$$



Power

V_{out}

I_{load}

R_p

L

M

N

C

D

V_{pv}

R_{pv}

I_{pv}

V_{nv}

R_{nv}

I_{nv}

V_{out}

I_{load}

V_{in}

R_p

L

M

N

C

D

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R_{pv}

I_{pv}

V_{nv}

R_{nv}

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V_{out}

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In order to maintain V_{out} , D must be increased to compensate for V_{loss} .

$$V_{out} = D' V_{del}$$

$$D' = D + \Delta D$$

$$D V_{del} = \underline{D' V_{del}} - V_{loss}.$$

$$D V_{del} = D V_{del} + \Delta D V_{del} - V_{loss}$$

$$\Delta D V_{del} = V_{loss} \Rightarrow \boxed{\Delta D = \frac{V_{loss}}{V_{del}}}$$