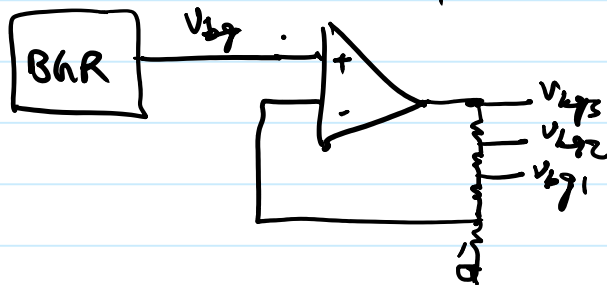
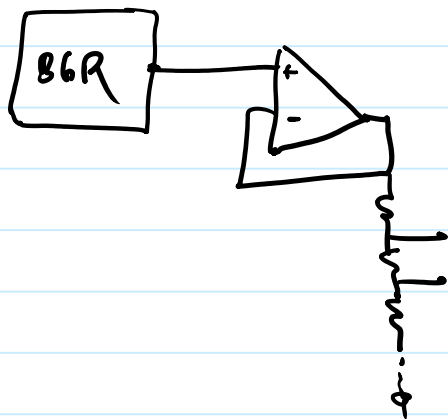
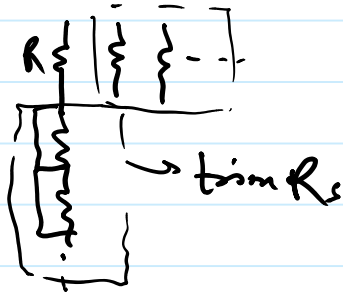
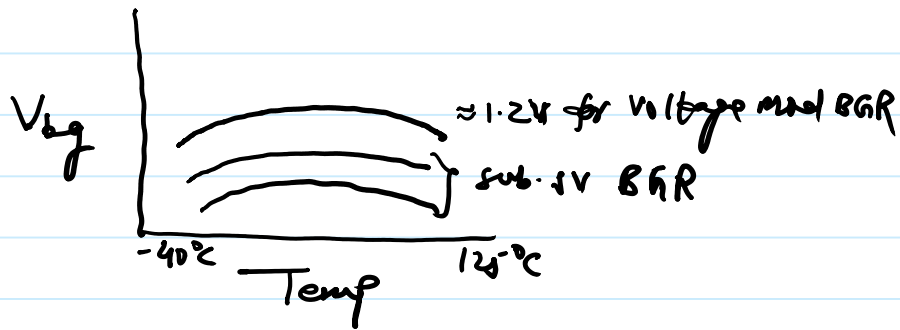
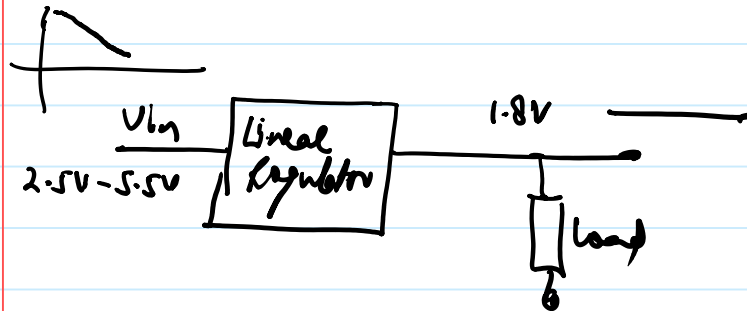


Bandgap Reference

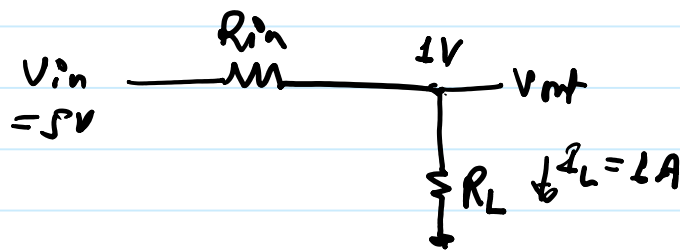


Linear Regulators

Continuous time circuit which regulates output voltage.



Basic Concept

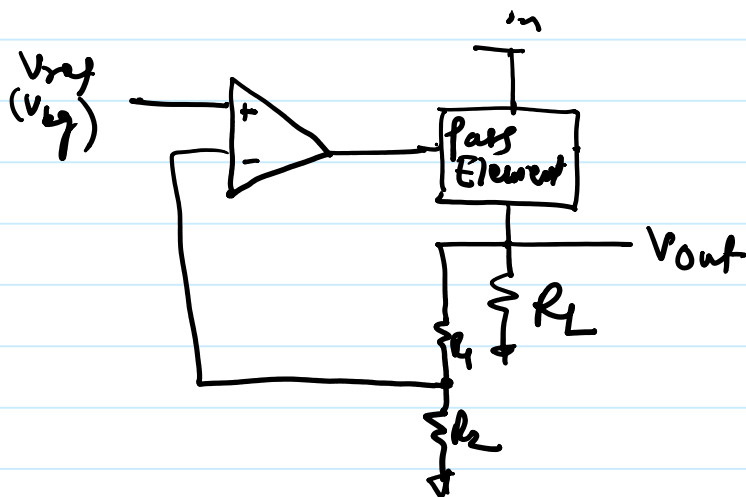
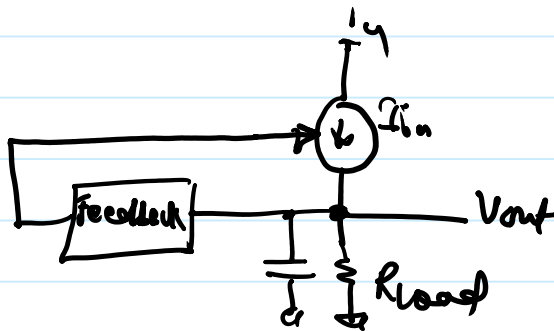
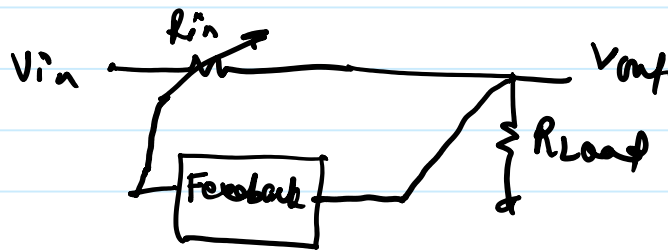


$$V_{out} = 4V$$

$$R_{in} = 1\Omega$$

for fixed output and varying V_{in} & I_{load}
 R_{in} must be changed.

Controlling Input Resistance and Current



$$V_{out} = \left(1 + \frac{R_1}{R_2}\right) V_{ref}$$

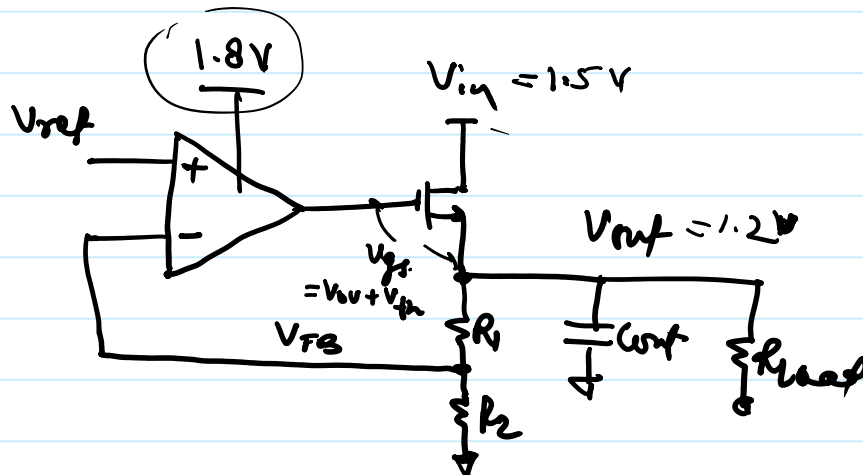
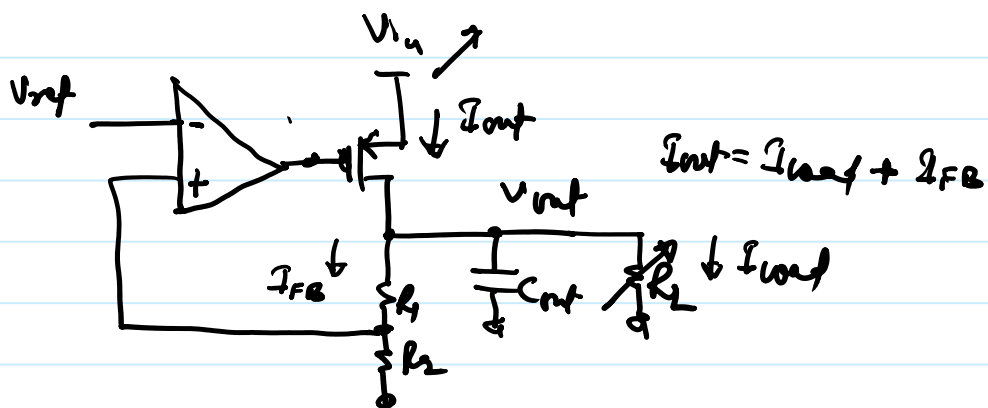
Implementing Pass Element

PMOS

NMOS

PNP

NPN



Low Drop-Out (LDO) Regulator

LDO is linear regulator with low drop out voltage

$$V_{drop} = V_{in} - V_{out}$$

LDOs $\rightarrow V_{drop} < 10\%$ of V_{in}

$$\eta = \frac{P_{out}}{P_{in}} = \frac{V_{out}}{V_{in}} = \frac{V_{in} - V_{drop}}{V_{in}}$$

$$= 1 - \frac{V_{drop}}{V_{in}}$$